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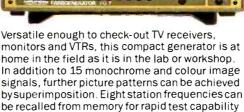
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Forth in control

He explains how.

by Peter Ferris

68000 board - 5

by R.F. Coates

some users

by Tom Ivall

by David N. Sands

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argues D.N. Sands, particularly

Timing by remote control

This versatile self-contained Z80 timer controls eight appliances with

100 on-off settings using coded r.f.

Guided tour of the Kaybug monitor

D.b.s. — a plan in search of 75

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conferences. Tom Ivall explains.

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## Short-wave loop aerial

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 3Hz-300kHz. 5 ranges, acc 2% + 0.1Hz up to 100kHz, 3% at 300kHz Sine or square <200µV to 2.5Vms. Distn.</td>

 <0 2% 50Hz-50kHz DM has an output meter.</td>

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 $\label{eq:235/265} \begin{array}{r} f235/265\\ 16 \mbox{ LF ranges as TM3A/B} + 8 \mbox{ HF ranges 1mVfs/3Vfs},\\ accuracy \mbox{ 4\%} + 1\% fs \mbox{ at 30MHz}. \mbox{ \pm 3dB 300kHz} \mbox{ 400MHz}. \end{array}$ 

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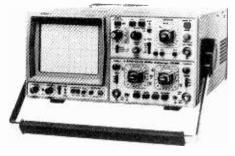
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CIRCLE 52 FOR FURTHER DETAILS.

# The price of entertainment

Possibly one or two well-heeled readers may have received satellite dishes in their Christmas stockings. If so, they will probably be too busy watching Mirrorvision, Teleclub, RAIuno and the rest to worry much about the future of tv: no doubt they think they already have it.

The mushrooming satellite tv industry is installing small 12GHz terminals as fast as it can get them and already there are many in private hands. But even though the high-street rental chains can now offer you a system on monthly instalments, satellite tv is likely to stay very much a rich man's toy.

Of course, true d.b.s. television for all has yet to arrive in Europe. But when it does, it is hard to believe that the price of terminals will fall to the levels which the public is willing to pay for conventional aerial installations (which is to say, pratically nothing).

And now the prospect of still greater expense is on the horizon for satellite tvwatchers, with the introduction of high-definition tv. Satellite transmission gives the perfect opportunity to introduce new technical standards which can by-pass the shortcomings of our present system. In Japan, an 1125-line, 60-field standard, devised by Sony and adopted by the Japanese broadcasting corporation NHK has already been chosen for the d.b.s. service to come into operation there in 1988.

The CCIR has so far shown no taste for this system. But there will be pressure from other 60Hz countries, notably the US, to adopt it as a de-facto world standard.

This pressure ought to be resisted. For all its qualities and the pictures and sound it gives are all that could be desired — the Sony system is quite unsuitable for a public service.

For one thing, it breaches the cardinal principle of technical innovation in broadcasting, which is that you launch a new development only when you have found a way of making it compatible with existing equipment. This principle made possible stereo sound, colour tv, teletext and many other improvements of which the casual viewer is unaware.

Certainly there has to be

major change sometimes. In Britain, the last occasion came with the introduction of u.h.f. tv and the closure of the v.h.f. stations. Though costly for the broadcasters, it went virtually unnoticed by the public.

But high-definition tv is a different matter all round. At a stroke, the Sony system makes *all* existing equipment obsolete, both in the studio and the viewer's home. This would not necessarily matter but for the enormous cost of its replacement.

The audience for the new service will have to find simultaneously the price of a satellite terminal (the wrong side of £1000 at current prices), the cost of a special tv set, plus a contribution, somehow or other, to the huge expense of making the programmes. The special tv set is perhaps the most daunting part. Imagine the cost of a super-high resolution c.r.t. or projection tv with a 5:3 aspect ratio, plus the special processor needed to recover the compressed video signal!

Costs at the transmission end are just as dizzying. Camera, telecines, v.t.rs, production

desks, distribution equipment,

all will have to be thrown out. And simultaneous transmission of the new programmes on existing channels will be possible only via complex standards converters (at a quarter-of-a-million pounds a time, according to one source). We can expect few viewers or broadcasters to show much enthusiasm.

Surely a much better solution is that proposed in Britain by the IBA and outlined elsewhere in this issue. The IBA has charted a coherent hierarchy of improvements to the existing standard, a ladder which viewers and broadcasters could ascend each at their own pace.

The biggest step is still the first one, the adoption of C-mac in place of Pal. But with the move towards modular tv receivers and the development of special C-mac i.cs, there is no reason why it should add significantly to the viewer's costs. And the EBU has already declared its support for C-mac.

The goal at the top of the IBA's ladder is just the same, true high-definition tv. But reached this way, it could be affordable.



Steve Webb developed a system for receiving data from the two UOSAT satellites to keep his daughter Jenny, and his two other children, amused. The system, known as Astrid (see WW New Products Oct. 1985), is now being marketed worldwide and has found many enthusiastic users in schools, receiving news and data from space as well as pictures of the earth and cloud formations; useful for amateur weather forecasters.

# World's largest satellite network

Linking 93 locations in 52 countries for Explo 85, an international congress of 15 million Christians, BT International claim that this was the biggest link-up of its type ever. They have organized global satellite links before, for events such as the Olympics, the World Cup, and the recent Live Aid concert. For the congress BT booked broadcast quality tv channels on three Intelsat vehicles, two over the Atlantic and one over the Indian ocean. Similar channels have been booked on the European regional satellite and on domestic satellite systems over N. America and the Caribbean, Brazil and the Indian subcontinent. The major satellites transmit signals between continents; the signals were then retransmitted to local satellites for reception on locally-sited small-dish anntennae.

# **Enhanced C-mac**

In the wake of Sony's demonstrations of the 1125-line tv system chosen for the forthcoming d.b.s. service in Japan, Britain's Independent Broadcasting Authority has been showing the results of its own work on improved television standards.

'Evolution not revolution' is the slogan adopted by the IBA. For whilst the Sony-NHK

system (December issue, page 5) represents a major break with existing transmission systems, the IBA aims to reach a high-definition standard gradually. through a series of orderly enhancements to the present European standard. And in demonstrations staged at their London headquarters in December, IBA engineers showed a transitional highdefinition system based on modified 625-line equipment

and their C-mac transmission system.

Tom Robson, the IBA's director of engineering, while saluting the achievement of the Japanese engineers, stressed the drawbacks of their approach. "The 1125/60 system is not satisfactory to Europe and should not be accepted as a worldwide system", he said.

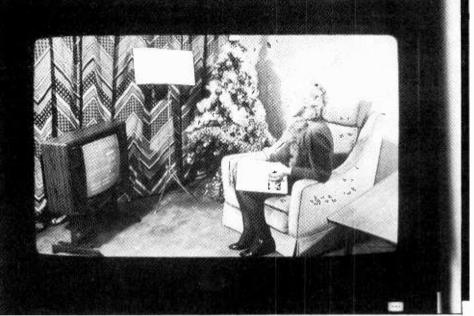
A major problem was the 60Hz field rate, since threequarters of the world used 50Hz. And although standards conversion was possible, good conversion was very expensive. According to another IBA engineer, Sony's own h.d.t.v. converter was rumoured to have cost over half a million pounds. And standards conversion could not cope with the 10Hz beat which arose when 60Hz equipment was used under 50Hz studio lights.

But one of the most serious objections, according to Mr Robson, was that direct change to any new system such as Sony's would mean making all existing equipment obsolete overnight. He said that broadcasters would find it difficult to finance such a changeover; a high-definition service would be an expensive luxury in its early days and revenues would take a long time to build up.

In the IBA demonstration, live pictures came from a Link Electronics 625-line camera modified to give the extra width were impressively realistic, though they still looked like television — in contrast to Sony's, which have the appearance of a superior kind of cinema picture. Even so, the film grain on 35mm telecine was noticeable at close quarters. Yet the option to enhance resolution later on by increasing the number of lines still remains open under the IBA's scheme.

For comparison, two conventional colour monitors showed the same pictures market, which seemed likely to adopt the NHK standard, appeared to see it mainly as a medium for electronic production for the cinema rather than as a broadcast system in its own right.

Describing the IBA system in more detail, Dr Gary Tonge outlined the changes which would take place, both at the studio and in the viewer's home. First, PAL pictures would give way to C-mac as d.b.s. transmissions were introduced.



This off-screen photograph shows the 5:3 aspect ratio of the IBA's system. When the camera moved closer, the Debussy arabesque on the music-stand could be read clearly enough to be played.

demanded by the 5:3 picture aspect ratio, and there were inserts from video recordings and telecine. All signals, with accompanying stereo sound, were coded in the enhanced Cmac system (by which the analogue luminance and colour signals are combined by timedivision multiplexing, with a separate time-slot given over to data and digital sound).

Display equipment included a fairly ordinary, though modified, American-made projection tv (which cost, as the engineers gleefully pointed out, about a tenth of the price of Sony's); and a Barco shadowmask monitor fitted with a Japanese tube and some special electronics, which gave a sequential scan of all 625 lines at a 50Hz repetition rate (twice the normal rate) simulating the linestructure of a 1250-line picture.

existing equipment obsolete The large-format pictures ELECTRONICS & WIRELESS WORLD FEBRUARY 1986 through an ordinary C-mac decoder and in PAL form after transcoding. And it could be seen that the double scan of the wide-screen monitor reduced field-rate flicker very effectively, disposing of one of the major advantages claimed for the 60Hz system.

Mr Robson said that the Japanese approach was based on 12-year-old technology and did not take account of recent developments in tv equipment design - such as the field-store in the wide-screen monitor. (And indeed the charge-coupled delay-line i.cs recently developed by Philips mean that field stores should be appearing in 625-line domestic sets very soon.) Yet it might take years for domestic receiver technology to match the quality which had been shown in h.d.t.v. demonstrations.

And he indicated that the US

MAC had been shown to fit well into a satellite channel, and C-mac decoders (which would use chips now at an advanced stage of development) would not add greatly to the cost of a receiving terminal. Then the system could be enhanced by stages to give a wider image, more lines displayed per field and reduced flicker.

So enhanced Cmac would reach the same targets as Sony's h.d.t.v., but by an evolutionary route rather than

direct. Viewers — and broadcasters — could pay for whatever level of quality they required. Dr Tonge added that the quality of the present demonstration system might be enough to satisfy many.

But in addition, enhanced Cmac could offer features such as high-quality stereo digital sound (up to eight mono channels were available), data transmission, and scrambling of subscription ty programmes.

Straightforward C-mac is already recommended by the European Broadcasting Union as the standard for d.b.s. television services and it has the support of major European electronics manufacturers, including Philips; though at present it is in active use by only one broadcaster, Norway. So how quickly could the enhanced version be put into practice? "If the IBA was given the go-ahead to appoint franchise-holders for a satellite channel, it would take four years to get the channel ready'', said Mr Robson. "We could be ready then.'

## Prize-winning aids for the disabled

Cedric the computer allows the user to select words, letters or symbols from a video screen by sensing the direction of eye movement. It was designed for use by those with high-level paralysis and loss of speech and was a winner of the IEE Prize for helping disabled people. The other joint winner was a device for bladder control for paraplegics.

The Cedric computer uses very low intensity red light to reflect off either eye and detects the point on a video screen to which the user is looking. Selection is accomplished by eye fixation on the desired word or control can contain more than 1,500

words and there is also a visual keyboard to "type" out letters and words. There is no attachment to the user but a headrest is recommended for comfort and stability. Small head movements are allowed for, as is eye blinking. If the eye moves out of range the screen display tells the user and waits for realignment. Each time the computer is switched on, it is automatically calibrated to the user's eve. The computer can be linked through an RS232 port to a printer or other peripheral and can be used as a remote control system for environmental control, to switch lights or appliances on and off, for example. The joint

Robert Hum is about to test a v.l.s.i. telecommunications circuit in a GenRAD GR 18 i.c. tester. The tester, claimed to be the most advanced in the world, is capable of performing 11 520 000 000 tests per second. So it would have completed about 170 billion tests in the time it takes to read this paragraph. Mr Hum works for Bell-Northern Research in Canada on the development of v.l.s.i. circuits for Northern Telecom. The manufacture of reliable chips depends on the ability of the research centre in understanding how and why chips fail and then on the ability to prevent such occurances.



## Open technology

A technical college equivalent of the Open University has been launched by Telford College, Edinburgh, called Teltec. Each course consists of practical kits. audio and video tapes and texts and coverssuch areas as control and instrumentation, electronics, microelectronics, pneumatics and hydraulics, electrical skills and computer appreciation. Tutorial centres are located throughout the country so that students can have expert guidance when needed. Tutors can also work under contract to individual companies to provide in-house training.

If Teltec does not offer a course required they can trace and request a suitable package from another participant in the Open Tech scheme. prizewinner was Cedric's inventor, Andrew Downing, senior lecturer in electrical and electronic engineering at Adelaide University.

The other prizewinner was Peter Donaldson, chief engineer at the MRC neurological prosthesis unit. The bladder controller treats incontinence by electrical stimulation of the sacral anterior roots in the spine. A receiver unit is planted beneath the ribs and is controlled by a transmitter held adjacent to it outside the body.

Signals from the receiver are connected by implanted cables to the nerves in the spinal column.

## **Alvey's Flagship**

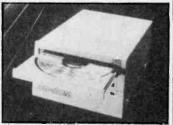
Flagship is the major demonstration model for the Alvey advanced computer research programme. Much accent is placed on the need for parallel processing and also on 'declarative' programming languages which improve the computer interface and enable the user to tell the computer what to do in plain language.

The project is the joint responsibility of Plessey, ICL, Manchester University and Imperial College, London. Manchester University has developed a data-flow computer and Imperial College are working on Alice, a graphreduction machine which uses about 200 Inmos Tranputers.

Plessey's contribution will be in speech recognition and synthesis and in v.l.s.i. design, under the leadership of Dr. Keith Warren.

We suggested to Dr. Warren that task-specific transputers would be the next logical step in speeding up the processing, and he agreed but said that they were already progressing beyond the Transputer and were looking at bit-slice processors, some of which may be task-specific.

The computers produced by this research are not aimed at replacing existing systems but at doing tasks not previously possible on a computer, especially in the areas of artificial intelligence and in user/computer interfacing. As has been predicted for some time, the compact disc can be used to store computer data and used like a rom. Hitachi have produced CD-Rom. Each disc has a read-only memory capacity of 552Mbits and the data transfer rate is 176Kbit/s.



**Dr David Wright was** awarded an Honorary Fellowship of the Royal **Television Society. He is a** specialist in colour and worked in optics when he became involved in the early development of television. The colour system adopted by the NTSC was based largely on Dr Wright's researches. He was Professor of **Technical Optics at** Imperial College, and has published many papers and a standard reference work: The measurement of colour.

The next generation of mobile telephone systems in Europe is under consideration at Ericsson. Basing their researches on the existing Nordic cellular system and on experiences in other areas of the world, Ericsson will present the results of their deliberations to the European Telecommunications Administration during this year.

A better deal for disabled and elderly telephone users is called for by DIEL, an independent advisory committee on telecommunications for the disabled and elderly. They are particularly concerned that the research into low data-rate visual phone system should continue at Essex University and that such users should be relieved from paying vat on equipment and services.

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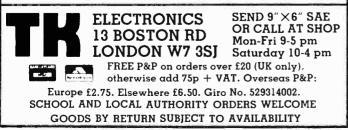
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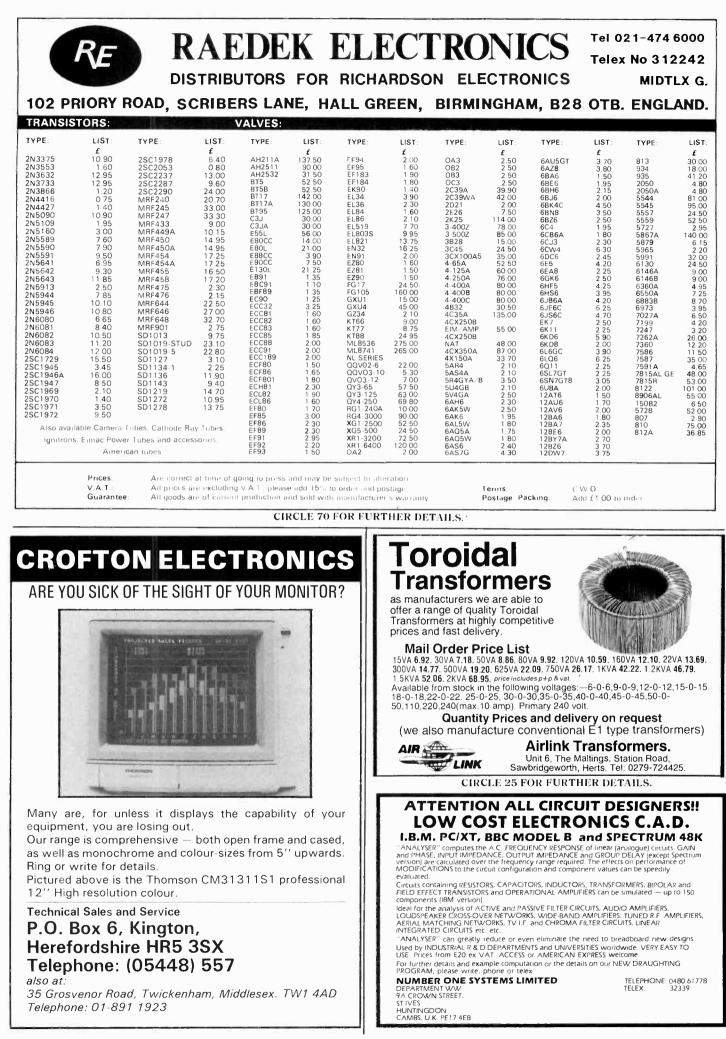


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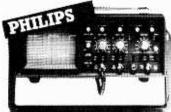
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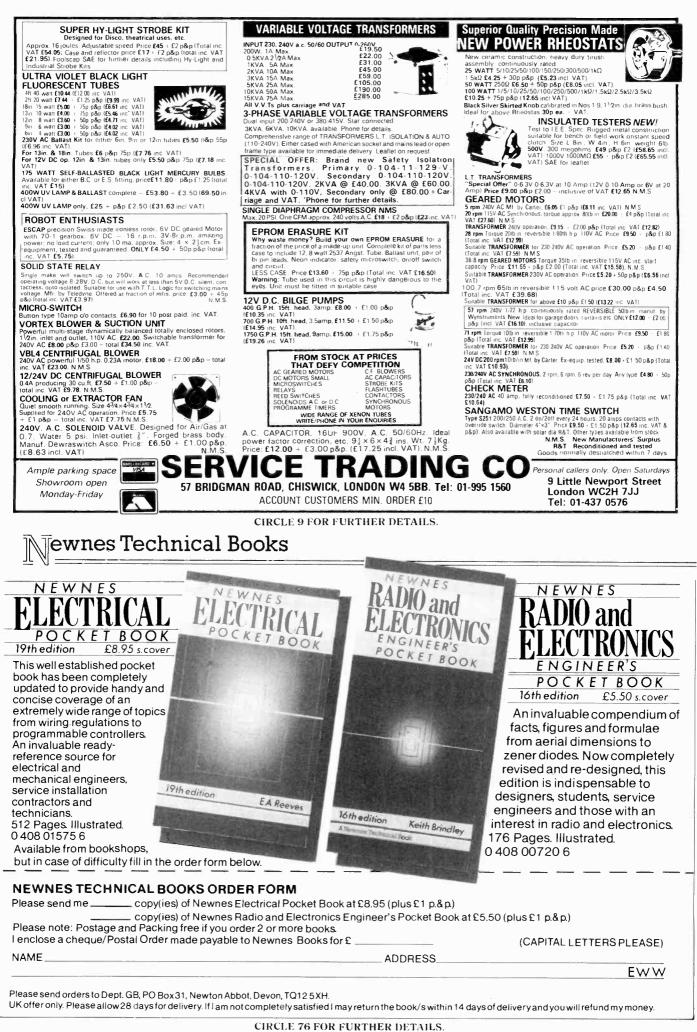
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	7420 0.30 7421 0.60 7422 0.36 7423 0.36 7423 0.36 7425 0.40 741 7426 0.40 741	7         7           ALSO0         0.24         7           ALS01         0.24         7           ALS02         0.24         7	74LS363 1.80 74LS364 1.80 74LS365 0.50 74LS366 0.50 74LS367 0.52 74LS368 0.50	74C93         1.50           74C95         1.60           74C107         1.00           74C150         5.00           74C151         2.00           74C157         2.50	4506         0.90           4507         0.35           4508         1.20           4510         0.55           4511         0.55	CA3090AQ 3.75 CA3130E 0.90 CA3130T 1.30 CA3140E 0.45 CA3140T 1.00 CA3146 2.25	LM3302 0.90 LM3900 0.80 LM3909 1.00 LM3911 1.80 LM3914 3.50 LM3915 3.40	TDA1170S 3.00 TDA2002 3.25 TDA2003 1.90 TDA2004 2.40 TDA2006 3.20 TDA2020 3.20	8035         3.50           80C35         6.00           8039         4.20           80C39         7.00           8080A         4.20	7.00 Z80BPIO 5.00 Z80BCTC 5.00 Z80BDART 9.00	27C64-25 10.00 27128-25 3.00 27128-25 7.50 27256-25 20.00 27256-30 20.00	75492         0.65           8T26         1.20           8T28         1.20           8T95         1.20           8T95         1.20           8T96         1.20	AY31015P 3.00 AY51013P 3.0 COM8017 3.00
	7428         0.43         741           7430         0.30         741           7432         0.36         741           7433         0.30         741           7433         0.30         741           7433         0.30         741           7433         0.30         741           7438         0.40         741	4LS04 0.24 7 4LS05 0.24 7 4LS08 0.24 7 4LS09 0.24 7 4LS10 0.24 7 4LS11 0.24 7	4LS374 0.90 4LS375 0.75 4LS377 1.30 4LS378 0.95 4LS379 1.30 4LS381 4.50	74C161 1.80 74C162 1.80 74C163 1.80 74C173 1.00 74C173 1.00 74C174 1.50 74C175 1.50	4513         1.50           4514         1.10           4515         1.10           4516         0.55           4517         2.20           4518         0.48	CA3161E 2.00 CA3162E 6.00 CA3189E 2.70 CA3240E 1.50 CA3280G 3.00 D7002 6.00	LM13600 1.50 M51513L 2.30 M51516L 4.50 MB3712 2.00 MC1310P 1.50 1413 0.75	TDA2593 5.00 TDA2653 7.00 TDA3560 7.50 TDA3810 7.50 TDA7000 3.50 TEA1002 7.00	80C85A 7.50 8086 22.00 8088 17.50 8741 15.00	216-150 4.00 2101 4.00 2102 2.50 2107B 5.00 2111A-35 4.00	27512 P.O.A TMS2716 5.00	8T98 1.20 81LS95 1.40 81LS96 1.40 81LS96 1.40 81LS97 1.40 81LS98 1.40	6MHz 3.75
	7440         0.40         741           7441         0.90         741           7442A         0.70         741           7443A         1.00         741           7443A         1.00         741	4LS14 0.50 7 4LS15 0.24 7 4LS20 0.24 7 4LS21 0.24 7 4LS22 0.24 7	4LS390 0.60 4LS393 1.00 4LS395A 1.00 4LS399 1.40 4LS399 1.80	74C194 1.50 74C195 1.50 74C221 2.50 74C224 2.00 74C245 2.25	4520         0.60           4521         1.15           4522         0.80           4526         0.70           4527         0.80           4528         0.65	DAC0800 3.00 DAC0808 3.00 DG308 3.00 HA1366 1.90 ICL7106 6.75 ICL7611 0.95	MC1495 3.00 MC1496 0.70 MC3340P 2.00 MC3401 0.70 MC3403 0.65 MF10CN 4.10	TL062 0.60 TL064 0.90 TL071 0.40 TL072 0.70 TL074 1.10 TL081 0.35	TMS9980 14.50 TMS9995 12.00 Z80 2.50	2114-2 <b>3.50</b> 2147 <b>4.00</b> 4116 <b>2.00</b>	CRT5037 12.00 CRT6545 9.00 EF9364 8.00 EF9365 25.00 EF9366 25.00	9602 3.00 9636A 1.60 9637AP 1.60	VISION
Nome	7447A         1.00         741           7448         1.00         741           7450         0.36         741           7451         0.35         741           7453         0.38         741	\$LS27         0.24         7           \$LS28         0.24         7           \$LS30         0.24         7           \$LS32         0.24         7           \$LS32         0.24         7           \$LS32         0.24         7	4LS490 1.50 4LS540 1.00 4LS541 1.00 4LS608 7.00 4LS61019.00	74C902 1.20 74C911 9.00 74C912 4.50 74C922 6.00 74C923 6.50	4531         0.75           4532         0.65           4534         3.80           4536         2.50           4538         0.75           4539         0.75	ICL7660 2.50 ICL8038 4.00 ICM7216B 22.00 ICM7217 7.50 ICM7555 0.90	MK50398 11.00 ML902 5.00 ML922 4.00 MM6221A 3.00 NE529 2.20	TL083 0.75 TL084 1.00 TL094 2.00 TL170 0.50 TL430C 1.20 UAA1003-3 9.35	280H 7.50 SUPPORT DEVICES 2651 12.00	41256-157 0.50 4164-15TI 3.00 4164 2.00	MC6845SP 6.50 MC6847 6.50 SFF96364 8.00 TMS9918 15.00 TMS9928 10.00		32.768KHz 1.00 1.00MHz 2.70 1.6432MHz 2.25
Name	7470         0.50         74           7472         0.45         74           7473         0.45         74           7474         0.50         74           7475         0.60         74	4LS38 0.24 7 4LS40 0.24 7 4LS42 0.50 7 4LS43 1.50 7 4LS48 0.90 7	4LS626 2.25 4LS628 2.25 4LS629 1.25 4LS640 2.00 4LS640-1 3.00	74ALS SERIES	4543         0.70           4551         1.00           4553         2.40           4555         0.36           4556         0.50           4557         2.40	LC7130 3.00 LC7131 3.50 LC7137 3.50 LC7137 1.20 LF347 1.20 LF351 0.60	NE555 0.22 NE556 0.60 NE564 4.00 NE565 1.20 NE566 1.50	UA2240 1.20 UAA170 1.70 UCN4801A 4.00 ULN2001A 0.75 ULN2002A 0.75	3245 4.50 6520 3.00 6522 3.50 6522A 5.50 6532 4.80	4532-20 2.50 4816AP/3 2.00 5101/5501 4.00 5514/5114 4.00	AD7581 15.00 ADC0808 11.90	8271 P.O.A FD1771 20.00 FD1791 20.00 FD1793 20.00	2.00 2.45760MHz(S) 2.50 2.5MHz 2.50 2.662MHz 1.75
1       1	7480         0.65         74           7481         1.80         74           7483A         1.05         74           7484A         1.25         74           7485         1.10         74           7486         0.42         74	4LS51 0.24 7 4LS54 0.24 7 4LS55 0.24 7 4LS73A 0.30 7 4LS73A 0.35 7 4LS75 0.45 7	4LS642 2.50 4LS642-1 3.00 4LS643 2.50 4LS643-1 3.00 4LS644 3.50 4LS645 2.00	74ALS02 0.45 74ALS04 0.50 74ALS08 0.50 74ALS10 0.45 74ALS20 0.45 74ALS22 0.45	4566 1.40 4568 2.40 4569 1.70 4572 0.45 4583 0.90	LF356N 1.10 LF357 1.00 LF398 4.00 LM10CLH 4.50 LM301A 0.30	NE570 4.00 NE571 3.00 NE592 0.90 NE5532P 1.50 NE5533P 1.60 NE5534P 1.20	ULN2068 2.90 ULN2802 1.90 ULN2803 1.80 ULN2804 1.90 UPC575 2.75	68B21 2.50 6829 12.50 6840 3.75 68B40 6.00 6850 1.60	5517AP 6.00 6116P-3 3.50 6116LP-3 3.50 6264-12 12.00 6264P-157.50	AM25S10 3.50 AM25LS25213.50 AM25LS25383.50 AM26LS31 1.20 AM26LS32 1.20 AM7910DC 25.00	WD2143 12.00 WD2793 27.00	3.5795MHz 1.00 4.00MHz 1.50 4.194MHz 2.00 4.43MHz 1.00 4.9152MHz 2.50 5.00MHz 1.50
12/200       12/200	7490A 0.55 744 7491 0.70 744 7492A 0.70 744 7493A 0.55 744 7494 1.10 744 7495A 0.60 744	4LS78         0.42         7           4LS83A         0.70         7           4LS85         0.75         7           4LS86         0.35         7           4LS90         0.48         7           4LS91         0.90         7	4LS668 0.90 4LS669 0.90 4LS670 1.70 4LS682 2.50 4LS683 3.00 4LS684 3.50	74ALS138 1.50 74ALS139 1.50 74ALS244 4.00 74ALS245 4.75 74ALS573 2.60	4585         0.60           4724         1.50           14411         7.50           14412         7.50           14416         3.00           14419         2.60	LM308CN 0.75 LM310 2.25 LM311 0.60 LM318 1.50 LM319 1.80 LM324 0.45	OP-07EP 3.50 PLL02A 5.00 RC4136 0.55 RC4151 2.00 RC4195 1.50 RC4558 0.55	UPC1156H 3.00 UPC1185H 5.00 XR210 4.00 XR2206 4.50 XR2207 3.75 XR2211 5.75	6852         2.50           6854         6.50           68854         8.00           6875         5.00	. 6514-45 4.00 6810 1.60 74S189 1.80 74S289 2.25	28.00 DM8131 6.00 DP8304 3.50 DS3691 3.50 DS8830 1.40 DS8831 1.50	RO32513UC 7.50	6.00MHz 1.40 6.144MHz 1.40 7.00MHz 1.50 7.16MHz 1.75 8.00MHz 1.50 8.867MHz 1.75
74:49       110       74:58:16       74:50       0.00       1.00	7497         2.10         741           74100         1.90         741           74107         0.50         741           74109         0.75         741           74110         0.75         741           74111         0.55         741	4LS93 0.54 7. 4LS95B 0.75 7. 4LS96 0.90 4LS107 0.40 4LS109 0.40 4LS112 0.45	4LS688 3.50 4LS783 21.00	4000 SERIES 4000 0.20 4001 0.24	14495         4.50           145000         6.50           14599         2.00           22100         3.50           22101         7.00           22102         7.00	LM335Z 1.30 LM336 1.60 LM339 0.40 LM348 0.60 LM358P 0.50 LM377 3.00	S50240 9.00 SAA1900 16.00 SFF96364 8.00 SL490 3.00 SN76013N 3.00 SN76023N 3.00	XR2240 1.20 ZN404 1.00 ZN414 0.80 ZN419P 1.75 ZN423E 1.30 ZN424E 1.30	8156         3.80           8205         2.25           8212         2.00           8216         1.60	93L422 7.50 93425 6.00	DS8833 2.25 DS8836 1.50 DS8838 2.25 D7002 8.00 MC1488 0.60 MC1489 0.60	REAL TIME CLOCK	10.50MHz 2.50 10.70MHz 1.50 11.00MHz 3.00 12.00MHz 1.50 14.00MHz 1.75 14.31MHz 1.60
1128       0.68       744530       0.68       74530<	74118         1.10         74           74119         1.70         74           74120         1.00         74           74121         0.55         74           74122         0.70         74           74123         0.80         74	ALS114 0.45 ALS122 0.70 7 ALS123 0.80 7 ALS125 0.50 7 ALS126 0.50 7 ALS132 0.65 7	4\$00 0.50 4\$02 0.50 4\$04 0.50 4\$05 0.50 4\$08 0.50	4006         0.70           4007         0.25           4008         0.60           4009         0.45           4010         0.60	40085         1.20           40097         0.36           40098         0.40           40100         1.50           40101         1.25           40102         1.30	LM380N 1.50 LM381AN1.70 LM383 3.25 LM384 2.20 LM386N-1 1.00 LM387 2.70	SN76115N 2.15 SN76489 4.00 SN76495 4.00 SN76660 1.20 SP0256AL2 7.00	ZN426E8 3.00 ZN427E8 6.00 ZN428E8 4.50 ZN429E8 2.25 ZN447E 9.00 ZN449E 3.00	8228 5.50 8243 2.60 8250 9.50 8251A 3.25	24S10 2.50 18S030 2.00 18SA030 2.00 74S188 1.80 74S287 2.25	MC3459 4.50 MC3470 4.75 MC3480 8.50 MC3486 2.25 MC3487 2.25 MC4024 5.50	TELETEXT	15.00MHz 2.00 16.00MHz 2.00 17.734MHz 1.50 18.00MHz 1.50 18.432MHz 1.50
7.143       1.20       7.4538       0.60       40.19       0.20       40.19       2.30       VOLVAGE REQUITORS       822       300       75.15       1.60       1.60       1.60       1.60       1.60       1.60       1.60       1.60 <t< td=""><td>74126         0.55         741           74128         0.55         741           74132         0.75         741           74136         0.70         741           74141         0.90         741</td><td>LS136         0.45         7           LS138         0.55         7           LS139         0.55         7           LS139         0.55         7           LS145         0.95         7           LS147         1.75         7</td><td>74S11         0.75           74S20         0.50           74S22         0.50           74S30         0.50           74S32         0.60</td><td>4013         0.36           4014         0.60           4015         0.70           4016         0.36           4017         0.55</td><td>40104         1.20           40105         1.50           40106         0.48           40107         0.55           40108         3.20           40109         0.80</td><td>LM391 1.80 LM392N 1.10 LM393 0.85 LM394CH 4.00</td><td>TA7130 1.40 TA7204 1.50 TA7205 0.90 TA7222 1.50</td><td>ZN459CP 3.00 ZN1034E 2.00 ZNA1040 6.60 ZNA134J 23.00</td><td>8255AC-5 3.20 8256 18.00 8257C-5 54.00 8259C-5 4.00 8271 P.D.A</td><td>74S387 2.25 82S23 1.50 82S123 1.50</td><td>MC14411 9.00 MC14412 7.50 75107 0.90 75108 0.90 75109 1.20</td><td>SAA5020 6.00 SAA5030 7.00 SAA5041 16.00</td><td>24.000MHz 1.75 48.000MHz 1.75 116MHz 2.5</td></t<>	74126         0.55         741           74128         0.55         741           74132         0.75         741           74136         0.70         741           74141         0.90         741	LS136         0.45         7           LS138         0.55         7           LS139         0.55         7           LS139         0.55         7           LS145         0.95         7           LS147         1.75         7	74S11         0.75           74S20         0.50           74S22         0.50           74S30         0.50           74S32         0.60	4013         0.36           4014         0.60           4015         0.70           4016         0.36           4017         0.55	40104         1.20           40105         1.50           40106         0.48           40107         0.55           40108         3.20           40109         0.80	LM391 1.80 LM392N 1.10 LM393 0.85 LM394CH 4.00	TA7130 1.40 TA7204 1.50 TA7205 0.90 TA7222 1.50	ZN459CP 3.00 ZN1034E 2.00 ZNA1040 6.60 ZNA134J 23.00	8255AC-5 3.20 8256 18.00 8257C-5 54.00 8259C-5 4.00 8271 P.D.A	74S387 2.25 82S23 1.50 82S123 1.50	MC14411 9.00 MC14412 7.50 75107 0.90 75108 0.90 75109 1.20	SAA5020 6.00 SAA5030 7.00 SAA5041 16.00	24.000MHz 1.75 48.000MHz 1.75 116MHz 2.5
7:455       0.80       7:45:86       0.75       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.80       7:45:87       0.70       0.125       0	74143         1.30         741           74144         2.70         741           74145         1.10         741           74147         1.70         741           74148         1.40         741           74150         1.75         741           74151A         0.70         741	ALS151 0.65 7 ALS152 2.00 7 ALS153 0.65 7 ALS154 1.60 7 ALS155 0.65 7 ALS156 0.65 7 ALS156 0.65 7 ALS156 0.65 7 ALS158 0.65 7	4S40         0.50           4S51         0.60           4S64         0.45           4S74         0.70           4S85         3.00           4S86         1.00           4S112         1.50	4019         0.60           4020         0.80           4021         0.60           4022         0.70           4023         0.30           4024         0.48           4025         0.24           4026         0.90	40114         2.25           40147         2.80           40163         1.00           40173         1.20           40174         1.00           40175         1.00           40192         1.00	1A FIX +VE 5V 7805 0. 6V 7806 0. 6V 7808 0.	ED VOLTAGE PLA 45 7905 50 7906 50 7908	STIC TO220 -VE 0.50 0.50 0.50	8279C-5, 4,80 8282 4,00 8284 4,60 8287 3,80 8288D 9,50 8755A 16,00	2816-30 <b>15.00</b> 2K+8 <b>30.00</b> 9306 256bits (16×16) <b>4.50</b>	75112         1.60           75113         1.20           75114         1.40           75115         1.40           75122         1.40           75122         1.40           75150P         1.20	All prices ar change wit Only current	e subject to hout notice prime grade
72:163       110       72:153       110       120<	74155         0.80         74L           74156         0.90         74L           74157         0.80         74L           74169         1.75         74L           74160         1.10         74L           74161         0.80         74L	LS161A 0.75 7 LS162A 0.75 7 LS163A 0.75 7 LS163A 0.75 7 LS164 0.75 7 LS165A 1.10 7 LS166A 1.50 7	45114 1.20 45124 3.00 45132 1.00 45133 0.60 45138 1.80 45139 1.80	4028         0.60           4029         0.75           4030         0.35           4031         1.25           4032         1.00           4033         1.25	40244 1.50 40245 1.50 40257 1.80 40373 1.80 40374 1.80	15V 7815 0. 18V 7818 0. 24V 7824 0. 1A FI) 5V 78L05 0. 6V 78L06 0.	50 7915 50 7918 50 7924 KED VOLTAGE PLA 30 5V 7 30 12V	0.15 0.50 0.50 STIC TO92 9L05 0.45 79L12 0.50	2N5777 0.50 BPX25 1.80 BPW21 2.80 OCP71 1.80 ORP12 1.20	0.125" RED TIL2090.12 GRN TIL211 0.16 YEL TIL212 0.20	0.2" TIL220 0.15 TIL222 0.18	rang	je of:
7473       1.40       745175       3.20       4433       0.80       143238       3.8 5V       3.50         74174       1.00       745184       1.00       745184       1.00       78112       5.10       745184       3.50         74175       1.00       745189       1.00       4045       1.00       78112       5.45 5V       5.46       90         74176       1.00       745184       3.00       4044       0.66       789105       10.45 5V       90         74178       1.50       745195       3.00       4047       0.60       789105       10.45 5V       90       10.3916       3.00       10.00 Mill       10.00 Mill       3.00       10.00 Mill       3.00       10.00 Mill       10.00 Mill       10.00 Mill       3.00       10.00 Mill       10.00	74163         1.10         744           74164         1.20         741           74165         1.10         744           74166         1.40         744           74166         1.40         744           74167         4.00         744           74170         2.00         744	ALS169 1.00 7 ALS170 1.40 7 LS173A 1.00 7 ILS174 0.75 7 ILS175 0.75 7 ILS181 2.00 7	4\$151 1.50 4\$153 1.50 4\$157 2.00 4\$158 2.00 4\$163 3.00 4\$169 5.50	4035         0.70           4036         2.50           4037         1.10           4038         1.00           4040         0.60           4041         0.55	80C97 0.75 80C98 0.75 FIXED REG	12V 7BL 12 0. 15V 7BL 15 0. OTHER REC	30 30		ORP61 1.20 SFH205 1.00 TIL32 0.55 TIL78 0.55 TIL31B 1.20 TIL81 1.20	CXQ (Bi colour 1.00 10 LED Bar Graph: Red 2.25	74C925 6.50 74C926 6.50 74928 6.50 7216B 22.00	Bridge F Thyrist	lectifiers, ors and
Critical 1,00       Critical 1,00 <thcritical 1,00<="" th=""> <thcritical 1,00<="" t<="" td=""><td>74173         1.40         741           74174         1.10         740           74175         1.05         740           74176         1.00         740           74176         1.50         740           74179         1.50         740</td><td>LS190 0.75 7 LS191 0.75 7 LS192 0.80 7 LS194A 0.75 7 LS195A 0.75 7 LS195A 0.75 7 LS196 0.80 7</td><td>4\$175 3.20 4\$188 1.80 4\$189 1.80 4\$194 3.00 4\$195 3.00 4\$196 3.50</td><td>4043         0.60           4044         0.60           4045         1.00           4046         0.60           4047         0.60           4048         0.55</td><td>LM323K 78H05KC 78H12 78P05 VARIABLE</td><td>3A 5V 5A 5V 5A 12V 10A 5V</td><td></td><td>3.50 5.40 6.40 9.00</td><td>DISPLAYS</td><td>MAN66102.00 NSB58815.70 TIL311 6.50 TIL729 1.00</td><td>LM3914 3.50 LM3915 3.50 LM3916 3.50 UDN6118 3.20</td><td>det</td><td>ails.</td></thcritical></thcritical>	74173         1.40         741           74174         1.10         740           74175         1.05         740           74176         1.00         740           74176         1.50         740           74179         1.50         740	LS190 0.75 7 LS191 0.75 7 LS192 0.80 7 LS194A 0.75 7 LS195A 0.75 7 LS195A 0.75 7 LS196 0.80 7	4\$175 3.20 4\$188 1.80 4\$189 1.80 4\$194 3.00 4\$195 3.00 4\$196 3.50	4043         0.60           4044         0.60           4045         1.00           4046         0.60           4047         0.60           4048         0.55	LM323K 78H05KC 78H12 78P05 VARIABLE	3A 5V 5A 5V 5A 12V 10A 5V		3.50 5.40 6.40 9.00	DISPLAYS	MAN66102.00 NSB58815.70 TIL311 6.50 TIL729 1.00	LM3914 3.50 LM3915 3.50 LM3916 3.50 UDN6118 3.20	det	ails.
74192       1.10       7452457       2.50       4056       0.85       74194       1.15         74193       1.15       745258       2.50       4060       0.75       78192       1.75         74193       1.15       745258       2.50       4060       0.75       78192       1.75         74193       1.10       745258       2.50       4060       0.75       78192       0.70       78192       0.70       78192       0.70       78192       0.75       75492       0.70       78192       78192       78197       78010 <td>74181         3.40         741           74182         1.40         741           74184         1.80         741           74185A         1.80         741           74190         1.30         741</td> <td>ILS221 0.90 7 ILS240 0.80 7 ILS241 0.80 7 ILS242 0.90 7 ILS243 0.90 7 ILS244 0.80 7</td> <td>4S201 3.20 4S225 5.20 4S240 4.00 4S241 4.00 4S244 4.50 4S251 2.50</td> <td>4050         0.35           4051         0.65           4052         0.60           4053         0.60           4054         0.80</td> <td>LM317T LM317K LM337T LM350T LM396K LM723N</td> <td>TO3 10A+VAR 10A+VAR</td> <td></td> <td>2.40 2.25 4.00 15.00 0.50</td> <td>FND500/TIL730 1.00 FND507/TIL729 1.00 MAN71/DL707 1.00</td> <td>MAN8910 1.50 MAN8940 2.50 DISPLAY DRIVERS</td> <td>ULN2003 0.90 ULN2004 0.90 ULN2068 2.90 ULN2802 1.90 ULN2883 1.80 ULN2804 1.90</td> <td>ILD74 1.30 ILQ74 2.20 MCT26 1.00 MCS2400 1.90</td> <td>FIL111         0.70           TIL112         0.70           TIL113         0.70           TIL116         0.70           SN137         3.60</td>	74181         3.40         741           74182         1.40         741           74184         1.80         741           74185A         1.80         741           74190         1.30         741	ILS221 0.90 7 ILS240 0.80 7 ILS241 0.80 7 ILS242 0.90 7 ILS243 0.90 7 ILS244 0.80 7	4S201 3.20 4S225 5.20 4S240 4.00 4S241 4.00 4S244 4.50 4S251 2.50	4050         0.35           4051         0.65           4052         0.60           4053         0.60           4054         0.80	LM317T LM317K LM337T LM350T LM396K LM723N	TO3 10A+VAR 10A+VAR		2.40 2.25 4.00 15.00 0.50	FND500/TIL730 1.00 FND507/TIL729 1.00 MAN71/DL707 1.00	MAN8910 1.50 MAN8940 2.50 DISPLAY DRIVERS	ULN2003 0.90 ULN2004 0.90 ULN2068 2.90 ULN2802 1.90 ULN2883 1.80 ULN2804 1.90	ILD74 1.30 ILQ74 2.20 MCT26 1.00 MCS2400 1.90	FIL111         0.70           TIL112         0.70           TIL113         0.70           TIL116         0.70           SN137         3.60
74199       2.20       74152457.070       745289       2.25       4070       0.24       SG3524       3.00         74291       1.10       7415259       1.00       7415259       1.00       745373       4.00       4071       0.24       TL494       3.00         74259       1.00       7415259       1.00       745373       4.00       4072       0.24       TL494       3.00         74259       1.00       7415266       0.60       75       74537       4.00       4072       0.24       7484       3.00         74259       1.00       7415266       0.60       74537       4.00       4072       0.24       7484       3.00         74259       1.50       7415266       0.60       745387       2.25       4075       0.24       78540       2.50         74259       1.50       7415266       0.60       745387       2.25       4075       0.24       78540       764195       1.50         TERCHNOMATIC LTD       1.50       PLEASE ADD 50p p&p & 15% VAT         (Export: no VAT, p&p at Cost)	74193         1.15         741           74194         1.10         741           74195         0.80         740           74196         1.30         740           74197         1.10         740	LS247 1.10 7 LS248 1.10 7 LS249 1.10 7 LS251 0.75 7 LS253 0.75 7	4S258 2.50 4S260 1.00 4S261 3.00 4S283 2.70 4S287 2.25	4060 0.70 4063 0.85 4066 0.40 4067 2.30 4068 0.25	78HGKC 78GUIC 79HGKC 79GUIC SWITCHING	5A+VAR 1A+VAR 5AVAR 1AVAR			MAN4640 2.00 LOW PROF 8pin 9p 14pin 10p 2	9370 4.50 ILE SOCKETS BY 18pin 16p 24p 20pin 18p 28p	75492 0.70 TI in 24p 8pin in 26p 14pin	25p 18pin 50 35p 20pin 60	KETS BY TI p 24pin 70p p 28pin 80p
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MAIL ORDERS TO: 17 BURNLE I ROAD, LONDON NWIO ILD Orders non Oovernment Depts. & Coneges etc. welcome.	MAIL						W10 1ED	Order	(Ea	port: no VAT	, p&p at Cost		ne.



CIRCLE 76 FOR FURTHER DETAILS.

# Naiad training robot

## Robot arm interfaces with Apple, BBC, Commodore and IBM computers. Design by Peter Wells and Dick Becker

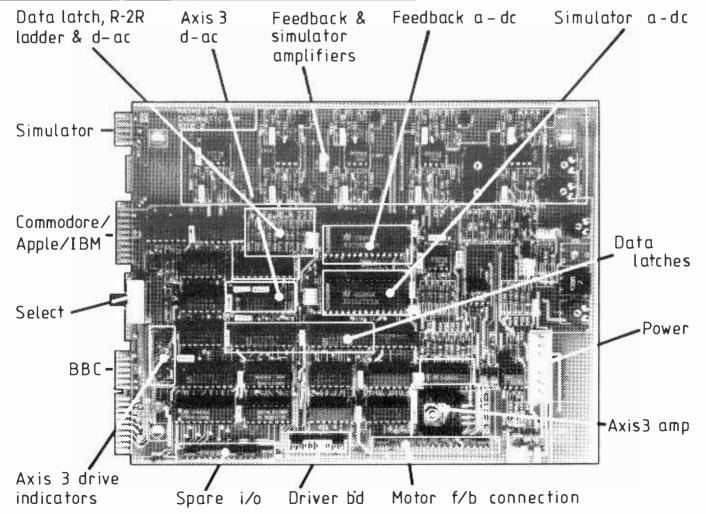
he control electronics of Naiad are split between the three printed-circuit boards: computer interface, solenoid driver and power supply. The computer interface shown below, communicates with the host computer bus, provides the power for the d.c. motor, generates 5V signals for the solenoid driver board, and processes signals for return to the computer. There are also a number of inputs and outputs for interfacing to additional equipment such as sensors,

conveyor belt, indexing table and so on.

The robot works with parallel data from the computer and the fastest way of interchanging data is by connecting straight onto the bus of the computer. Computer manufacturers provide a variety of means of gaining access to the bus and there are two edge connectors on the interface board. The first connector is specifically for the BBC computer and the second one for other machines. Leads terminated with suitable plug-in cards for the Commodore 64, Apple IIe and IBM computers are available. Connection is made to the 1MHz bus connector, expansion port (rom games socket) and expansion slots respectively.

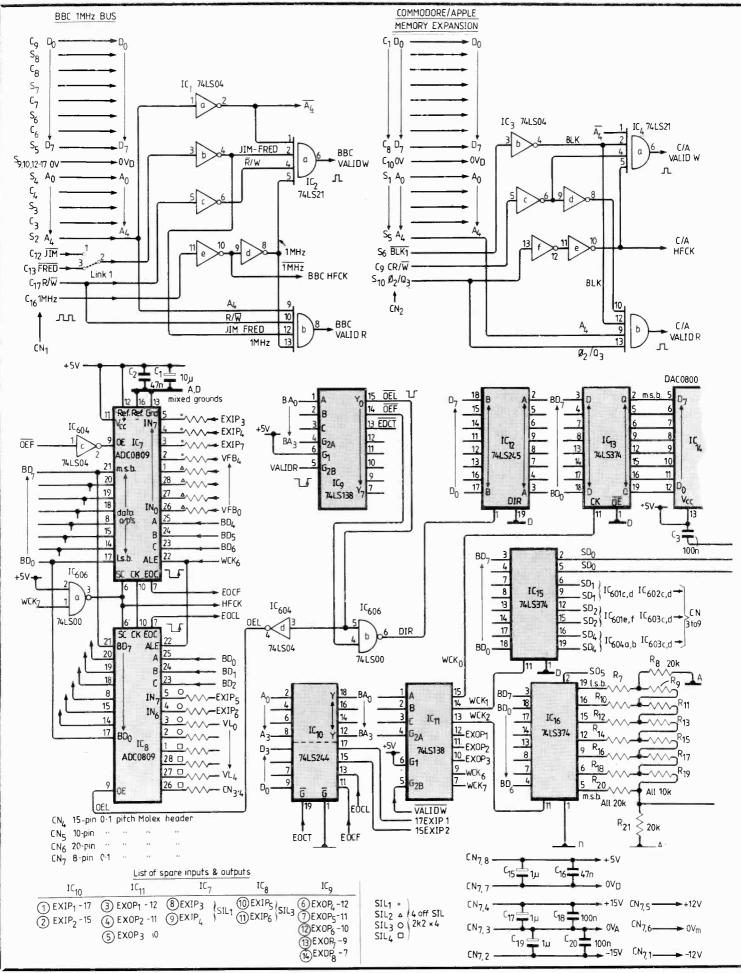
The signals required for the computer interface are: eight data lines, the five leastsignificant address lines, the read/write signal, a block decode of a minimum of 32 addresses, and the clock. For

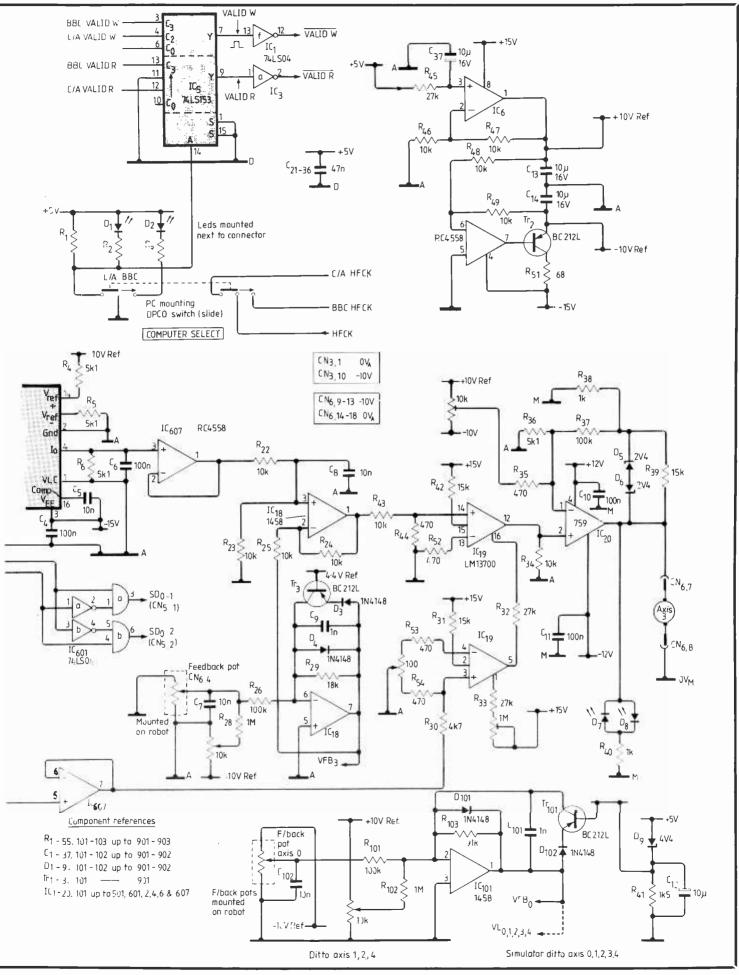
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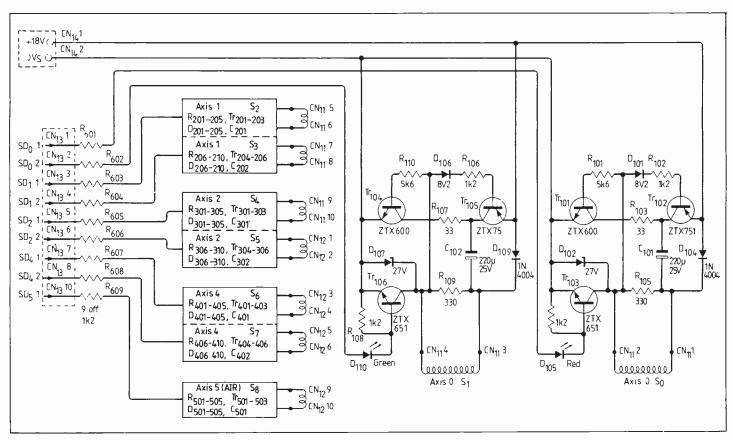


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## by R.H.Becker







the IBM the generation of a block decode and the combination of signals i/o write and i/o read to read/write takes place on the IBM plug-in card. Although only four computer types have been mentioned, almost any computer or microprocessor system will control the Naiad providing there is access to the bus and a 20-way cable is made.

Operation of the interface, pp: 18 & 19, follows. Circuits  $IC_1$  and  $IC_2$  gate together  $A_4$ , the clock, read/write and a linkselectable choice of block decodes (Jim and Fred) to generate valid write and valid read signals defining the conditions that data is being sent to the robot, and is required from it, respectively. Circuits  $IC_3$  and  $IC_4$  act similarly for computers other than the BBC. Only one computer can be connected at any one time and a slide switch shown, positioned between the edge connectors, selecting which interface is operative, and this is shown by led indicators  $D_1$  and  $D_2$ . The switch selects directly the clock and indirectly, by data selector IC5, the valid write and valid reads signals.

To avoid loading heavily the bus of the host computer the data bus is buffered by  $IC_{12}$  which is a transceiver meaning 20

that it will work in either direction. This is necessary as data needs to be received back from the robot as well as sent and there is a direction signal on pin 1. Normally this signal is low and the computer data is passed to the data bus of the robot, but when the analogue to digital converter signals are read, the direction signal goes high and data on the bus of the robot is transferred to the bus on the computer.

The lowest four address lines are also buffered with half of  $IC_{10}$  and the addresses decoded by  $IC_9$  and  $IC_{11}$ .  $IC_9$  decodes the addresses to give the signals 'output-enable learn' and 'output-enable feedback' for the two a-to-d converters and 'endof-conversion test' which is used for checking that the converters are ready to be read.  $IC_{11}$ decodes the addresses to give a write-clocks signal for the three data latches IC<sub>13</sub>, IC<sub>15</sub> and IC<sub>16</sub> and to give the signals 'address latch enable' and 'output enable' for the converters. The unused outputs of the decoders function as external outputs for operating peripheral equipment.

Data latch  $IC_{13}$  is the one written to, for defining the next position of axis 3, the d.c. motordriven wrist elevation. The data, which is held on the latch until written to again, is transferred to the digital-toanalogue converter  $IC_{14}$ . The output of the d-to-a converter is buffered by  $IC_{607a}$  providing a 0 to +5V signal. Being an eightbit system there are  $2^8$  i.e. 256 possible levels, therefore for each bit data change there is an output change of 20mV.

The reference voltage for the d-to-a converter is +10V and is derived from the +5V rail by  $IC_{6a}$ . With +5V on pin 3, the output settles at the voltage which also gives +5V on pin 2. As  $R_{46}$  and  $R_{47}$  are equal the output is twice the input. IC<sub>6b</sub> inverts the +10V to provide a -10V rail. As the -10V rail is used for powering the various potentiometers used for position sensing of the robot and the simulator, the current output capability of the operational amplifier is boosted by Tr<sub>2</sub>.

Axis 3 feedback (position sensing) potentiometer output is buffered by  $IC_{18b}$ . The potentiometer nulls out the offset voltage which is the voltage on the potentiometer at the zero (lowest) position. The gain of the stage is less than unity to give an output range of 0 to +5V for the 250° of movement of the axis. The circuit has around it two diodes and a transistor to limit the output to no more than 5V, which is the maximum

permitted on the input of the ato-d converter.

The feedback signal from  $IC_{18b}$  is compared with the dac output by  $IC_{18a}$  to give an error signal in the range of -5 to +5Vand this then passes through  $IC_{19b}$  which is a variable gain transconductance amplifier. The current out from pin 12 is proportional to the voltage across its input pins 13, 14 and the gain is proportional to the current entering pin 16.

The gain controlling current comes from voltage-to-current converter  $IC_{19a}$ , the voltage to which comes from a discretecomponent d-to-a converter comprising R7 to R21 and IC607b. Seven out the outputs of data latch IC<sub>16</sub> feed an R/2R ladder network giving an output in the range of 0 to 2.5V which is buffered by  $IC_{607b}$ . This type of converter is not as accurate as  $IC_{14}$  but although a couple of percent difference in position is very important, this amount of change in the gain of the system is hardly noticeable. Sources of error in the discrete component version are the resistors themselves, which are 1% tolerance, and the differing impedances of t.t.l. outputs in the high and low states. TT logic is intended to sink current rather than supply it and the output impedance is about 200

ohms higher in the high state.

The error signal from  $IC_{19b}$  is finally amplified to a level suitable for driving the motor by power operational amplifier IC<sub>20</sub>. The potentiometer nulls out the input offset so that zero error signal gives zero output. As motors do not start to move until 2 to 3V is applied to them, the gain of the stage is boosted (by blocking the feedback path with zener diodes  $D_5$  and  $D_6$ ) so that almost any error will cause an output sufficient to just turn the motor. Once 3V output is reached, the diodes conduct and the gain of the stage drops to that set by the ratio of  $R_{37}$  to  $R_{36}$ . Diodes  $D_7$  and  $D_8$  indicate the direction of drive to the motor.

Controlling the solenoids is rather simpler than the motor as the aim is simply for the computer to turn them on or off and the eight outputs of data latch IC<sub>15</sub> together with one of the outputs of IC<sub>16</sub> set the states of the nine solenoids. To prevent a programming error from simultaneously turning on both valves of an axis, thereby causing a hydraulic short circuit, gates 601 to 604 are used to disallow this condition.

The solenoids are 24V types meaning that 24V should be

applied to turn them on. However once turned on, they will stay on until the voltage drops below about 8V. A continuous 24V is therefore a waste of energy which causes unnecessary heating inside the robot base and the solenoid driver board is used to supply more appropriate voltages to the solenoids. The board takes the 5V control signals from the interface board and briefly applies over 30V to the appropriate solenoid turning it on very rapidly. This surge is then followed by a steady 17V to give a holding current which causes only 50% of the heat resulting from the use of a steady 24V.

The solenoid drive circuitry, (page 20) is repeated for the nine valves. Taking solenoid S<sub>0</sub>: when the input signal is at  $0\ddot{V}$ , which represents the off condition,  $Tr_{103}$  is off and its collector high turning on Tr<sub>10</sub>. The voltage across  $D_{104}$  and  $R_{109}$ is insufficient to turn on D<sub>101</sub> hence  $Tr_{102}$  is off.  $C_{101}$  charges almost to supply voltage, from current flowing along the path  $D_{104} - C_{101} - R_{103} - Tr_{101}$ When the input signal switches to +5V,  $D_{105}$  glows indicating this condition and  $Tr_{103}$  saturates, turning off  $Tr_{101}$  and also turning on  $Tr_{102}$ . This places the charged capacitor across  $D_{104}$ . The voltage on the solenoid is now equal to the supply rail plus the charge on the capacitor (less the small drops across  $D_{104}$  and  $Tr_{103}$ ). The solenoid has a resistance of about 100 ohms so the time constant of the capacitor discharge is 22ms which is more than adequate for turning on the valve which normally has a turnon time of about 5ms.

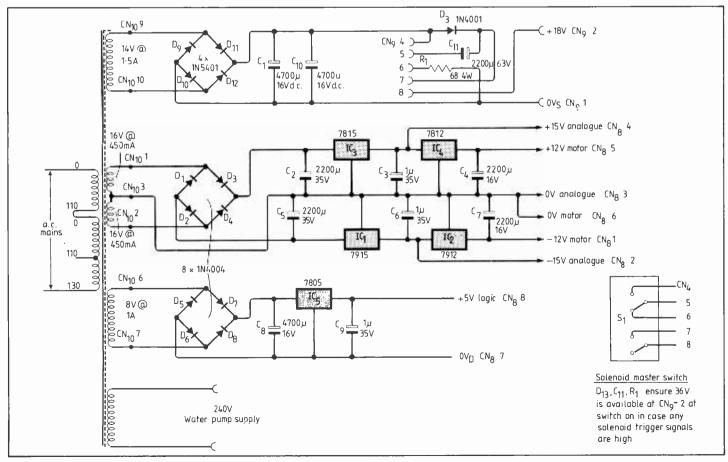
To enable the computer to read the positions of the axes of the robot and of the simulator there are two a-to-d converters. IC<sub>7</sub> reads the feedback from the robot whilst IC<sub>8</sub> is for the learn axes of the simulator. Spare inputs are used as external inputs from any peripheral equipment. The ADC0809 is an 8 bit, 8 channel multiplexed converter.

First the converters are written to, to define the axis to be read. Buffered data lines 0, 1, 2 are used to define the learn axis and data lines 4, 5, 6 to define the feedback axis. Next they are written to again to start the conversions, which occur simultaneously on the two converters. The data sent in this write operation is irrelevant but in the examples given later, zero is always used. Finally a read operation takes data from one of the converters. Conversion takes about  $100\mu$ s so if in doubt as to whether sufficient time has elapsed for the conversion (little doubt when using Basic!) the state can be checked by reading the end-of-conversion signals.

Although there is no mention of it in the data book, a-to-d converters can catch out the unwary with a phenomenom known as s.c.r. breakdown. If this occurs heavy current is drawn leading to overheating and eventual destruction. This can be triggered by voltage spikes on the bus or overvoltage on the inputs. To avoid any chance of breakdown, the converters are connected to the buffered bus of the robot rather than straight onto the computer bus. There are also resistor network on the converter inputs and voltage limiters on the amplifiers feeding them.

Each signal from the potentiometers passes through a buffer (IC<sub>101</sub>, up to IC<sub>504</sub>), similar to that of axis 3, providing offset adjustment and limiting for the converter.

The power supply, below



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A = & FD00

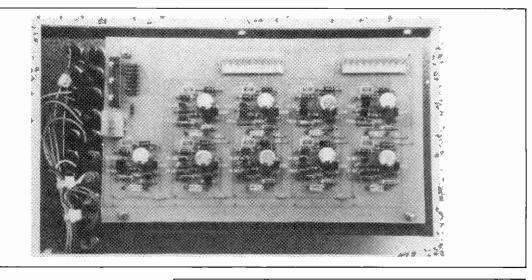
provides regulated supply rails of +5V, +15V, -15V for the computer interface board, +18V unregulated for the solenoid driver board and 240V for the pump.

The Naiad is memory mapped (i/o mapped when used on the IBM) meaning that the computer treats the robot as if it were part of its memory (i/o space on the IBM) and can be written to or read from. Table 1 gives the addresses used by the robot. Operation is most simply shown by examples and for these, BBC Basic will be used. In BBC Basic both poke and peek are represented by ?.

## To: define address of start of block decode JIM

rotate axis 0 clockwise turn on solenoid S<sub>0</sub>) set ?(A + 1) = 1all bits low except D<sub>0</sub> rotate axis 0 anticlockwise turn on solenoid S1) Write Addresses set ?(A + 1) = 2all bits low except D1 A + 0raise axis 2 (turn on solenoid S<sub>4</sub>) set all bits low ?(A + 1) = 16except D<sub>4</sub> A + 1 raise axis 2 and rotate axis 0 clockwise set all bits low except  $D_0$ ,  $D_4$ close the gripper set  $D_0$  of next latch high (G is ?(A + 1) = 17an even integer defining the gain of axis 3 ?(A + 2) = G + 1amplifier) ?(A + 2) = Gopen gripper again send axis 3 to lowest position ?A = 0 ?A = 128 send axis 3 to centre position A + 2?(A + 2) = 128set amplifier gain to 1/2 of maximum ?(A + 6) = 0read axis 0 feedback(set data lines low on (A + 7) = 0multiplexer) A + 3EXOP1 F0 = ?(A + 17)A + 4 EXOP2 ?(A + 6) = 48read axis 3 feedback (set D<sub>4</sub>, D<sub>5</sub> high on A + 5EXOP3 ?(A + 7) = 0multiplexer) A + 6F3 = ?(A + 17)read axis 3 learn (set D<sub>0</sub>, D<sub>1</sub> high on ?(A + 6) = 3multiplexer) ?(A + 7) = 0L3 = ?(A + 16)read analogue input EXIP5 ?(A + 6) = 7?(A + 7) = 0EXIP5 = ?(A + 16)enable external device on EXOP1 ?(A + 3) = 0EOCF = ?(A + 18)read end-of-conversion feedback AND 1 B = ?(A + 18): EXread external digital input EXIP1 IP1 = (B AND)8)DIV8 A + 7You can see that very simple table, a conveyor system using a A + 19 EXOP4 statements will operate the axes stepper motor for position A + 20 EXOP5 of the Naiad together with incrementing and a range of EXOP6 A + 21 external devices. Data back sensors. A + 22 EXOP7 from the robot and external

ware has already been written for the Naiad and will be supplied with it. Even more is equipment is under development. The first devices to be available will be an indexing *Device and Device and* 



#### Table 1. Addresses used by Naiad. A is address of block decode. Data Sent Axis 3 position Data range 0 to 255 (whole range) Solenoids 0 - 7 DO (1) S0 D1 (2) **S**1 D2 (4) S2 D3 (8) S3 D4 (16) S4 D5 (32) S5 D6 (64) S6 D7 (128) **S**7 Solenoid 8 D0 (1) S8 Data rane 0 to 254 (even Axis 3 gain numbers) Data irrelevant Learn, feedback axes 0 Set adc multiplexer 0 1 Learn axis 1 2 2 ,, 3 3 ,, 4 4 ,, 5 5 6 EXIP6 7 EXIP5 16 Feedback axis 1 32 2 ,, 48 3 ,, 64 4 80 EXIP7 96 EXIP4 112 EXIP3 Start conversion Data irrelevant ,, ,, ,, ,, EXOP8 A + 23,, **Read Addresses Read Data** A216 Learn adc Data range 0 to 255 (whole (inc. EXIP5,6) range Data range 0 to 255 (whole **A** + 17 Feedback adc inc. EXIP3,4,7) range A + 18 D0 (1) Feedback adc End-of-conversion Test (inc. EXIP1,2) D1 (2) Learn adc D2 (4) EXIP2 D3 (8) EXIP1

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devices is also easily obtained by the host computer, and

programs performing servoing

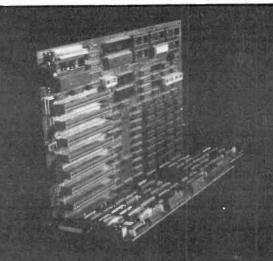
and interacting with peripheral

devices are readily accom-

plished. A large amount of soft-

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**CIRCLE 63 FOR FURTHER DETAILS.** 

# Antennas — more width

The bandwidth of many current receiving antennas for u.h.f. television and v.h.f. radio is barely adequate and can produce problems. The performance towards the limits of the various "groups" tends to fall off, particularly at the high-frequency end, for example, when rainwater collects on the elements. For Band 2 radio, many of the multi-element beams in use to improve stereo reception were designed with an upper limit of 100MHz and are becoming far less efficient as the band extends up to 108MHz. The problem also occurs on some of the older transmitting antennas which, for local radio, were designed for use below 100MHz and are now having to be changed to accommodate the new Geneva Plan 1984 frequencies.

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However, the design of antennas having broader bandwidth is making progress. At the 1985 International Conference on Antennas and Propagation (ICAP85) several papers were published on wideband u.h.f. antennas suitable for television applications. A Polish paper by R.J. Katulski of Gdansk University, Poland, analysed the log, pyramidal periodic (l.p.p.) antenna which comprises two log -periodic structures mounted in diverging planes. His design was claimed as suitable for television reception throughout the range 150 to 900MHz with more gain than a comparable log.-periodic array.

A BBC paper on antennas for outside broadcast links included log.-periodic arrays, parabolic dish reflectors with log.-periodic feeds and a conical logarithmic-spiral antenna that forms a "rove" antenna with circular polarization for mobile links, with all three types suitable for use from 580 to 950MHz. Conical logarithmic-spiral antennas are also attracting increasing interest for communications applications.

Jimmy Wong and Howard King of the Aerospace Corporation have show (*IEEE Trans on Ant. & Prop.*, Vol AP-33, No 8, August 1985) that by using an "open-sleeve" dipole as the feed element, the bandwidth of a Kraus corner reflector antenna can effectively cover 240 to 400MHz, a bandwidth ratio of 1:1.7. Incidentally earlier work by King and Wong was used by James Miller in the design of his helical antennas for 435MHz (*E&WW*, June 1985, pp. 43-6).

A new end-fire radiator antenna, the corrugated surface plane (c.s.p.) system, has been reported by C.A. King and D.A. Downs, based on work carried out at the US Naval Research Laboratory, Washington DC (*Electronics Letters*, Vol 21, No 24, 21 November 1985).

They note that many applications require the use of narrow-beam endfire radiators. A popular microwave approach is the polyrod antenna comprising a dielectric rod fed by a short section of waveguide. However, at u.h.f. such antennas become heavy and cumbersome due to the weight of the dielectric. They have evolved an antenna consisting of a pair of corrugated surfaces mounted back-to-back with a dipole feed element perpendicular to the surface with its mid-point in the plane of the surfaces. Each of the 21-inch corrugated surfaces has 42 slots, 1.5-in wide and 1.5-in deep. The antenna weighs 15lb compared to an estimated 60lb for a comparable polyrod. Measurements at 500, 750 and 1000MHz show half-power beamwidths of  $50^\circ$ .  $31^\circ$  and 25° respectively. Sidelobe levels are 20dB down, considered low in comparison with previously-reported surface-wave antennas.

## Philips embrace MAC

There was a piquant flavour to the 1985 Shoenberg memorial Lecture of the Royal Television Society. Given by C.J. van der Klugt, vicepresident of Philips, it argued very strongly in favour of "evolutionary MAC" and — at least for the 75 per cent of the world with 50Hz mains supplies and 50-field television systems — strongly against acceptance of "revolutionary" 1125-line, 60Hz, h.d.tv systems.

His almost wickedly provocative attack on the widely-promoted NHK/Sony production standard was presented under the urbane chairmanship of recently retired Stuart Sansom who, only a few weeks before, had been arguing equally strongly in favour of the NHK production standard as one of the founder-members of Sony Broadcast.

Mr van der Klugt confirmed that Philips expect to begin marketing first-generation MAC receivers in 1987, though he did not elaborate on whether these would be suitable for D2-MAC or C-MAC or both.

The 60Hz proposals, formulated by Japan with support from the USA and Canada, attracted much opposition at the recent CCIR meeting. It failed to achieve the desired status of a draft recommendation, and remains simply a proposal. With so much opposition from 50Hz countries, it would appear to have little chance of being formally approved as definite recommendation at the pleanary assembly of the CCIR next May. It could be, of course, possible for countries to go ahead without CCIR endorsement.

In his lecture. Mr van der Klugt said: "Philips anticipates a technical solution, concerning the 50/60Hz impasse. If we achieve what we now believe is possible, an umbrella standard for studio production for conversion to both 50Hz and 60Hz, one of the stumbling blocks will be removed. It would seem obvious that major modifications to the Japanese h.d.tv system will be required before it can adequately meet the test of providing the best service for the most people." We have an important not-tobe-missed opportunity to create an orderly videocompatible world . . . but if fear of no decision drives us to making a decision that puts more than half the world at a disadvantage, then we have failed to use the opportunity now available to good advantage."

To obtain agreement on world standards he believes it

essential for people to get together before taking up a firm stand and quotes as a valuable precedent the successful adoption of the Compact Disc standard following early discussions between Philips and Sony.

## Early radio

Dr Geoffrey Phillips resolutely tackled the near-impossible in attempting to cover "the history of sound broadcasting" from Faraday in 1831 to current re-engineering by the BBC of the v.h.f. network for circular polarization, all in the course of a single IEE lecture. nevertheless he succeeded in spotlighting, if only briefly, surprisingly large number of the landmarks in radio broadcasting.

He noted the early work in Belgium in 1914, interrupted by the Great War and then succeeded by the famous Hague Concerts of 1919 to about 1922 to which UK listeners were invited to subscribe and which were also sponsored for a year by the Daily Mail. The Melba broadcast from Chelmsford in 1920 was followed by the procrastinations of the British government, finally resolved by the building of 2LO London, 2ZY Manchester, 5IT Birmingham, the long-wave transmitter at Daventry (5XX) in the era of the British Broadcasting company (1922 -1926), and the old Savoy Hill studios in the IEE building in which he was speaking, with their "meat safe" microphones. Steel tape recording was a feature of the 'thirties, first with the Blattnerphone and later the Marconi-Stille machines.

He acknowledged the early "Empire" broadcasts in 1927 and 1928 by Gerald Marcuse on 31 metres with 1.5 kW, followed by the BBC from G5SW at Chelmsford and finally the official start of BBC external services in 1932.

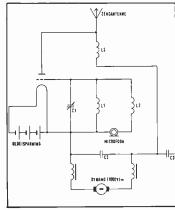
The progression from crystal sets to valve sets with their "reaction" whistles to upset the neighbours gave Dr Phillips the opportunity to stress the advantages for medium-wave reception of the old-fashioned "frame" and modern "ferrite-rod" aerials that can help to reject interference, including local electrical interference. He also highlighted the anti-fading properties of the two-section 725-ft mast radiator on which the old "Third Programme" used to go out from Droitwich.

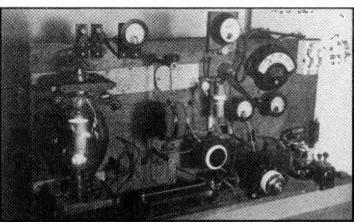
He admitted that the UK had been slow to adopt v.h.f. radio. The classic paper by Howard Armstrong had been published in 1936 but it was not until 1954 that the service began from Wrotham, although this followed exhaustive tests not only of f.m./a.m. but also a.m.l. in which a very broad receiver response permitted the use of very effective noise limiters on pulses that had not been sharpened by a narrow i.f. This system, he considered, came very close to f.m. in performance.

## PCGG

The mention by Dr Phillips of the Hague Concerts deserves to be elaborated upon. These were organized by 35-year-old Hanso Steringa Idzerda after he obtained a licence to transmit music and voice on PCGG in The Hague. His first transmission was on November 6, 1919 after the programme of his "Radio Soiree-Musicale" had been advertised the day before in a Dutch newspaper, and the station remained in service until October, 1924. It preceded KDKA in 1920. Wireless World drew attention to these broadcasts in June 1920 and special concerts for

Simplified circuit of the PCGG transmitter. The carbon microphone in series with coil L2 effects frequency modulation





The original PCGG transmitter as it was displayed in the Nederlands Postmuseum at The Hague, Netherlands. Not visible are the huge aerial loading coil and the rotary machines used for powering the transmitter. PCGG operated on a wavelength of 670 meters.

English listeners were introduced.

In 1917 Idzerda, as manager of Nederlandse Radio-Industrie, had persuaded Philips to begin manufacturing radio valves which they agreed to do if he would agree to buy at least 180 valves per year. So began, early 1918, production of Philips-Ideezet "soft" valves. In the first year he sold 1200 of them. His broadcasts were intended to promote the sale of his receivers. Later it is believed he guarrelled with Philips, though he had initiated their entry into valve manufacture.

The Dutch claim PCGG as the world's first broadcast transmitter, though this tends to be disputed by the Belgians, and there was also Prof. Fessenden's Christmas Eve "broadcast" in 1906.

PCGG actually employed a form of narrowband frequency modulation on 670 metres, being received best with the receiver slightly detuned.

In 1940 the original transmitter was donated to the Dutch Postmuseum where for many years it was regularly demonstrated on dummy load and is still located.

Hanso Idzerda, unhappily, was arrested by the Germans on 3 November 1944, found in possession of radio equipment, (and may have been involved in the clandestine Dutch radio service) and shot without trial at Wassenar.

My thanks to Dick Rollema, PAOSE, for much of this information.

## CH radar

Adding to the excellent series of papers presented last year at the three-day IEE seminar to mark the 50th anniversary of British radar, readers may wish to note several special articles in The GEC Journal of Research (Vol 3, No 2, 1985). These include a detailed account by B.T. Neale "CH the first operational radar' providing technical and operational details of the 25MHz Chain Home with its transmitters built by Metropolitan-Vickers and receivers by A.C. Cossor Ltd to TRE specification.

W.E. Willshaw describes the evolution of the microwave magnetrons to which Eric Megaw of the GEC Research laboratories and Gutton of SFR, Paris contributed significantly. The account by Willshaw is one of the most detailed accounts of this major UK development by Boot and Randall.

## Amateur Radio

## Awards

RSGB awards this year did reflect some genuine experimental work. Roy Jones, G3NKL received the Mullard Award for some careful observations of 10GHz signals over obstructed paths in which he found interesting signal enhancements just after sunset and just before dawn.

Ray Cracknell, G2AHU (former ZE2JV) received the Wortley Talbot trophy and an ARRL award for technical excellence. As one of the pioneers whose detailed observations led to a better understanding of transequatorial propagation, since his return to the UK he has continued his studies of 50MHz propagation. It may be recalled that he had the greatest difficulty in persuading the Radio Regulatory Department to renew his British licence without having to retake the examinations!

There appears to be a good chance that the new 50.0 to 50.5MHz band will be released on a 24-hour basis to UK amateurs about February 1986. It is clear, however, that DTI are undertaking a major revision of the terms of the amateur licence.

A Raynet Trophy was presented to Staffordshire amateurs who had helped organize emergency communications with Mexico after the recent earthquake.

## In brief

Arthur Watts, G6UN, who died recently aged 91 was a World War I member of the Royal Navy Intelligence Department and, as president of the RSGB in 1939, was responsible for the recruitment of several hundred pre-war radio amateurs as Voluntary Interceptors of the Radio Security Service (MI5) working under Lord Sandhurst ... During June 1985 a new world record for a tropospheric contact on 430MHz was established between KH6HME, Hawaii and a station near San Francisco. On 1296MHz KH6HME worked N6CA in Los Angeles. Both distances exceed 4000km over the Pacific path that appears to support v.h.f. and u.h.f. signals at intervals of several years . . . The ARRL has proposed a band plan for 24MHz as follows: 24.89 to 24.92MHz c.w. only. 24.92 to 24.93 c.w./digital. 24.93 to 24.99MHz s.s.b./s.s.tv.

# **Data conversion**

# This supplement to January's data conversion feature contains the first part of a comprehensive list of d-to-a and a-to-d converter i.cs and modules.

Digital-to-analogue converters are given in the first part of the table and analogue-to-digital devices in the second. There's news of a new molybdenum i.c. process on page 29 and an example of converter grounding on page 32.

An extra column is included in the a-to-d converter section to

show the conversion method used. With the aid of last month's glossary, the abbreviations used should be clear.

Speed is given as seconds for conversion. With most of the devices, we have given typical parameters. Where the typical value is unclear we have given a worst-case figure.

Manufacturer	Device	Bits	Speed	Interface	Tech.	Features
AMD	DAC08	8	85n	Par.	Bip.	Mult., 0.1% linearity, differential I o/p
	Am1408	8	250n	Par.	Bip.	Mult., 0.2% linearity, 157mW, I o/p
	Am1508	8	250n	Par.	Bip.	Mult., 0.2% linearity, 157mW I o/p
	Am6012	12	250n	Par.	Bip.	Mult., 4mA diff. o/p, 230mW
	Am6022	12	75n	Par.	Bip.	Mult., 4mA diff. o/p, 500mW
	Am6070	12	300n	Par.	Bip.	Companding for control systems, 72dB
	Am6080	8	160n	Proc.	Bip.	Mult., 0.1% linearity, 5ppm/°C, I o/p
	Am6081	8	200n	Proc.	Bip.	Mult., 0.1% lin., range mpx, diff. I o/p
Analog Devices	AD390	4x12	8μ	Proc.	Hyb.	Quad, ±½1.s.b. linearity
0	AD558	8	lμ	Proc.	Hyb.	2 range, 5V supply, 75mW
	AD561	10	250n	Par.	Bip.	±41.s.b. error
	AD562	12	1.5µ	Par.	Bip.	Mult., ±½l.s.b. error, I o/p
	AD563	12	1.5µ	Par.	Bip.	Mult., $\pm$ 41.s.b. error, I o/p, int. ref.
	AD565	12	250n	_		
		12		Par.	Bip.	225mW, I o/p, internal reference
	AD566		350n	Par.	Bip.	180mW, I o/p
	AD567	12	500n	Proc.	Bip.	500ns to ±½bit, 1% 10V reference
	AD569	16	6μ	Proc.	Bi/mos	Ratiom., 0.02% linearity, V o/p, 150mW
	AD667	12	4μ	Proc.	Bip.	4μs to 0.01%, 1% 10V ref., 300mW
	AD1408	8	250n	Par.	Bip.	Mult., 0.1% linearity, 157mW, I o/p
	AD1508	8	250n	Par.	Bip.	Mult., 0.1% linearity, 157mW, I o/p
	AD3860	12	5μ	Proc.	Нуb.	o/p. amp., ±½l.s.b. linearity, int. ref.
	AD6012	12	250n	Par.	Bip.	Mult., ±½l.s.b. linearity, I o/p, 230mW
	AD7110	6	150kHz	Par.	C-mos	Log. 0 to -88dB, 1.5dB res., 100dB s/n
	AD7111	8	3µ	Proc.	C-mos	Log. 0 to -88dB, 0.375dB res., 5V supply
	AD7115	9/12	5μ	Proc.	C-mos	Log. 0 to -19.9dB, 0.1dB res., b.c.d. i/p
	AD7118	6/17	0.4µ	Par.	C-mos	Log. 0 to -85dB, 1.5dB res., 5V supply
	AD7224	8	7μ	Proc.	C-mos	10V o/p, 2 i/p regs, +ve supply
	AD7225	4x8	5μ	Proc.	C-mos	4 sep. refs, + or ± rails, V 0/p
	AD7226	<b>4x</b> 8	7μ	Proc.	C-mos	4x8bit, positive supply, V o/p
	AD7240	12	550n	Par.	C-mos	±1 l.s.b. error, 30mW, +ve supply, V o/p
	AD7520	10	500n	Par.	C-mos	Mult., 20mW, 2ppm f.s./°C, 5V supp., I o/p
	AD7521	12	500n	Par.	C-mos	Mult., 20mW, 2ppm f.s./°C, 5V supp., I o/p
	AD7522	10	500n	Ser/par.	C-mos	Mult., positive supplies, I o/p
	AD7523	8	100n	Par.	C-mos	Mult., I o/p
	AD7524	8	150n	Proc.	C-mos	Mult., $\pm$ %1.s.b. acc., 5V rail, 10mW, I o/p
	AD7525	12	100Π 1μ	Par.	C-mos	
	AD7528	2x8	350n	_	-	3½-digit b.c.d. i/p pot., ±½1.s.b. lin.
				Proc.	C-mos	Mult., dual latches, I o/ps matched to 1%
	AD7530	10	500n	Par.	C-mos	Mult., I o/p, 20mW, 5 to 15V supply
	AD7531	12	500n	Par.	C-mos	Mult., I o/p, 20mW, 5 to 15V supply
	AD7533	10	600n	Par.	C-mos	Mult., I o/p, 5 to 15V supply
	AD7534	14	1.5µ	Proc.	C-mos	Mult., 8bit bus, 0.5ppm/°C gain co., I o/p
	AD7535	14	1.5µ	Proc.	C-mos	Mult., 0.5ppm/°C gain co., <20nA o/p leak.
	AD7541	12	600n	Par.	C-mos	Mult., I o/p, ±1 l.s.b. gain error
	AD7542	12	2μ	Proc.	C-mos	Mult., 40mW, ±½bit linearity, 4bit bus
	AD7543	12	2μ	Ser.	C-mos	Mult., 40mW, ±½bit linearity
	AD7545	12	2μ	Proc.	C-mos	2ppm/°C gain coeff., 5V supply, 0.5mW
	AD7546	16	4-10µ	Proc.	C-mos	V o/p, 50mW, 16bit bus/latch
	AD7548	12	1.5µ	Proc.	C-mos	8bit bus, mult., 5ppm/°C gain co., +ve supp.
	AD7549	2x12	1. <b>5</b> µ	Proc.	C-mos	Mult., 4bit bus, 3 l.s.b. f.s. error, I o/p
	AD9700	8	10n	Latch	Bip.	125MHz samp. vid. + sync, -5V supp., int. ref.
	AD9702	3x4	5n	Par.	Bip.	RGB channels, 125MHz samp., e.c.l./t.t.l. i/p
	AD9768	8	5n	Par.	Bip.	Vid. 100MHz samp., 20mA o/p, int. ref.
	DAC08	8	85n	Par.	Bip.	Mult., 0.1% linearity, I o/p
	DAC71	16	5μ	Par.	Hyb.	0.003% lin., 7ppm/°C gain co., V or I o/p
	DAC72	16	5μ	Par.	Hyb.	0.003% lin., 5ppm/°C gain co., V or I o/p
	DAC80	12	2µ	Par.	Bip.	±41.8.b. linearity, int. ref., I or V 0/p
	DAC85	12	2μ	Par.	Bip.	±41.s.b. linearity, int. ref., I or V o/p
	DAC87	12	2μ	Par.	Bip.	±1.s.b. linearity, int. ref., I or V o/p
	DACIOO	10	275n	Par.	Bip.	t.t.l./d.t.l. i/p, I o/p, int. ref.
					LILM.	

## DATA CONVERSION \_\_\_\_

DAC1138 18 10μ Par. Hyb. ±½1.s.b. linearity DAC1146 18 6μ Par. Hyb. lppm/°C, high linearity	
DAC1146 18 6µ Par. Hyb. lppm/°C, high linearity	
HDD0810 8 10n Par. Hyb. Vid., 75Ω o/p, sync i/p, int. re	
HDD1015 10 15n Par. Hyb. Vid., $75\Omega \circ p$ , sync i/p, int. re	
HDD1206 12 2μ Par. Hyb. I/p register, 6MHz update, o/r HDD1409 14 5μ Par. Hyb. 200kHz sample rate, o/p buffe	
HDG805 8 8n Par. Hyb. Video, $-5.2V$ supply, sync i/p	
HDG605 6 6n Par. Hyb. Video, -5.2V supply, sync i/p	
HDG405 4 4n Par. Hyb. Video, -5.2V supply, sync i/p	
HDH0802 8 200n Par. Hyb. 10V o/p, video, i/p buffer HDS0820 8 20n Par. Hyb. 10mA o/p, video, i/p buffer	
HDH1005 10 300n Par. Hyb. 10V o/p, video, i/p buffer	
HDH1205 12 500n Par. Hyb. 10V o/p, video, i/p buffer	
HDM1210 12 175n Per Mult., 10mA o/p, 10MHz 3dB an	alogue b.w.
HDS0810 8 10n Par. Hyb5.2V rail, 75Ω o/p, e.c.l. i/p HDS0820 8 20n Par. Hyb. 10mA o/p, video, int. ref., i/p	huff
HDS1015 10 15n Par. Hyb. $-5.2V$ rail, $75\Omega \circ /p$ , e.c.l. i/p	buil.
HDS1025 10 25n Par. Hyb. 10mA o/p, video, e.c.l. i/p, int	. ref.
HDS1240 12 40n Par. Hyb. 16mA o/p, e.c.l. buff., int. ref.	
HDS1250 12 35n Par. Hyb. 10mA o/p, video, e.c.l. i/p, int Analogic AH8304TC 4x3 10n Latch Hyb. 1V RGB o/ps, t.t.l., synchronou	
AH8304TM 4x3 50n Latch Hyb. As TC but with 32 word colour	-
AH8308E 8 7n Latch Hyb. 1V comp. vid. o/p, 5V supply, t	t.t.l. i∕p
AH8308T 8 15.5n Latch Hyb. 1V comp. vid. o/p, 5V supply, t	
AH8308TC 8x3 10n Latch Hyb RGB 1V o/p, t.t.l. i/p, synch. b AH8404TC 4x3 40n Latch Hyb. RGB 1V o/p, t.t.l. i/p, 0.6W.	uenking
AH8404TM 4x3 50n Latch Hyb. As 8404TC but with 32 word m	em., 0.8W
MP1480 12 10µ Proc. Mod. 4-20mA current loop o/p, bin.	
MP1814 14 $\langle 15\mu $ Par. Mod. V o/p 4 rngs, int. ref., $\pm 0.0039$	
MP1913A 13 lμ Latch Mod. I or V o/p, int. ref., ±0.006% f. MP1914A 14 lμ Latch Mod. I or V o/p, int. ref., ±0.003% f.	
MITSIGN 14 $\mu$ Latch Mod. 1 of v o/ p, Int. 161., 20.000 s 1. MP1914TC 14 $\mu$ Latch Mod. As A vers. but lppm/°C stab. r	
MP1915A 15 1.2µ Latch Mod. I or V o/p, int. ref., ±0.0015%	
MP1915TC 15 1.2µ Latch Mod. As A vers. but lppm/°C stab. r	
MP1916A 16 1.5μ Latch Mod. Ι or V o/p, int. ref., ±0.001% f. MP1916TC 16 1.5μ Latch Mod. As A vers. but lppm/°C stab. r	
MITSTORE 10 1.3 $\mu$ Hatch Mod. As A vers. Sut 1991. 0 stab. 1 MP1926A 16 $\langle 3\mu$ Par. Mod. Audio 0.005% h. dist., int. ref.	
MP19265 16 <40µ Par. Mod. Audio 0.005% h. dist., int. ref.	, V o∕p, 0.25W
MP1936 16 6 $\mu$ Par. Mod. Audio -110dB noise, <-86dB f.s.	
MP8116 16 25µ Latch Mod. ±0.25bit lin., 3 V & 2 I o/p rng MP8308ECL 8 10n Latch Mod. Video o/p, sync./blank i/ps, ±	
MP8308TTL 8 25n Latch Mod. Video o/p, sync./blank i/ps, ±	
MP8318ECL 8 10n Latch Mod. Video $o/p$ , $\pm 5V$ supplies	
MP8318TTL 8 25n Latch Mod. Video o/p, ±5V supplies	Antit label - PM
Brooktree BT101* 8x3 20n Par. C-mos 50MHz samp. RGB 19mA o/p, 50 BT102* 8 14n Par. C-mos 75MHz samp. vid., 28mA o/p, 5	
BT103* 4x3 14n Par. C-mos 75MHz samp. RGB 26mA o/p, 50	
BT444* 4x3 25n Par. C-mos 40MHz samp. RGB vid. o/p, 600	
Burr-Brown DAC10HT 12 200n Par. Hyb. Int. ref., -55 to 200°C, ±4 bit 1	
DAC60-12 12 150n Par. Mod. Int. ref., ±½bit linearity, I o/ DAC63 12 35n Par. Hyb. Int. ref., ±30ppm/°C gain drif	p, lubit vers av. t I o/p
DAC70 16 50 $\mu$ Par. Hyb. Int. ref., 1 0/p, ±0.005% f.s. lin	
DAC71/I 16 lµ Par. Hyb. Int. ref., I $o/p$ , ±0.003% f.s. lin	
DAC71/V 16 10 $\mu$ Par. Hyb. Int. ref., V o/p, ±0.003% f.s. li	
DAC72/I 16 l $\mu$ Par. Hyb. Int. ref., ±0.003% f.s. linearity DAC72/V 16 l $0\mu$ Par. Hyb. Int. ref., ±0.003% f.s. linearity	
DAC72/V 16 10µ Par. Hyb. Int. ref., ±0.003% f.s. linearity DAC73 16 50µ Latch Mod. Int. ref., I or V o/p (V=50µs),	
DAC74 16 20 $\mu$ Latch Mod. Self cal., ±0.0015% total error,	, 10 or ±10V o/p
DAC80/I 12 300n Par. Hyb. Int. ref., 2 I o/p rngs, ±0.012%	
DAC80/V 12 3μ Par. Hyb. Int. ref., 5 V o/p rngs, ±0.012% DAC82 8 2.5μ Par. Hyb. Mult., int. ref., 5 V & 2 I o/p r	
DAC85/I 12 300n Par. Hyb. Int. ref., 2 I o/p rngs, ±½bit f	
DAC85/V 12 5µ Par. Hyb. Int.ref., 5 V 0/p rngs, ±½bit 1	f.s. lin. error
DAC90 8 200n Par. Monol. Int. ref., 2 I o/p rngs, ±½bit f	
DAC700 16 350n Par. Monol. Int. ref., -2mA ο/p, <±0.003% f DAC701 16 <8μ Par. Monol. Int. ref., 10V ο/p, <±0.003% f.s	
DAC702 16 350n Par. Monol. Int. ref. $\pm 1$ mA o/p, $\langle \pm 0.003\%$ f.	
DAC703 16 <8µ Par. Monol. Int. ref., ±10V o/p, <±0.003% f.	s. lin. error
DAC706 16 350n Proc. Hyb. Int. ref., $\pm 1$ mA o/p, $\pm 0.003\%$ f	
DAC707 16 <8µ Proc. Hyb. Int. ref., ±10V o/p, <±0.003% f. DAC708 16 350n Proc. Hyb. Int. ref., -2mA o/p, <±0.003% f	
DAC709 16 $\langle 8\mu$ Proc. Hyb. Int. ref., 10V o/p, $\langle \pm 0.003\%$ f.s	
DAC800/I 12 300n Par. Monol. Int. ref., 2 I o/p rngs, ±½bit l	
Data Trans. DT214 12x4 35µ Par. Mod. Mult., quad 10V or 4-20mA o/r DT214H 12x4 8µ Par. Mod. Mult. quad 10V or 4-20mA o/r	
DT214H 12x4 8 $\mu$ Par. Mod. Mult., quad 10V or 4-20mA o/r	
DT215 8x4 35μ Par. Mod. Mult., quad 10V or 4-20mA o/r DT215H 8x4 8μ Par. Mod. Mult., quad 10V or 4-20mA o/r	
DT215H 8x4 8 $\mu$ Par. Mod. Mult., quad 10V or 4-20mA 0/p DT212 12x2 1 $\mu$ Par. Mod. 2 ch. XY point plot, 50mA 0/ps	
Ferranti ZN425 8/6 lµ Par./ck Bip. Int. reference & ramp count.,	V o∕p, 5V supply
ZN426 8/6 lµ Par. Bip. Int. reference, V o/p, 5V supp.	
ZN428 8 800n Proc. Bip. Int. reference, 5V supply, V o/ ZN429 8 lµ Par. Bip. 5V supply, V o/p, 25mW	. Р
ZN429 8 10 1a1. Bip. 57 supply, topp, some ZN434 4 300n Par. Bip. 57 supply, ext. reference or in	nt. 5V/2, V o/p
ZN435 8 800n Par., 'ck Bip. Int. reference, ck & u/d ramp	

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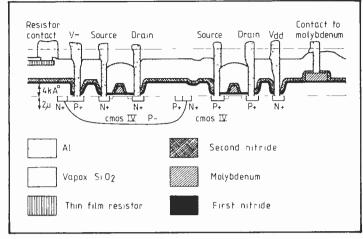
## New molybdenum- gate c-mos technique increases converter speed and accuracy

A newly developed c-mos i.c. process involving the use of molybdenum is now being used to manufacture a highspeed and accurate flash converter which, it is claimed, outperforms much more expensive hybrid devices.

Sampling at up to 2MHz with  $\pm \frac{1}{2}$  bit accuracy, the 11-bit monolithic flash converter costs significantly less than equivalent hybrid products says manufacturer and developer Micropower Systems.

Two-step conversion is used (see subranging converter under 'Converter terms'), one step for the five mostsignificant bits and one for the six least significant. Through use of an 'overflow' bit, two devices can easily be connected in series to give 12bit resolution. Alternatively, two devices can be connected in parallel to give a 4MHz sampling rate.

Refractory molybdenum gate metal with a low resistance of  $0.5\Omega$  per square resulting in very short delays of 0.5ps over  $10\mu$ m at  $3\mu$ m width is the main feature of the patented c-mos process.



This speed compares with 10 to 20ps delays for silicon. Consumption of the device is 150mW.

Not only speed is improved. Using molybdenum also allows more accurate and stable capacitors and resistors to be made on the chip. Dry plasma etching is used for the molybdenum pattern which gives precision matched resistors. Molybdenum with the second layer of aluminium is also used to produce 'auto-zero' capacitors.

Direct ion implantation greatly improves parasitic input capacitance. The process is implemented using what is claimed to be a unique reduced-width molybdenum gate which does not overlap at the edges of the source and drain regions. The metal acts as a mask for ion implantation which brings the source and drain regions back to the edge of the gate as the diagram shows. A non-critical self-aligned



process with low gate overlap capacitance results. Effective gate length of  $2\mu$ m with 0.1pF overlap gate capacitor and less than 1ns gate delay are achieved.

Molybdenum is also used as a first layer metal interconnect to further improve circuit density and speed without other disadvantages, claims Micropower. Typical molybdenum interconnect time delay is less than 0.5ps for each 10µm length.

Moreover the molybdenum, with its low resistivity of  $0.5\Omega$  per square and ability to make good contact, is easily plasma etched to fine tolerances to make the precision resistors needed for quantizing voltage levels in a flash converter.

Second layer interconnect aluminium at  $0.025\Omega$  per square with silicon nitride dielectric between defines a reliable high-value capacitor of  $0.8pF/25\mu m^2$ . This further reduces parasitics associated with the capacitor bottom plate due to its much reduced size and increases dynamic range of the input signal.

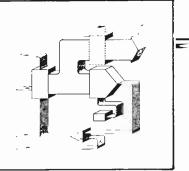
Manufacturer	Device	Bits	Speed	Interface	Tech.	Features
 	ZN436	6	1μ	Par.	Bip.	V o/p, 5V supply
	ZN454	4x3	8n	Par.	Bip.	RGB o/ps, syncs, int. reference, ext. gain set
	ZN558	8	800n	Par.	Bip.	Int. reference, latches, 5V supply, V o/p
Honeywell	CAL24010	12	-	-	-	Calibrator, 1 to 0.001 decade attenuator
(S. P. Tech.)	DAC805	8	5n	Par.	Hyb.	200MHz samp. vid. + sync, int. ref./latch, HDG805 compat
	DAC9700	8	5n	Par.	Bip.	200MHz samp. vid. + sync, int. ref./latch, AD9700 compat
	DAC34010	4x3	5n	Par.	Bip.	200MHz samp. RGB + sync, int. ref./latches, -5V supply
	DAC34020	4x3	10n	Par.	Bip.	100MHz samp. RGB + sync, int. reference/latches
Intech	DAC3400	4x3	15n	Par.	C-mos	40MHz samp. RGB + sync, 5V 250mW, latches
	DAC3405S	4x3	15n	Par.	C-mos	40MHz samp. RGB + sync, int. ram, 5V
	DAC3800	8x3	-	Par.	Hyb.	40MHz samp. RGB + sync, latches, 500mW
	DAC3808	8x3	-	Par.	Hyb.	40MHz samp. RGB + sync, memory, 1.5W
	DAC5150	4x3	13n	Par.	E.c.1.	80MHz samp. RGB + sync, int. reference/ram
	DAC5151	4x3	7n	Par.	E.c.1.	150MHz samp. RGB + sync, int. ref., latches
	DAC1840	8	15n	Com. bin.	C-mos	40MHz samp., vid./sync o/p, latches, -5V supply
	DAC1842	8	15n	Bin.	C-mos	40MHz samp., vid./sync o/p, latches, -5V supply
Intersil	AD7520	10	500n	Par.	C-mos	Mult., 5V 20mW supply, I o/p, 200nA o/p leakage.
	AD7521	12	500n	Par.	C-mos	Mult., 5V 20mW supply, I o/p, 200nA o/p leakage.
	AD7523	8	100n	Par.	C-mos	Mult., 5V supply, 8/9/10bit lin. opts., I o/p
	AD7530	10	500n	Par.	C-mos	Mult., 5V 20mW supply, I o/p, 300nA o/p leakage.
	AD7531	12	500n	Par.	C-mos	Mult., 5V 20mW supply, I o/p, 300nA o/p leakage.
	AD7533	10	600n	Par.	C-mos	Mult., 5V supply, 8/9/10bit lin. opts., I o/p
	AD7541	12	lμ	Par.	C-mos	Mult., 5V supply, 11/12bit lin. vers., I 0/p
	ICL7112	12	500n	Par.	C-mos	Mult., 5V supply, ±0.02/0.01% lin., I 0/p
	ICL7113	3dig.	500n	Par.	C-mos	Mult., 5V supply, b.c.d. i/p, I o/p
	ICL7134	14	3μ	Proc.	C-mos	Mult., + or +/- o/p, preliminary
	ICL7520	10	500n	Par.	C-mos	Mult., 20mW, 8, 9 or 10 bit
Micro Networks	DAC71/I	16	lμ	Par.	Hyb.	-2 or ±lmA o/p, int. ref., ±0.003% f.s. linearity
	DAC71/V	16	10µ	Par.	Hyb.	10 or ±10V o/p, int. ref., ±0.003% f.s. linearity
	DAC80/I	12	300n	Par.	Hyb.	-2 or ±lmA o/p, int. ref., comp. bin./b.c.d. opts
	DAC80/V	12	3μ	Par.	Hyb.	5 V o/p ranges, int. ref., comp. bin./b.c.d. opts
	DAC85/I	12	300n	Par.	Hyb.	-2 or ±1mA o/p, int. ref., ±½ or 4bit lin. opts
	DAC85/V	12	3μ	Par.	Hyb.	5 V o/p ranges, int. ref., ±½ or %bit lin. opts
	DAC87	12	3μ	Par.	Hyb.	5 V o/p ranges, int. ref., <±%bit linearity, 0.9W
	DAC88	12	<10µ	Latch	Hyb.	3 V o/p ranges, int. ref., <±½bit linearity, 0.76W

\* Preliminary data only; these devices do not use R-2R ladder.

## DATA CONVERSION

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Manufacturer	Device	Bits	Speed	Interface	Tech.	Meth.	Features
AMD	Am6108	8	lμ	Proc.	Bip.	-	Int. ref., 0.1% lin., ratiom.
	Am6112	12	7μ	Proc.	Bip.	S.a.	Prog. modes, 15mA ref. o/p, 8bit bus, 1bit lin.
	Am6148	8	lμ	Proc.	Bip.	-	Int. ref., 0.1% lin., ratiom.
Analog Devices	Am6688 AD376	4 8	5n	Par. -	Bip.	Samp.	100MHz rate, 50MHz b.w., e.c.l.
AUGIOR Devices	AD570	8	15μ 25μ	- Par. 3s	Hyb. Bip.		Ref. Ref. & int. ck, 10 or ±5V i/p
	AD571	10	25µ	Par. 3s	Bip.	S.a.	Ref. & int. ck, 10 or ±5V i/p
	AD572	12	<25µ	Ser/par	Hyb.	S.a.	0.012% lin., long term stab., 900mW
	AD573	10	20µ	Proc.	Bip.	S.a.	Ref. & clock, 10 or ±5V i/p
	AD574 AD575	12 10	25µ 700	Proc.	Hyb.	S.a.	12bit bus, 250ns access, 4 range i/p
	AD578	12	30µ 3µ	Ser. Par/ser	Bip. Hyb.	S.a. S.a.	Ref. & int/ext ck, 4 range i/p 0.012% lin., long term stab., 4 range
	AD579	10	1.8µ	Par. 3s	Hyb.	S.a.	0.05% lin., long term stab., 4 range
	AD670	8	10μ	Proc.	Bip.	S.a.	Sig. cond. i/p, 5V supp., int. ref.
	AD673	8	20µ	Proc.	Bip.	S.a.	Ref. & clock, 10 or ±5V i/p
	AD5010 AD5200	6 12	10n 50µ	Par. Ser/par	Bip. Hyb.	Flash S.a.	100MHz, 450mW, e.c.l. o/p, 4bit lin. No adj., 0.4% f.s. abs. acc.
	AD5210	12	1 <b>3</b> µ	Ser/par	Hyb.	S.a.	No adj., 0.1% f.s. abs. acc.
	AD5240	12	5μ	Ser/par	Hyb.	S.a.	0.012% lin., long term stab.
	AD6020	6	20n	Par.	Bip.	Flash	50MHz, 450mW, e.c.l. o/p
	AD7550	13	40m	Proc.	C-mos	Q.slo.	Ratiom., lppm/°C, 'sbit rel. acc.
	AD7552 AD7571	12 10	160m 80µ	Proc. Ser/par	C-mos C-mos	Q.slo. S.a.	Ratiom., 1 l.s.b. error Diff. i/p, int. ck., 10bit + sign
	AD7574	8	15µ	Par. 3s	C-mos	S.a.	Ratiom., 5V supp., diff. i/p, 30mW
	AD7576	8	10μ	Proc.	C-mos	S.a.	15mW, 5V supp., high i/p imp.
	AD7578	12	100µ	Proc.	C-mos	S.a.	8bit bus, auto-zero, 75mW
	AD7581 AD7582	8x8 12	66µ	Proc.	C-mos	S.a.	8-i/p mpx, 64-bit mem., ratiometric
	AD7820	8	100μ 1.4μ	Proc. Proc.	C-mos C-mos	S.a. Subrng	4-i/p mpx, 8bit bus, auto-zero Ratiometric, 5V supp., 75mW
	AD9000	6	13n	Par.	Bip.	Flash	75MHz, e.c.l. o/p
	ADC71	16	45μ	Ser/par	Hyb.	S.a.	45µs for 14bit, 0.003% lin., 850mW
	ADC72	16	45µ	Ser/par	Hyb.	S.a.	45µs for 14bit, 0.003% lin., 850mW
	ADC80 ADC84	12 10	25µ 6µ	Ser/par Ser/par	Hyb. Hyb.	S.a. S.a.	0.012% lin., ref. c/p, 5 i/p ranges 0.048% lin., ref. c/p, 5 i/p ranges
	ADC85	12	10μ	Ser/par	Hyb.	S.a.	0.012% lin., ref. o/p, 5 i/p ranges
	ADC816	10	800n	Ser/par	Hyb.	S.a.	bit lin., 6 i/p ranges, 15V supp.
	ADC1131	14	12μ	Ser/par	-	S.a.	10ppm/°C gain drift, 14bit acc./res.
	ADC1140 ADC1143	16 16	35μ 70μ	Par. Ser/par	Hyb. -	S.a. S.a.	0.003% lin. to 0.003% diff. lin., lppm/°C, 150mW
	CAV0920	9	50n	Par.	P.c.	Subrng	
	CAV1040	10	25n	Par.	P.c.	Subrng	40MHz, e.c.l. o/p, range select
	CAV1210	12	100n	Par.	P.c.	-	10MHz, e.c.l. o/p
	HAS0802 HAS1002	8 10	1.2μ 1.7μ	Par. 3s Par. 3s	Нуb. Нуb.	S.a. S.a.	0.05% f.s. error, low power 0.025% f.s. error, low power
	HAS1201	12	lμ	Par. 3s	Hyb.	-	Track & hold, 1MHz word rate
	HAS1202	12	2.8µ	Par. 3s	Hyb.	S.a.	0.012% f.s. error, low power
	HAS1204	12	2μ	Ser/par	Hyb.		500kHz word rate, 4MHz bandwidth
	HAS1409 MATV811	14 8	8µ 90n	Par. 3s Par.	Hyb. P.c.		Track & hold, 125kHz rate, for f/t.d.m. Small 11MHz vid., 74S o/p, buff. i/p
	MATV816	8	63n	Par.	P.c.		Small 16MHz vid., 74S $o/p$ , buff. $i/p$
	MATV820	8	50n	Par.	P.c.	Flash	Small 20MHz vid., buff. i/p
	MOD1005	10	200n	Par.	P.c.		20MHz analogue b.w., t.t.l. o/p
	MOD1020 MOD1205	10 12	50n 200n	Par. Par.	P.c. P.c.		20MHz word rate, e.c.l. o/p 5MHz word rate, t.t.l. o/p
Analogic	ADAM724	14	6.8µ	Par. 3s	Mod.	S.a.	Int. ref., s-&-h, p.g.a., ±0.003% lin.
	ADAM812	12x2	19.5µ	Par.	Mod.	S.a.	Int. ref., s-&-h, ±0.025% f.s. error
	ADAM822	12x2	39µ	Par.	Mod.	S.a.	Int. ref., s-&-h, ±0.025% f.s. error
	ADAM824 ADAM825	14 15	50μ 59μ	Par. 3s Par. 3s	Mod. Mod.	S.a. S.a.	Abs. acc. $\pm 0.006\%$ f.s., $20$ kHz rate, $0.9W$ As 824 but 18kHz rate, both have s-&-h
	ADAM826-1	16	2.3µ	Par. 3s	Mod.	S.a.	Int. $s-\&-h$ , 10 or ±10V i/p options
	ADAM826-2	16	2µ .	Par. 3s	Mod.	S.a.	Int. s-&-h, 10 or ±10V i/p
	ADAM826-3	16	1.5µ	Par. 3s	Mod.	S.a.	Unbuffered i/p
	ADAM834 ADAM835	14 15	63µ 63µ	Par. 3s Par. 3s	Mod. Mod.	S.a. S.a.	Int. s-&-h, wide temp. rng, 0.9W, 50µV noise As ADAM834 but 15bit, better lin. & temp. co.
	MP2316	16	27m	Pulse	Mod.	2 sl.	Isolated with supply, p.g.a. & reference
	MP2321	3.5d	10m	Par.	Mod.	Int.	Ratiom., isol. b.c.d. o/p, int. supp. & ref.
	MP2322	12	10m	Par.	Mod.	Int.	Ratiom., isol. bin. o/p, int. supp. & ref.
	MP2712	12 13	5μ 100	Par/ser	Mod.	S.a.	Int. ck/ref., 4 i/p ranges, $\pm 0.015\%$ abs. acc. Int. ck/ref., 4 i/p ranges, $\pm 0.009\%$ abs. acc.
	MP2713 MP2714	14	10µ 10µ	Par/ser Par/ser	Mod. Mod.	ຽ. <b>ລ.</b> ຽ.ສ.	Int. $ck/ref.$ , 4 i/p ranges. $\pm 0.007\%$ abs. acc.
	MP2734	14	6.8µ	Par. 3s	Mod.	_	Int. ck/ref., 4 i/p ranges, ±0.007% abs. acc.
	MP2735-1	15	5μ	Par.	Mod.		Audio ±0.005% t.h.d., int. ref., 3dB idle noise
	MP2735-2	15	5µ	Par.	Mod.	Subrng	
	MP8008R MP8014	8 14	3μ 10μ	Par. 3s Par/ser	Mod. Mod.	Samp. S.a.	0.02% differential linearity, ±5V i/p Int. ck/ref., ±0.006% abs. acc., 10 or ±10V i/p
	MP8015	15	15µ	Par/ser	Mod.	S.a.	Int. ck/ref., $\pm 0.006\%$ abs. acc., 10 or $\pm 10V$ i/p
	MP8016	16	32µ	Par/ser	Mod.	S.a.	Int. ck/ref., $\pm 0.003$ % abs. acc., 10 or $\pm 10$ V i/p
	MP8037	17	4ms	Par. Ban(san	Mod.	Subrng	
Burr-Brown	SHAD2A ADC10HT	16x2 12	17.5µ 50µ	Par/ser Par/ser	Mod. Hyb.	3−sl. S.a.	Audio 150kHz samp. sing. ch., -86dB distortion Ck/ref., -55 to 200°C, ±0.012% f.s. lin., 0.25W
Burr-Brown	ADC60-12	12	3.5µ	Par/ser	Mod.	S.a.	Int. ck/ref., 6 i/p ranges, ±0.195% f.s. lin.
	ADC60-10	10	1.88µ	Par/ser	Mod.	S.a.	Int. ck/ref., 6 i/p ranges, ±0.0488% f.s. lin.
	ADC60-8	8	0.88µ	Par/ser	Mod.	S.a.	Int. ck/ref., 6 i/p ranges, ±0.0244% f.s. lin.
	ADC71	16	50µ	Par. Ban	Hyb.	S.a.	Int. ck/ref., 6 i/p ranges, ±0.003% f.s. lin.
	ADC72 ADC73	16 16	50µ ∙170µ	Par: Par/ser	Hyb. Mod.	S.a. S.a.	Int. ck/ref., 6 i/p ranges, ±0.003% f.s. lin. Int. ck/ref., 4 i/p ranges, ±0.00075% f.s. lin.
	ADC76	16	15µ	Par.	Hyb.	5.a. S.a.	Int. ck/ref., 6 i/p ranges, ±0.003% f.s. lin.
	ADC80-10	10	21µ	Par/ser	Hyb.	S.a.	Int. ck/ref., 5 i/p ranges, $\pm 0.048\%$ f.s. lin.
	ADC80-12	12	25μ	Par/ser	Hyb.	S.a.	Int. ck/ref., 5 i/p ranges, ±0.012% f.s. lin.

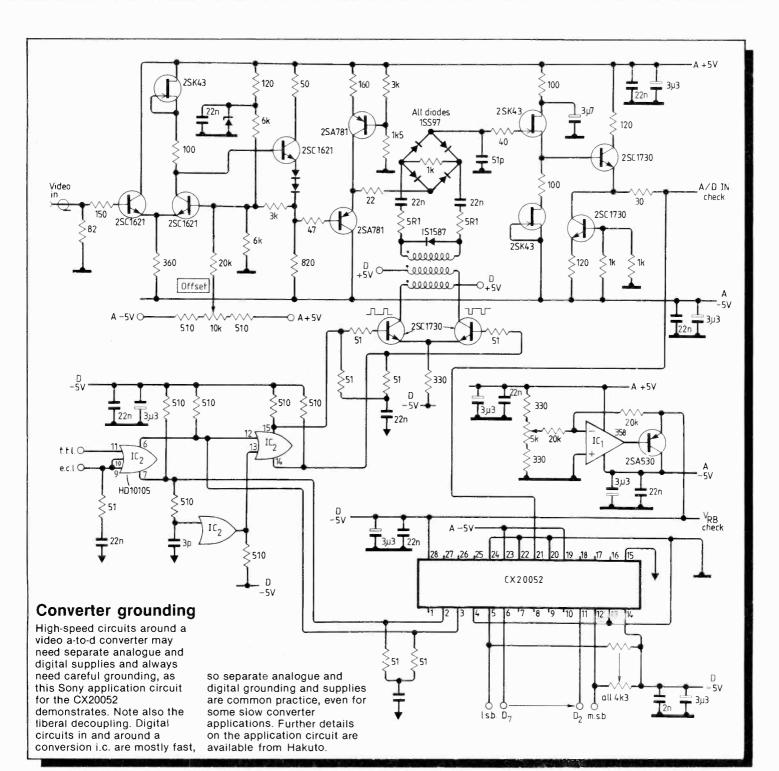


Manufacturer	Device	Bits	Speed	Interface	Tech.	Meth.	Features	
	ADC82	8	2.8µ	Par/ser	Hyb.	S.a.	Int. ck/ref., 6 i/p ranges, ±0.2% f.s. lin.	
	ADC84-10	10	6μ	Par/ser	Hyb.	S.a.	Int. ck/ref., 5 i/p ranges, ±0.048% f.s. lin.	
	ADC84-12	12	10μ	Par/ser	Hyb.	S.a.	Int. ck/ref., 5 i/p ranges, ±0.012% f.s. lin.	
	ADC85		6/10µ	Par/ser	Hyb.	S.a.	As ADC84 10 & 12bit, but better temp. co.	
	ADC100	16	200m	Par/ser	Mod.	Int.	Ck/ref., 0.005% lin., models for bin. & b.c.d. o/p	
	ADC731	16	170µ	Par/ser	Mod.	S.a.	ADC73 with instrumentation amplifier i/p	
	ADC803	12	1.5µ	Par.	Hyb.	5.a.	Int. ck/ref., 3 i/p ranges, ±0.012% f.s. lin.	
	ADC804	12		Ser.	Hyb.	5.a.	±0.012% f.s. lin., <500mW	
			17µ			-		
D-+- <b>m</b>	PCM75	16	17µ	Par.	Hyb.	S.a.	Audio, 0.004% f.s. t.h.d., int. ck/ref.	
Data Trans.**	DT5701	12	19µ	Par.	Mod.	-	16 ch. mpx, 4 ranges, ser. o/p opt.	
	DT5710	12	6µ	Par.	Mod.	-	16 ch. mpx, 4 ranges, ser. o/p opt.	
	DT5703	12	-	Par.	Mod.	-	4 ch. mpx, 10mV to 10V i/p, isolated	
	DT5704	12	10μ	Par.	Mod.	-	4 ch. s/h mpx, 10V i/p	
	DT5712	12	25μ	Par.	Mod.	-	16 ch. mpx, 10mV to 10V i/p	
	DT5714	14	70µ	Par.	Mod.	-	16 ch. mpx, 5mV to 10V i/p	
	DT5716	16	350µ	Par.	Mod.	-	16 ch. mpx, 5mV to 10V i/p	
	DT5722	12	2.5µ	Par.	Mod.	-	16 ch. mpx, opt. p.g.a., 3 range i/p	
	DT5726	16	6µ	Par.	Mod.	-	4 ch. mpx, ±10V i/p	
Ferranti	ZN425	8/6	lm	Par.	Bip.	Ramp	Internal reference & counter, 5V supply	
	ZN427	8	10µ	Proc.	Bip.	S.a.	Internal reference, ratiometric	
	ZN432	10/8	15µ	Par/ser	Bip.	S.a.	Int. ref. & amplifier	
	ZN433	10/8	lμ	Par/ser	Bip.	Track.	Int. ref. & amplifier,	
	ZN435	8	800µ	Par.	Bip.	Ramp	Int. ck, ref. & count., 5V supp.	
	ZN435 ZN439	8	5μ	Proc.	Bip.	S.a.	Int. ref., ck, 4, 4 or lbit lin. options	
		6	60n	Par.	Bip.	Fl <b>as</b> h	$16MHz$ rate, $\pm\frac{1}{2}$ bit lin., t.t.l., 0.7W	
	ZN440 7N447	8/6		Proc.	Bip.	S.a.	Int. clock & ref., ±4bit linearity	
	ZN447		9µ			S.a.	Int. clock & ref., ±% bit linearity	
	ZN448	8/6	9µ	Proc.	Bip. Bip	-	Int. clock & ref., ±1bit linearity	
	ZN449	8/6	9µ 1 B	Proc.	Bip. Bip	S.a.	Int. ref., 1 or 2byte read, ±%bit lin.	
	ZN 501	10	15µ	Proc.	Bip.	S.a.	Int. ref., 1 or 2byte read, ±3bit lin.	
	ZN 502	10	15µ	Proc.	Bip.	S.a.	1110, 101, 101, 200, 001000, -10101111.	
				-	<i>a</i>		Diff FM i (nn FM sunn thhit lin	
Intersil	ADC0801	8	<100µ	Proc.	C-mos	S.a.	Diff. 5V i/pp., 5V supp., ±4 bit lin.	
	ADC0802	8	<100µ	Proc.	C-mos	S.a.	Diff. 5V i/p, 5V supp., ±% bit linearity	
	ADC0803	8	<100µ	Proc.	C-mos	S.a.	Diff. 5V i/p, 5V supp., ±½bit linearity	
	ADC0804	8	<100µ	Proc.	C-mos	S.a.	Diff. 5V i/p, 5V supp., ±1bit linearity	
	ICL7109	12	0.03	Proc.	C-mos	2-sl.	Xtal i/p ck, diff. i/p, 15µV noise	
	8052/7104	14-16	-	Proc./ser.	-	2- <b>s</b> l.	2 i.c., ratiom., int. ck, ref., auto zero	
	8068/7104	14-16	-	Proc. ser.	-	2-sl.	As above but 2 not 1MHz analogue bandwidth	
Micro Networks	ADC80	12	25µ	Par/ser	Hyb.	S.a.	Int. ck/ref., 5 i/p ranges, 10/12bit opts	
	ADC84	12	8μ	Par/ser	Hyb.	S.a.	Int. ck/ref., 5 i/p ranges, 10/12bit opts	
	ADC85	12	8μ	Par/ser	Hyb.	S.a.	As ADC84 but 15 not 30ppm/C gain drift	
	ADC87	12	8μ	Par/ser	Hyb.	S.a.	As ADC85 but for -55 to 125°C	
	MN574A	12	25µ	Proc.	Hyb.	S.a.	8/16bit read, ±0.012% f.s. err. int. ck/ref.	
	MN5065	8	100µ	Par/ser	Hyb.	S.a.	Int. ref., 12V 70mW supp., ±5V i/p	
	MN5066	8	100µ	Par/ser	Hyb.	S.a.	Int. ref., 12V 70mW supp., 10V i/p	
	MN5100	8	1.5µ	Par/ser	Hyb.	S.a.	Int. ref., 9 i/p ranges, ±½ l.s.b. lin.	
	MN 5101	8	900n	Par/ser	Hyb.	S.a.	Int. ref., 9 i/p ranges, ±½ l.s.b. lin.	
	MN5120	8	6u	Par/ser	Hyb.	S.a.	Int. ref., 4 i/p range options	
	MN 5130	8	2.5µ	Par/ser	Hyb.	S.a.	Int. ref., 4 i/p range options	
	MN5140	8	2.5µ	Par/ser	Hyb.	S.a.	As 5130 but ±12V not ±15V supply	
	MN5140 MN5150	8	2.5µ 2.5µ	Proc/ser	Hyb.	S.a.	Int. ref., ±% l.s.b. linearity	
		8 12	2.5μ 50μ	Par/ser	Hyb. Hyb.	S.a. S.a.	0.05% f.s. err., 0.9W, int. ref. opt.	
	MN5200		50μ 13μ				0.05% f.s. err., 0.9W, int. ref. opt.	
	MN5210	12		Par/ser Par/ser	Hyb. Hyb	S.a.	Int. ck & ref., 5 buffered i/p ranges	
	MN5240	12	5μ 2	Par/ser	Hyb.	S.a.		
	MN5243	12	2µ	Par/ser	Hyb	S.a.	Int. ck & ref., 6 i/p ranges, 10/12bit opts	
	MN5244	12	2µ	Par/ser	Hyb	S.a.	Int. ref., 6 i/p ranges, 10/12bit opts	
	MN5245	12	900n	Par.	Hyb.		Int. ref., 0.024% f.s. err., 5V i/p	
	MN5245A	12	900n	Par. 3s	Hyb.		Int. ref., 0.024% f.s. err., 5V i/p	
	MN5246	12	900n	Par.	Hyb.		Int. ref., 0.024% f.s. err., ±2.5V i/p	
	MN5246A	12	900n	Par. 3s	Hyb.		Int. ref., 0.024% f.s. err., ±2.5V i/p	
	MN5247	12	450n	Par. 3s	Hyb.		Int. ref., 12bit monot., 5V i/p	
	MN5248	12	450n	Par. 3s	Hyb.	_	Int. ref., 12bit monot., ±2.5V i/p	
	MN5250	12	175µ	Par/ser	Hyb.	S.a.	0.1% f.s. abs. err., 80mW, 4 i/p range opts	
	MN5260	14	2 <b>5</b> 0µ	Par/ser	Hyb	S.a.	0.05% f.s. err., 300mW, ±10V i/p	
	MN5280	16	100µ	Par/ser	Hyb.	S.a.	0.006% f.s. linearity, 6 i/p rngs, int. ref.	
	MN5282	16	50µ	Par/ser	Hyb.	S.a.	0.006% f.s. linearity, 6 i/p rngs, int. ref.	
	MN5284	16	50µ	Par/ser	Hyb.	S.a.	15bit monot., 7 i/p rngs, 300mW	
	MN5290	16	35µ	Par/ser	Hyb.	S.a.	0.00 <b>3% f.s</b> . lin., 6 i/p ranges. int. ck/ref.	
	MN5291	16	35µ	Par/ser	Hyb.	S.a.	As 5290 but 13 not 14bit mon. over temp. range	
	MN5420	12+4	3.1µ	Par.	Mod.	-	Float. pt., 120dB - 10µV in 10V, 320kHz rate	
	MN 5610	12	13µ	Par/ser	Hyb.	S.a.	1 1.s.b. lin., 915mW, four range options	
	MN5700	12	250µ	Par/ser	Hyb.	S.a.	1% f.s. err. at 200°C, int. ref., 4 i/p rngs	
	MN5815	8	700n	Par/ser	Hyb.	S.a.	Int. ck/ref., 6 rngs, log. sel. + or ± i/p	
	MN5825	8	lµ	Par/ser	Hyb.	S.a.	Int. $ck/ref.$ , 6 rngs, log. sel. + or $\pm i/p$	
				Par5s/sei		-	8 ch. mpx, 75k ch./s, int. ck, ±%bit lin.	
	MN7120	8	7μ 40μ				8 ch. mpx, int. ck/ref., 0.1% f.s. abs. err.	
	MN7140	12	<b>4</b> 0μ	Par.	Hyb. Hyb	S.a.		
	MN7150-8	12	9µ 0u	Proc.	Hyb. Hyb	_	8 ch. mpx diff. high-Z i/p, int. ck/ref.	
	MN7150-16	12	9µ	Proc.	Hyb.	-	16 ch. mpx high-Z i/p, int. ck/ref., 50k ch/s	
							ces and a large	
	number of a	analogu	e inpu	t/output π	odules	for speci	fic computer	
	buses and s							

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MCP Electronics 38 Rosemount Rd Alperton Middx HA0 4PE

The rest of this list of i.cs and modules, together with relevant addresses, will appear next month.

## **CIRCUIT IDEAS**

# New approach to instrumentation amplifier design

This idea deals with two new concepts, leading to a programmable amplifier design.

The first circuit uses theoretical results by Sun, according to which effective resistance  $R_{eff}$  of a resistor and switch in series is approximately proportional to

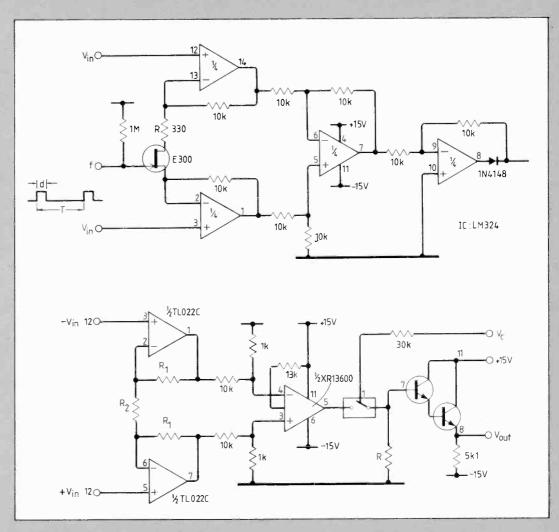
 $(\mathbf{R} + \mathbf{R}_{on})T_s \mathbf{d} = \mathbf{R}_o T_s / \mathbf{d}$ 

where  $R_{\rm on}$  is the on resistance of the switch,  $T_{\rm s}$  is the switching period and d is the on time in a period. Circuit gain A is given by

$$(1+2\frac{R_1}{R_0})\frac{d}{T_1}$$

Gain of this instrumentation amplifier may be programmed by choosing the ratio of  $d/T_s$ , where  $f_s = 1/T_s$ , is the subaudio switching frequency. To obtain a positive output voltage, a fourth op-amp with feedback diode is applied to give the absolute value of the output signal.

Two voltage op-amps at the input of the instrumentation amplifier are used in the second design. The difference amplifier is replaced by a transconductance op-amp with two linearizing diodes. For a transconductance op-amp, between the output current and input voltage  $I_{out} = gV_{in}$  is



the transconductance of the op-amp where g is  $I_c/2V_T$  which is 19.23 $I_c$ ; control current  $I_c \leq 2mA$ . Gain of the instrumentation

amplifier shown in the second figure is given by

Output voltage is

Mains

independent of mains voltage

1N4007

120V

10 mA

100k

10 Ok

· 5V---• 🗋

0·2 ms

variations. Resistors R1.2

 $A = 19.23(1 + 2R_1/R_2)RI_c$ 

Supposing  $(1 + 2R_1/R_2)$  is constant, gain A may be programmed either by resistance R or more simply by control current  $l_c$ .

provide additional safety is in

case the mains transformer primary and secondary short.

José I. Crakovski

470

2N5401

680

4k7

41

120p

MPSA42

**Buenos** Aires

Argentina

K. Kraus Rokycany Czechoslovakia Sun Y. and Frisch 5., Resistance Multiplication by Means of Switching. IEEE transactions on Circuit Theory, Vol. CT-15, Sept. 1968 No.3, pp.184-192.

\* V(

5k6

lk5

MPSA42

12 V

i2k

-: ;-- ) 2ms

25V to

100 V

## Pulses for medical use

We needed a device capabled of producing 30-100V at 50mA from 5V pulses for use as a stimulator for diagnosing nerve palsy. The 5V pulses come from a medical computer. Initially we used a pulse amplifer with a regulated power supply to compensate for fluctuating mains voltages, but this later approach is much simpler.

Transistor  $Tr_1$  is saturated by each positive pulse from the pulse transformer. This transistor's collector current pulses are applied to a shunt regulator, so output-pulse amplitude depends solely on the potentiometer setting.

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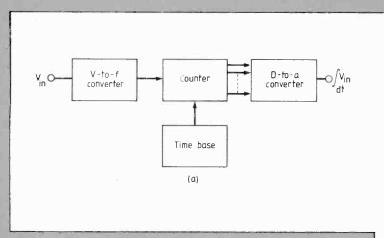
10 J

200V

000

6 1:1

## CIRCUIT IDEAS.





Long time-constant integrators can be built if output of a voltage-to-frequency converter is connected to a counter feeding a d-to-a converter (block diagram). Resulting output is the integral of input because each v-to-f converter may be taken as an integrator. Using a programmable

timer, the design is similar and cheaper(r.h. diagram). Duration of timing cycle T is given as NRC where N is an integer in the range  $1 \le N \le 225$ . When resistor R is replaced by a current source, frequency

10г

RE

1N914

VinO

0 - 10V

C

BC 2520

$$f = V_{in}/NCR_E V_H$$

where  $V_K$  is 3.8V for BC252C. Output is the integral of input voltage with a time constant between 1µs and 5 days. Cascading timers gives extremely long time constants. Kamil Kraus Rokycancy Czechoslovakia

+5V

Vcc

10k

10

20k

10k

OV<sub>out</sub>

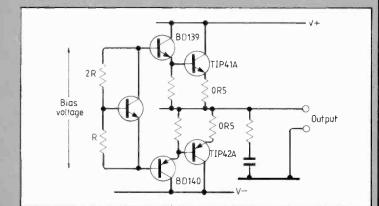
11 470

UA 2240

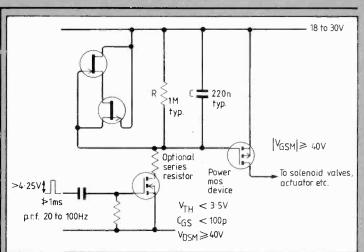
(b)

## Class-B output without adjustment

In this class B output stage, setting up is eliminated by using a transistor and two fixed resistors to provide bias voltage. As shown, the circuit is capable of delivering 20W, but output power can be increased by using Darlington output transistors and 3V bias. At low power, the driver transistors feed the load. These are mounted on the same heat sink as the ouput devices and operated at the highest practical output current by making their emitter resistors as low as possible, at between 4.7 and  $10\Omega$ . Graham Nalty Audio Kits Derby



DON'T WASTE GOOD IDEAS We prefer circuit ideas with neat drawings and widely-spaced typescripts, but we would rather have scribbles on "the back of the envelope" than let good ideas be wasted. Submissions are judged on originality or usefulness — not exluding imaginative modifications to existing circuits so these points should be brought to the fore, preferably in the first sentence.



## Power watchdog

Pulses from a computer port keep this power watchdog circuit activated by holding the p-channel power fet on.

The dual fet network provides negative resistance. When the network is unbiased, current equal to the lower of the two  $I_{DDS}$  can flow, keeping the p-channel device off even in the presence of considerable interference.

Once current supplied by the 2N7000 exceeds zero, the j-fets soon pinch off. If the watchdog pulses fail, the RC network

discharges until voltage across it is less than the combined pinch-off voltage, when the transistor turns off rapidly.

Devices with from 3-7V pinch-off voltage are suitable for this arrangement. Older dmos devices with gate zener diodes or less than 40V drainsource voltage should not be used.

Components R and C are chosen to suit the application. Leakage of the fet is typically less than 1nA in the pinchedoff condition. M.D. Bacon Taunton Somerset

## Burglar alarm

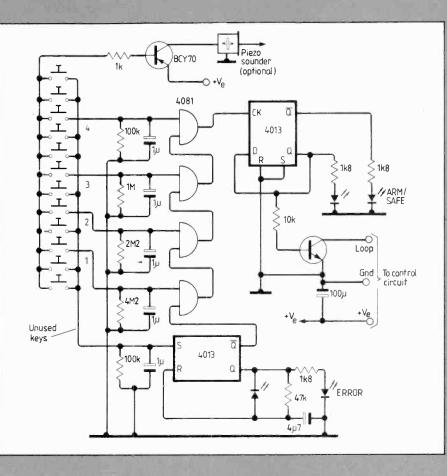
In spite of the simple appearance of this circuit, it has many features — keypad lock/unlock, arm delay, alarm delay, 15min alarm timeout, eight sensor loops and an anti-tamper interlock. There are four mat and four loop sensors.

The keypad is straightforward and may be replaced by a key switch. Heart of the circuit is a 4060 oscillator and divider which provides timing.

In safe mode, the oscillator and bistable devices are reset. On arming, the first 60s cycle elapses and inhibits the clock but enables the sensor latch. When triggering occurs, the clock is enabled and after 60s the alarm is set for a further 7.5, 15 or 30min depending on the circuit setting. After this period, the circuit returns to the initial armed mode ready for the next break-in.

Switching the keypad to safe at any time resets the control circuit. Affected zones are indicated by leds. Use a lead-acid accumulator and not an NiCd battery — it is more reliable. Cutting the keypad wire does not stop the alarm.

Quentin Rice Lower Kingswood Surrey

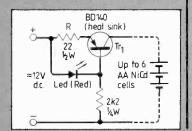


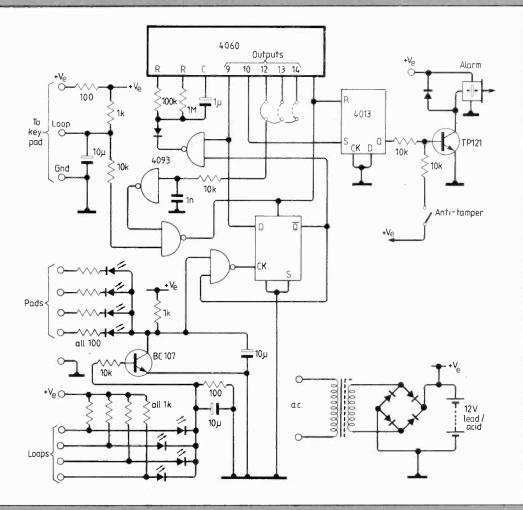
# Simple charger for AA cells

Constant current of 50mA for 14-16 hours is usually needed for AA NiCd cells. Overcharging at the correct current does not harm the batteries so an overnight charge is possible using a constant-current charger.

Up to six cells can be charged at once using this simple circuit. Besides indicating that the cells are properly connected and being charged, the led forms a constant-voltage source. When the cells are not connected, the led is off.

Resistor R determines charge current. V. Mehra Chandigarh India





## Shaft encoder counting

Croft's circuit for shaft encoder generated compared with counting in the November issue is effective in many applications but it can miscount if repeated reversals. occur

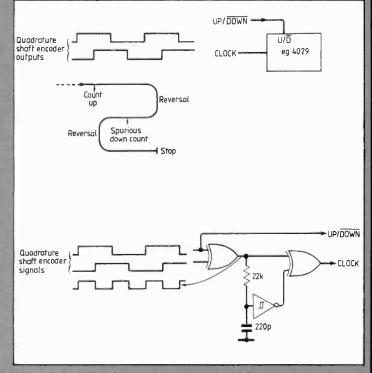
As the first timing diagram shows, if the shaft is repeatedly reversed over a fraction of a cycle it is possible to produce a unidirectional count even though no net motion has occurred. This can happen in practice if there is mechanical bounce on starting or stopping the shaft.

To solve this problem, a onebit longer counter is needed; twice the number of counts are

Croft's system. The first Or gate produces a transition at every transition of either of the quadrature lines.

A brief negative transition is produced by the second Or gate at every transition from the first gate because the signal on one of its inputs is inverted and delayed compared with the other one.

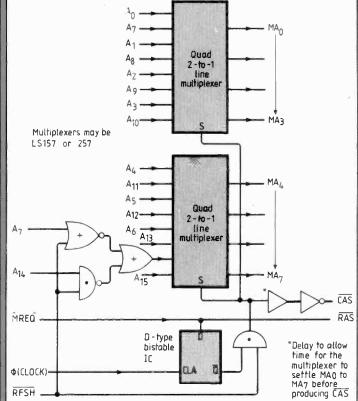
Trailing edges of these pulses clock the counter, after the up/down line has had plenty of time to settle. M. Winder **Reading University** Berkshire



## Analogue voltage multiplier

Suitable for low-frequency applications, this circuit was designed for use in measurement of patients' breathing patterns. Differential inputs A are provided, and A<sub>+</sub> must always be positive with respect to A\_. Input B can be positive or negative, as the 4016 analogue switch is at a virtual earth. The circuit can be rearranged as shown to form a divider.

Remaining i.c. elements were used in sample-and-hold circuits. G.G.R. Rutter London 36



# Multiplexer for 16 or 64K d-rams

In designing a Z80-based computer, I produced this circuit allowing simple switching between 16 and 64K d-rams.

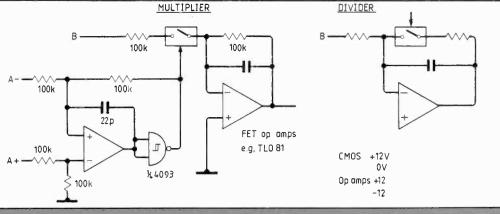
For 16K rams, the usual multiplexed addresses are produced as MA0-6. A further multiplexed address is needed for 64K devices, for which I used the remaining top two bits of the address bus A14. 15 to provide multiplexed addresses for 16 and 64K d-rams during write/read cycles

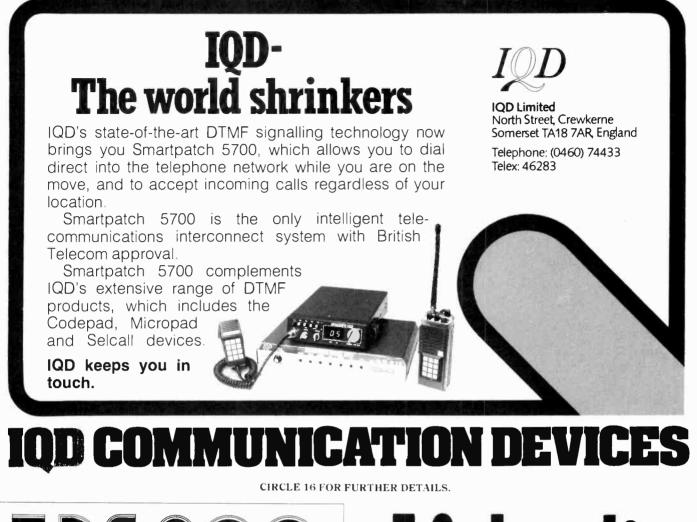
A problem with 64K rams is that during refresh they need  $A_7$  on MA<sub>7</sub>, not  $A_{14}$  as it would be on the system described so far. To overcome this I use the  $\overline{\text{RFSH}}$  signal to gate  $A_7$  to the

multiplexer during refresh, and A<sub>14</sub> during read/write cycles. Row-address strobe RAS is

produced by the MREG memory request signal. Switch signal MUX is provided by the next clock cycle after MREQ goes low provided that RFSH is high, therefore not switching during refresh. Columnaddress stobe  $\overline{CAS}$  is produced from MUX after delay and inversion

Using this circuit and simple wire links on the p.c.b., 16K drams can be exchanged for 64K types using the same sockets and p.c.b. R.J. McClelland Liverpool Merseyside







- \* TDS900 series computers have a full-screen editar
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- \* Execution times in microseconds can be measured
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- They have interrupts, both interrupt and extension
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AN124 2.50 AN214Q 2.60 AN240P 2.80 AN240P 2.80 AN7145 3.50 AN7145 3.50 AN7145 3.50 AN7145 3.50 AN7145 3.50 AN7150 2.95 BA521 3.36 CA352E 1.75 CA3086 0.46 CA3123E 1.50 HA1357 3.50 HA1358 1.50 HA1359 1.50 HA1359 1.56 LA420 1.95 LA420 3.50 LA420 3.55 LA420 3.50 LA420	MC1327Q 0.95 MC134PP 1.20 MC1357 2.35 MC1358 1.58 MC1495 3.00 MC1495 3.00 MC1495 3.00 MC1495 1.25 MC145108P7 MC145108P7 MC1357 2.95 MC357 2.95 MC3357 2.95 MC357 2.95 M	STR025 3.95 STR025 3.95 STR025 3.950 STR078 11.95 STR415 7.95 STR415 7.95 STR437 7.95 STR437 7.95 STR437 7.95 STR4437 7.95 STR4437 1.95 STR4437 1.95 STR4437 1.95 TA71061AP 3.95 TA71020 1.50 TA71202 1.50 TA71202 1.50 TA72024 2.95 TA72024 1.50 TA72024 1.50 TA72024 1.50 TA72024 1.50 TA72024 1.50 TA72024 1.50 TA72024 1.50 TA72024 1.50 TA72024 2.95 TA7203 1.50 TA7203 1.50 TA7200 1.50 TA7200 1.50 TA7200 1.50 TA7200 1.50 TA7200 1.	TBA540 125 TBA5400 1.35 TBA5500 1.35 TBA5500 1.95 TBA5500 1.46 TBA560C 1.46 TBA500 1.46 TBA50 1.46 TBA510 1.05 TBA6118 2.50 TBA6118 2.50 TBA720 2.65 TBA720 2.65 TBA720 2.65 TBA800 0.75 TBA800 1.65 TBA800 1.49 TCA270 1.60 TCA700 1.65 TBA800 1.49 TCA270 1.60 TCA50 1.69 TCA300 1.49 TCA270 1.50 TCA50 1.69 TCA300 1.65 TDA1002 2.50 TDA1002 2.50 TDA1002 2.50 TDA1002 2.50 TDA1002 2.50 TDA100 2.15 TDA100 2.15 TDA100 3.95 TDA1377 1.70 TDA2020 2.95 TDA120 3.95 TDA220 2.95 TDA220 2.95 TDA220 2.85	TDA2530 1.95 TDA2532 1.98 TDA2540 1.95 TDA2540 2.15 TDA2540 2.15 TDA2561 2.15 TDA2571 A.50 TDA2581 2.95 TDA260 6.50 TDA2800 6.50 TDA2800 2.50 TDA2800 2.50 TDA2800 2.50 TDA2800 2.50 TDA2800 2.50 TDA2800 2.50 TDA2800 2.45 TDA350 5.50 UPC5562 2.15 UPC1025H 1.95 UPC158H 0.75 UPC1025H 1.95 UPC1158H 0.75 UPC1158H 0.75 UPC1158H 2.75 UPC1158H 3.75 UPC1158H 3.75 UPC105 2.45 UPC105 2.4	CMEB22W CMEB22GH CMEB22GH CMEB22GH CME122BGH CME122BW CME132GH CME131GH CME132W CME131GH CME202GH CME202GH CME202GH CME3132BW CME312BW CME312BW CME3132GH CME3132GH CME3135W CCME3132GH CV1450 CV1450 CV1450 CV2185 CV2185 CV2185 CV2181 CV2185	19.00 25.00 39.00 39.00 39.00 39.00 45.00 45.00 45.00 45.00 25.00 35	F41-141LG F41-142LC M7-120W M14-100GM M14-100LC M17-151GVR M17-151GVR M19-100W M23-115GR M19-100W M23-112GM M23-112GM M23-112CW M23-112CW M23-112KA M24-120CM M24-120LC M24-120CM M24-120CH	55.00 45.00 45.00 55.00 35.00 35.00 35.00 35.00 75.00 55	M38-121LA M38-12CW M38-140LA M38-140LA M38-344P39 M40-120W M43-12LG/01 M44-120GR M50-120GR M50-120GH M50-120GV M50-120GV M50-120GV M50-120GV M50-120GV M50-120GV M50-120CC M50-1	65.00 65.00 85.00 85.00 85.00 85.00 65.00 65.00 65.00 65.00 65.00 65.00 65.00 65.00 55
SEMICONE	DUCTORS	BC232 0.35 BD233 0.35 BD234 0.35	BFR90 1.50 BFR91 1.75 BFT42 0.35	HCA16334 0.90 RCA16335 0.80 SKE5F 1.45	D14-162GH/84 D14-172GR D14-172GV	59.00 55.00 55.00	M28-12GH M28-13LC M28-13LG	55.00 49.00 49.00	VB004GH VB006GH V8010A VCR139A	65.00 65.00 11.50
AAY12 0.25 AC125 0.20 AC126 0.45 AC126 0.45 AC127 0.20 AC128 0.28 AC121 0.28 AC141 0.28 AC141 0.28 AC141 0.28 AC141 0.28 AC142K 0.32 AC167 0.22 AC176K 0.31 AC187 0.25 AC187K 0.28 AC188 0.25 AC188K 0.35 AC188K 0.35 AC188 0.25 AC188 0.25 AC188 0.25 AC188 0.75 AC187 0.25 AC187	BC178         0.15           BC182         0.10           BC182.0.10         BC183.0.10           BC183.0.09         BC183.0.09           BC184.0.09         BC204           BC104.0.09         BC183.0.09           BC1204.0.09         BC13.0.09           BC2121.0.09         BC214.0.09           BC214.0.09         BC214.0.009           BC214.0.09         BC214.0.09           BC214.0.09         BC214.0.09           BC214.0.09         BC214.0.09           BC214.0.09         BC214.0.09           BC214.0.09         BC214.0.09           BC214.0.09         BC214.0.09           BC214.0.09	BD2336         0.49           BD2337         0.40           BD2338         0.40           BD2420         0.65           BD2442         0.75           BD344         0.85           BD443         0.85           BD443         0.85           BD443         0.65           BD433         0.75           BD434         0.85           BD597         0.95           BD589         0.45           BD597         0.95           BD597         0.95           BD702         1.25           BD702         0.95           BD715         0.35           BF115         0.35           BF119         0.65           BF119         0.65           BF119         0.22           BF177         0.38           BF177         0.28           BF177         0.26           BF177         0.26	BFT43 0.35 BFW22 0.85 BFW22 0.30 BFX84 0.26 BFX85 0.32 BFX85 0.32 BFX86 0.30 BFV88 0.30 BFV88 0.31 BFV80 0.21 BFV50 0.21 BFV50 0.45 BF100 0.45 BF100 0.45 BF100 0.45 BF100 0.45 BF100 1.45 BF106 1.49 BF116 1.20 BF119 3.15 BF105 1.95 BF119 3.15 BF122 1.65 BU102 1.65 BU102 1.65 BU102 1.65 BU122 1.65 BU125 1.65 BU125 1.53 BU125 1.53	TiP225         0.40           TiP226         0.42           TiP30C         0.42           TiP31C         0.42           TiP32C         0.42           TiP32C         0.42           TiP32C         0.42           TiP32C         0.42           TiP34C         0.45           TiP44C         0.45           TiP42C         0.47           TiP42C         0.47           TiP12D         0.66           TiP142         1.75           TiP3055         0.85           TiS91         0.20           TV06/2         1.35           2N22219         0.28           2N3055         0.55           2N3055         0.52           2N3055         0.52           2N3055         0.52           2N3055         0.52           2N3056         0.52           2N3703         0.12           2N3704         0.12	D 14-173GH D 14-173GM D 14-173GM D 14-173GM D 14-181GH/98 D 14-181GM D 14-181GM D 14-181GM D 14-181GM D 14-182GH D 14-200GK D 14-200GK D 14-200GH D 14-200GH D 14-200GH/25 D 14-340CA D 14-340CA D 14-340CA D 14-340CA D 16-100GH/27 D 16-100GH/	65.00 53.00 55.00 65.00 53.00 53.00 53.00 59.00 89.00 85.00 75.00 75.00 75.00 75.00 75.00 45.00 45.00 45.00 65	M 28-13GR M 28-131GR M 28-133GH M 31-101GH M 31-182GR M 31-182GR M 31-182GV M 31-184W M 31-184W M 31-186W M 31-190GH M 31-190GH M 31-190GR M 31-190GR M 31-191GV M 31-270GY M 31-271GW M 31-271GW M 31-271GW M 38-170LG M 38-101GH M 38-101GH M 38-120WA M 38-120WA M 38-121GHR	49.00 55.00 55.00 55.00 53.00 65.00 53.00 85.00 85.00 55.00 55.00 55.00 55.00 55.00 65	1291 38Pi 38Pi 34/08M 34/08M 34/08M 38Pi 58Pi 58Pi 58Pi 58Pi 58Pi 58Pi 58Pi 5	13,50 11,50 11,50 55,00 18,50 30,00 30,00 30,00 30,00 30,00 10,00 13,50 15,00 15
AF124 0.65 AF125 0.35 AF126 0.32 AF127 0.65	BC307B 0.09 BC327 0.10 BC328 0.10 BC337 0.10	8F179 0.34 8F180 0.29 8F181 0.29 8F182 0.29	BU208 1.39 BU208A 1.52 BU208D 1.85 BU3276 1.20	2N3705 0.20 2N3706 0.12 2M3708 0.12 2N3733 9.50	NEW	VIDE	OSPARES	881	HEADS	
AF139         0.40           AF150         0.60           AF178         1.95           AF239         0.42           AF212         3.75           ASY27         0.85           AU105         4.50           AU113         4.50           AC107B         0.11           BC107A         0.10           BC108A         0.10           BC108B         0.12	BC333B 0.09 BC347A 0.13 BC461 0.35 BC478 0.20 BC527 0.20 BC547 0.10 BC548A 0.10 BC548A 0.10 BC557 0.8 BC557 0.8 BC557 0.8 BC557 0.8 BC557 0.80 BC555B 0.10 BC558 0.10 BC733A 1.60 BD115 0.30	BF183         0.29           BF184         0.28           BF195         0.21           BF195         0.11           BF195         0.11           BF195         0.11           BF195         0.11           BF195         0.16           BF197         0.16           BF200         0.40           BF245         0.30           BF258         0.28           BF258         0.28	BU407         1.24           BU508A         1.95           BU508A         1.90           BU526         1.90           BU807         2.25           BU528         1.90           BU801         1.70           MJ3000         1.98           MJE340         0.40           MF5A92         0.30           MRF450A         13.95           MRF453         17.50           MRF455         17.50	2N3773 2.75 2N3792 1.35 2N4442 1.15 2N5294 0.42 2N5296 0.48 2N5296 0.48 2N5496 0.48 2N5496 0.45 2SA715 0.60 2SC495 0.60 2SC495 0.60 2SC495 0.80 2SC1096 0.80 2SC1096 1.52 2SC172 2.20 2SC173 1.15	VIDEO HEAD 3H:SS Suitable for Mo and Ferguson modell 3H:SS(H) Suitable for VT:5000, VT8000, V VT8500, VT8000, V 4H:SS Suitable for mo National Panasonic M 4H:SSLUIN) Suitable f Panasonic Modell 37	ost JVC s£29,50 Hitachi T6000, £33.95 ost Nodels or V0 and	Ferguson 3/16. JVCH HR 3330/3600 JVC HR 3360/3660 Panasonic NV 300 Panasonic NV 2000B. Panasonic 3000B. Panasonic NV 8600B/ Banasonic NV 8600B/ B610B/VQ11.	3.75 1.50 1.50 1.50 1.50 1.00 3.75 3.75 3.75 3.50	Sanyo VTC 5500 Sanyo VTC 9300 Sharp VTC 9300 Sharp VC 9300 Sharp VC 9300 Sharp VC 9300 Sharp VC 9300 Sharp VC 9300 Sony SL 8000/8080 Sony SL 8000/8080 Sony SL 8000/8080 Sony SL 8000/8080	. 3.75 3.75 3.75 3.75 3.75 3.75 3.75 3.75
BC109 0.10 BC109B 0.12 BC109C 0.12 BC114 0.11 BC116A 0.15 BC119 0.24 BC125 0.25 BC139 0.20 BC149 0.24 BC125 0.25 BC140 0.35 BC141 0.25 BC142 0.21 BC147 0.22 BC147 0.12 BC147 0.12 BC147 0.12 BC147 0.12 BC148 0.09 BC148 0.09 BC149 0.09	BD131         0.42           BD132         0.42           BD133         0.40           BD135         0.30           BD136         0.30           BD138         0.32           BD138         0.32           BD138         0.32           BD146         0.32           BD140         0.32           BD150         0.45           BD160         0.55           BD179         0.72           BD182         0.70           BD202         0.65	BF271         0.26           BF273         0.18           BF337         0.29           BF338         0.32           BF355         0.37           BF365         0.36           BF337         0.29           BF355         0.37           BF365         0.32           BF365         0.36           BF427         0.25           BF427         0.32           BF425         0.36           BF425         0.36           BF425         0.36           BF494         0.45           BF494         0.45           BF495         0.23           BF495         0.23	MRF475         2.95           MRF475         10.00           MRF477         10.00           MRF478         10.00           MRF477         10.00           MRF478         10.00           MRF479         10.00	$\begin{array}{c} 2S(1306 & 1.40\\ 2S(1307 & 1.75\\ 2S(1364 & 0.50\\ 2S(1467 & 0.80\\ 2S(1467 & 0.80\\ 2S(1467 & 0.80\\ 2S(1954 & 0.85\\ 2S(1954 & 0.85\\ 2S(1954 & 0.85\\ 2S(1956 & 1.85\\ 2S(1956 & 1.95\\ 2S(2025 & 1.95\\ 2S(205 & 1.95\\ 1.95\\ 2S(205 & 1.95\\ 1.95\\ 2S(205 & 1.95\\ 1$	380. BETAMIX VIDEO PS3B 11 Pini Suitable and Toshiba 5000 Se NEC PV2400. RSV-3-8 Suitable for SLE000 SL8060 SLD DSR-10-R Suitable fo Sorry SLCS SLCG SLC Sonyo Head for VTC9300/9500. Senyo Head for VTC 5300/5000	£33.95 HEADS for Sony orles and £39.50 Sony 77ME £39.50 C7 £39.50 £41.50 £41.50	4-529-108008 VTC 4-5271-23501 VAR 4-527V-51000 VAR 4-527V-51000 FVH 143-0-490400900 FVH 143-0-545F-01701 VAR 143-0-545F-01701 VAR 143-0-545F-01700 VAR 143-0-661T-03800 VAR 143-0-661T-03800 VAR 143-0-662F-01201 VTC 143-0-9974-00100 FVH	ODEL 5150 10US 5150 19455 10US 10US 10US 5150 19615	DESCRIPTION Reel Motor 3.6W Motor Assy. Capaton Motor Gaer Idler Assy. Stopper Reel Base Pinch Rolier Assy. Jinch Rolier Assy. Idler Assy. Loading Roller Reel Drive Pulley Mod Kit IC BA6304A	9.95 9.75 29.95 0.55 8.95 1.95 0.95 0.95 0.95 8.50 8.50 1.95
BC158 0.09 BC159 0.09 BC174 0.09 BC174A 0.09 BC174A 0.09	BD203 0.78 BD204 0.70 BD222 0.46 BD223 0.59 BD225 0.48	BFR39 0.23 BFR40 0.23 BFR81 0.25 BFR88 0.30	R2010B 1.45 R2322 0.58 R2323 0.66 R2540 2.48	2SD234 0.50 3N211 2.95 3SK88 0.95 3SK88 0.95	Video Head Aer	osol Cleane	(VHS automatic wet/d r. onnector Kit 'ZV'	0.85	S 2P Colour Bars 30 min. S 3P Stairsteps 30 min.	49.50 49.50
BC177 0.15	BD225 0.48 BY210-8000.33	IN4001 0.04	LINE OUTPUT T	RANSFORMERS	E H T MULTIP	PLIERS	VARICAP TUNER	RS	PUSH BUTTON	UNITS
AA119         0.80           BA115         0.13           BA145         0.16           BA148         0.17           BA154         0.06           BA156         0.15           BA157         0.30	8Y223 0.90 8Y298-400 0.22 8Y299-8000.22 8YX10 0.20 BYX36-150R 0.20 8YX38-600R 0.60	IN4003 0.04 IN4004 0.05 IN4005 0.05 IN4007 0.06 IN4148 0.02 IN4448 0.10 IN5401 0.12 IN5402 0.14		7.95 9.95 8.25 8.26 15.45 , 2222, 5011-6011 13.45	ITT CVC30 PHILIPS G8 550 RANK T20A THORN 3000/3500 THORN 8500 THORN 9000	6.35 6.35 6.96 6.91 7.57 8.00 8.00	ELC1043/05 MULLARD ELC1043/06 MULLARD U321 U322 U324 THERMISTORS	8.65 8.65 8.25 8.25 11.00	DECCA, ITT, CVC20 6WA ITT CVC5 7-WAY PHILIPS G8 (550) 8-WAY 20MM QUICK BLO	10.19 14.49 W FUSES 8p each
BAX13 0.04 BAX16 0.06 BB105B 0.30 BT151 0.79	BXY55-6000.30 BYX71-6001.10 BZY95C30 0.35 CS4B 4.50	IN5403 0.12 IN5406 0.13 IN5407 0.16 IN5408 0.18	ITT CVC20 ITT CVC30 PHILIPS G&	8,20 8,25 8,50 8,99	UNIVERSAL TRIPLER		VA1040 VA1056S VA1104	0.23	200MA-5AMP	12p each GE FUSES
BY126 0.10 BY127 0.11 BY133 0.15	CS10B 8.45 OA47 0.09 OA90 0.05	ITT44 0.04 ITT923 0.15 ITT2002 0.10	PHILIPS G9 PHILIPS G11 PYE 725 RBM T20A	8.99 13.99 10.95 12.40			VA1104 VA8650 VA1097	0.70 0.45 0.25	100MA-800MA 1A - 5AMP	15p each 12p each
BY164 0.45 BY166 1.20 BY176 0.63	0A91 0.06 0A95 0.06 0A202 0.10	ZENER	TANDBERGE 90" TELEFUNKEN 711A THORN 1590	12.40 11.15 11.15 9.50		05V) 2.85 05V) 2.99 3.55	;	SPARE	& AIDS	
8T182 0.55 BY184 0.35 BY199 0.40 BY206 0.14 BY208-B000.33	IN21DR 5.00 IN23B 5.00 IN23C 5.00 IN23ER 5.00 IN23WE 5.00	DIODES BZX61Series 0.15 BZY88Series 0.10	THORN 9500 THORN 9000 THORN 9800 THORN 9800 THORN MAIN TRANSF 3000/3500	23.50 9.95 22.40	GEC 2110 (600/300V) ITT CVC2D (200/400V) PHIIPS G (600/300V) PHILIPS G9 (2200 × 63V	2.25 1.80 2.25	HEAT SINK COMPOUND FREEZE IT SOLDA MOP SWITCH CLEANER WD40	1.00 0.95 0.64 0.85 1.75	PUSH PULL MAINS SWI (DECCA, GFC, RANK, T) ETC.) PYE IF GAIN MODULE ANODE CAP (27kV)	TCH HORN 1.02 6.99 0.69

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CIRCLE 27 FOR FURTHER DETAILS.

## ELECTRONICS & WIRELESS WORLD FEBRUARY 1986

A SELECTION FROM OUR	M8225 3.95 ME1401 29.50	QS108/45 4.00 QS150/15 6.95	UF85 1.20 UF89 2.50	3A/110B 12:00 3A/141K 11.50	68M8 0.58 68M6 155.00	6L19 3.95 6L6GC 2.95	13E1 145 00 13EM7 3.50	955 1.00 958A 1.00
STOCK OF BRANDED VALVES           A174         24.50         EPR0         0.95         EM22         12.95           A1398         11.50         EPR8         0.95         EM4         9.00           A2087	ME 1402 29.50           ME 1402 29.50           ME 1402 29.50           ME 1501 14.00           MH4 3.50           MH4 3.50           MH4 3.50           MH4 3.50           MV1 3.50           MV1 3.50           MV1 3.50           MV3 8           MV3 8           MV3 7 12.50           N78 9 85           OA2 0.85           OA2 0.85           OB2 0.95           OB3 0.05           DP4 12.50           DM4 1.00           OM5 0.355           P61 2.50           P41 2.50           PA8C80 0.50           PC88 0.75           PC28 0.50           PC37 1.10           PC680 0.75           PC280 0.75           PC280 0.70           PC280 0.70           PC280 0.80           PC280 0.80           PC280 0.80           PC280 0.80           PC	OSI 50/30         6.5           OSI 50/30         6.5           OSI 50/30         3.95           OSI 50/30         3.95           OSI 2020         3.95           OSI 2110         1.90           OSI 2111         1.90           OSI 2111         5.00           OSI 2115         5.00           OVG 212         4.50           OVG 22         9.50           OVA-12         4.50           R116         1.50           R12         5.00           R12         5.00           R12         5.00           R12         5.00           R12         2.50           R12         5.00           R12         5.00           R12         5.00           R	UL44         3.50           UL85         0.85           UL95         0.80           UL95         0.80           UL9         3.50           UL9         250.00           V235A/1K         250.00           V235A/1K         225.00           V235A/1K         225.00           V235A/1K         225.00           V235A/1K         225.00           V241C/1K         195.00           V333 350         V4453           V4533         10.95           VP133         2.00           V15631         10.95           VP133         2.00           V1563         10.95           VP133         2.00           V10503         1.05           V212         4.50           V225         4.50           V21         4.50           V21         4.50           XC24         1.50           XC24         1.50           XC24         1.50           XC25         0.50           XC24         1.50           XC25         0.50           XC25         0.50           XC24         <	3A:147J         7.50           3A:167         10.00           3A:167         10.00           3A:167         10.00           3A:167         10.00           3A:167         10.00           3A:167         10.00           3A:167         3.95           3A:12         3.35           3B:2         3.00           3B:2         3.00           3B:2         12.00           3B:2         12.00           3B:2         12.00           3C:2         19.00           3C:4         10           3C:3         150           3C:4         10.00           3C:4         10.00           3C:4         10.00           3C:4         10.00           3C:5         150           3C:6         150           3C:6         150           3C:6         150           3C:6         150           3C:6         150           3C:6         150           3C:7         150           3C:7         150           3C:7         150           3C:7         150	68N4         1.65           68N4         1.65           68N7         4.50           6805         1.65           6807         3.95           68025         0.75           68025         0.75           68026         0.75           68027         0.72           6817         1.50           6887         4.95           6887         4.95           6887         1.50           6887         1.50           6887         1.50           6887         1.50           6887         1.50           6887         1.50           6887         1.50           6887         1.50           6887         1.50           6887         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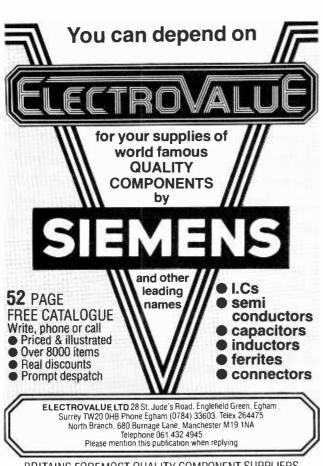
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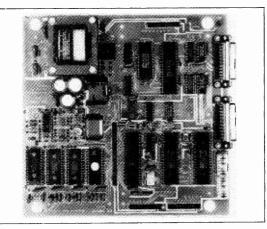


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by G. Wareham

Fig. 1 (a) shows the one

or two-turn primary on

recommended capacitor

position. At (c) is the

ferrite rod and, at (b), the

balun to match aerial and

downlead to receiver and

(d) shows the method of

# Short-wave loop aerial

# Modifications to the earlier designs enable the aerial to be used with domestic receivers

In April 1985, S. Mukherjee described a useful indoor short-wave aerial for use with well-screened receivers. His design is not very suited to ordinary domestic receivers because their lack of screening allows signals to bypass the loop. The result is loss of directionality and, at the same time, trouble from local interference of the kind which a magnetic aerial rejects.

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Some relatively simple changes can avoid these problems. The first requirement is to provide the receiver with some sort of screening enclosure to exclude direct pickup. A complete screening box is impracticable since it would prevent access to the controls. Experiment shows that an open-fronted box will work, provided that it is deep enough for the receiver to be pushed well inside.

My tests indicate that a cardboard box covered with aluminium kitchen foil makes an adequate short-wave screen. On long and medium waves the amount of screening provided by the thin foil is reduced, enabling the receiver's ferrite aerial to function on these bands.

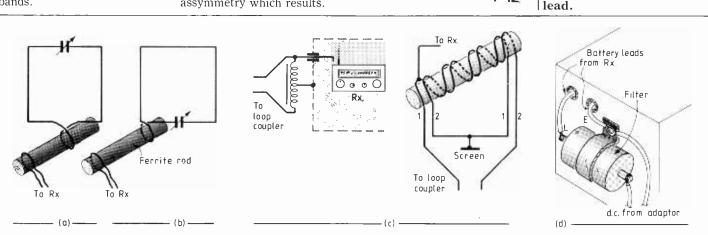
Having eliminated direct pickup of short-wave signals, the next job is to deal with pickup by the downlead. This is done in two steps. First, an unscreened, unearthed loop is substituted for the original design. Any thick, insulated wire (such as mains flex) can be used for the loop, the signals being extracted via a transformer. The primary is formed by passing one or two turns of the loop conductor round a ferrite rod (a). (An aerial rod from an old m.w. receiver is adequate.) Coupling to the unscreened twin downlead is effected by a secondary winding: two or three turns will generally be optimum, but the user can easily experiment with different numbers. For reasons explained below it is useful to make the secondary detachable.

Although considerations of symmetry suggest an arrangement like (a), where the loop can float above earth in a balanced fashion, practical use is eased by putting the tuning capacitor at the bottom (b) where it is easily reached. I have not noticed any impairment in performance from the assymmetry which results.

The aerial and downlead are balanced, but the receiver input circuit is not. A balun is needed, and this can take the form of a centre-tapped autotransformer (c), made with a bifilar winding of hookup wire on a magnetic core. Possible core materials are pieces of ferrite aerial rod, ferrite toroids, and tuning slugs of the throughhole type, which can be used as toroids. In general, the number of turns needed is 5-10, connected as shown. The balun is placed either just outside or just inside the screening box, with its centre tap connected to the foil by a short lead (fold its end into the foil or staple it to the foil). Coupling to the receiver is by wrapping a few turns of insulated wire round the end of the built-in telescopic aerial. (I find that this is still the best method, even in a receiver which also has proper aerial and earth terminals. It is not necessary to earth the screening box except when power supply considerations require it as explained later.

Check for downlead pickup by slipping the secondary coil off the transformer rod. Signals

Signalsavoiding signal injectionP42from the power-supply



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## continued from p46

should vanish or at least become very noisy.

If a receiver is mains operated, the mains lead brings unwanted signals into the screening box. A power-supply filter is then required. My own receiver is a battery-powered one which can be mains-driven via an external adapter (transformer-rectifier unit) which supplies the required low-voltage d.c. via a long lead. In this case, the appropriate side of the d.c. supply is connected to the screening foil and the 'live' side taken to the receiver via a three-terminal capacitor-type filter (d). A lead from the earthy battery terminal of the receiver is connected to the foil to complete the circuit. A conventional pi-section LC filter can also be used: I had good results from a home-made filter where the series L was a tv frame coil on a ferrite ring core and the Cs were 100nF polyester film capacitors. Whatever arrangement is used it is essential to keep the connections between capacitors and screening foil very short - a centimetre or less. If longer, their inductance impairs filtering. The case of my feed-through filter is the earth terminal and contact with the foil is made by bolting the filter unit to the screening box. The earth connections are made by trapping the bared ends of the earthy d.c. leads between filter and foil.

I have not so far attempted a filter for a receiver with a builtin mains power unit. A balanced filter would presumably be needed, with the earth line connected to the foil and to mains earth. Safety considerations suggest that the screening box should itself be enclosed in an insulating box to avoid contact with the foil.

If the receiver can be battery operated it is useful, when testing power-supply filters, to set up the receiver with the filter in situ but the mains power off. If the receiver is now battery operated it can be seen if signals are getting into the box via the filter.

When operating a wellscreened receiver, bringing one's hand to the controls introduces stray signals. This can be an advantage since it allows the receiver to be pretuned to the required frequency before tuning in the loop.

# | INDOOR LOOP | AERIAL

If you happen to live near a powerful radio transmitter you will find that short-wave reception is marred by breakthrough by the local transmission. This can occur even when the wanted station is far removed in frequency from the local transmission, through such mechanisms as intermodulation.

In principle, the directional properties of a loop aerial can be utilized to null the local transmission but, in practice, a simple loop oriented in the vertical plane does not give a complete null. The reason is that the ground wave from the local transmitter is not quite vertically polarized but tilted by wave drag.

The loop null can be improved by tilting the loop away from the vertical to match the interference. Readers who suffer from localstation interference might find it useful to construct short-wave loops in a way which permits some adjustments of tilt. For most cases a tilt range of 0-30° will be sufficient.

R. Jones Dorchester Dorset

# FFT

Your contributors Larsen and Dyrik express the hope that their article will inspire readers to experiement with machine code f.f.ts and spectral analysis.

We took this path some years ago and can report that indeed many happy hours can be spent optimising an implementation of the f.f.t. algorithm, and one dicovers many subtle issues that do not seem to be covered in the standard literature.

Starting from the Basic program given, the main decision to be made in preparing a machine-code version is how to represent the data arrays D and E. If floating point is used, execution will be little faster than in Basic. With fixed point, however, optimizing the dynamic range is a non-trivial problem, since the data values grow in a way which cannot reasonably be predicted at the outset. Automatic rescaling is thus usual and desirable, but this raises yet another level of subtlety if the rounding or truncation inherent in a rescaling is not to contribute further errors. Welch<sup>1</sup>considers three strategies, none of which is optimal.

For those who are more interested in the finished article than the joys and frustrations of development, we have made our BBC spectrum analyser commercially available through Structured Softare, and will be pleased to supply full details on request. P.G. Craven J.C. Davies Bromborough Wirral Merseyside

### Reference

1. Welch, P.D., A Fixed-Point Fast Fourier Transform Error Analysis. *IEEE Trans Audio & Electroacoustics* AU-17, 2, 151-157 (JUne 1969).

## ADD-ON CURRENT DUMPER

I was very interested to read Erik Margan's add-on circuit to convert a conventional Class B amplifier output into a non-switching type. I have, in the past, tried to develop such a circuit, but have never been fully satisfied with my efforts. Mr Margan's circuit appear to be a very competent solution.

Mr Margan gives no clue regarding the value of  $R_e$ . However, analysis of the circuit indicates that if  $R_c$  is less than about one fifth of the value of  $R_e$ , the circuit current limiting will come into operation before  $Tr_3$  or  $Tr_4$  are turned off.

I would be very interested to know of the effect of Mr Margan's modifications on the distortion figures of a typical Class B amplifier and the extent to which an improvement in sound quality may be heard. Graham Nalty Borrowash Derby

# ENERGY TRANSFER

P. L. Taylor (Wireless World, p. 15 October 1985) likes the choice between e.m. wave energy transfer through space, as required by the Poynting vector, or through wires, as required by the Slepian vector. There is, in fact, a third choice advocated by Cambridge Professor G. H. Livens. Writing 'On the flux of energy in radiation fields' at p. 313 of his 1926 book 'The Theory of Electricity', published by Cambridge University Press, he argued in favour of an alternative to Poynting's theory. Waves do not need to carry energy at their speed of propagation. Their generation merely adds energy to a common pool of energy in the field medium at the locality of the transmitter and their absorption draws on that pool in the locality of the receiver. I like this third alternative, because it is easier for me to picture

creation a a big splash in an existing smooth pool of energy than as a big bang appearing from nowhere in a complete void. J.N. Kidman Southampton

## NAVAL MARCONI

In the November issue you had an article by Nigel Cawthorne, which I am happy to say was complimentary to Marconi in most respects, but did rather belittle the Marconi Fast Tune (MFT) range of equipment. I would like to point out that in addition to the drive and 10kW amplifiers we also have a 50kW i.s.b. amplifier, a 1kW amplifier and a transceiver based on the drive/receiver in development. But by far the most important part of MFT2 is the remote computer control system that goes along with the basic hardware; this system allows for automation in service selection which includes frequency, mode, audio source and antenna selection, along with any other aspect that needs controlling.

I would also take this opportunity to point out that our new Swordfish transceiver is not, as depicted in the picture caption Redifon's R800 v.l.f. receiver. P.A.T. Turrall, Publicity Manager, Marconi Communication Systems, Chelmsford

# RELATIVELY BORING

H. Morgan complains in your July issue that relativity is boring. This view will be shared by many physicists, but they do not seem to have noticed that one cause of the confusions and contradictions is that it is not a scientific theory. This Einstein made quite clear when he said that the "real basis of the special relativity theory" was the Lorentz equations (Bull. Amer. Math. Soc., 41, 1935). Physics is based on matter, motion and force: equations in physics are only relations between terms that represent numbers obtained by measurement; and these are put into Nature by ourselves. They can never tell us, just as Einstein could not, whether an ether was a necessary physical assumption or not: at one time he had said not; and then later "space without ether is unthinkable". Moreover, Lorentz's times were dates and these do not enter into physical laws.

Many other confusions are caused by lack of linguistic care. If we think carefully about light, for example, we realise that we never

see light, or anything moving, and that beams and rays cannot be found in highly evacuated regions, so that reflection may be reradiation. One fact about light has, however, been verified sufficiently to give us considerable confidence in its truth; this that if we have a source and a receiver at a fixed distance from one another on a rigid body, the time delay of interaction between them is always d/c where c is a universal constant. I have suggested that c should be called the constant of interaction: no hypothetical element is then involved (such as ether, waves, photons and the rest) and action at a distance is not ruled out (as Maxwell admitted long ago). This one fact allows us to settle the thought experiment involving a train passing an embankment, which relativists use to 'prove' the supposed relatively of simultaneity.

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The train and the embankment are said to be struck by lighting at two places a distance apart. An observer on the embankment, halfway between the two flashes, is said to find them both simultaneous, but the observer on the train is said not to agree. In this case there are two rigid bodies — the embankment and the train and both observers are at a distance d/2 from both flashes. The time for interaction is thus the same for both flashes so that both observers should find simultaneity. If the sources are said to be moving with respect to the observer on the train, this should cause no difference, since the velocity of light' is held to be independent of the motion of the source.

As length-contraction is 'proved' in thought experiments by founding it in the supposed relativity of simultaneity, is clear that length contraction is also delusory.

A note of interest is a recent statement from a colleague of Einstein's who says that in his later years Einstein abandoned material consequences implied by relativity, which included his thought experiment purporting to show that people can get younger by rushing about — the Twins Paradox (Foundations of Physics, 15, 9, 1985). G. Burniston Brown Padstow Cornwall

# ELECTROLYTICS AND DISTORTION

Since both Mr Curl and Mr Armstrong (Letters, November 1985) are writing on the same topic, it seems logical to try to answer their complaints in the same letter.

ELECTRONICS & WIRELESS WORLD FEBRUARY 1986

I hoped that by now I had made it clear that I am perfectly well aware that it is possible to use electrolytic capacitors in such a way as to generate disturbances in an applied audio signal; the point I wish to make is that there is nothing new or mysterious about this. The effect (low-frequency harmonic distortion when an electrolytic is allowed to cyclically depolarize) is logical and predictable, and therefore avoidable. It should not arise in a properly-executed audio design.

To recapitulate, the important point is simply to ensure that there is no significant a.c. voltage across the capacitor in question. When an electrolytic is used as a coupling or dc-blocking component there is no reason why there should be; if there is then you have, accidentally or otherwise, made a high-pass filter of dubious accuracy due to the wide tolerance of electrolytics. The a.c. voltage across the capacitor can then give rise to unpleasant effects, of which depolarization is probably the worst, as it has depressing implications for the longevity of the component.

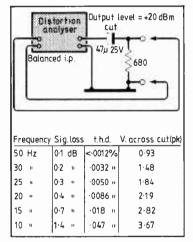
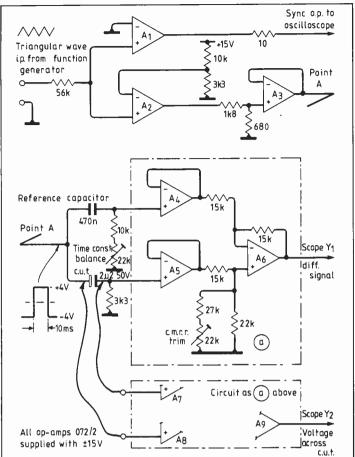


Figure 1 shows my own, simpler, method of demonstrating that capacitors generate distortion when misused. This is a simple high-pass filter; below it is shown a table of the distortion produced, against input frequency.

Harmonic distortion is unmeasurable from 20kHz down to 30Hz, where things suddenly start to go wrong. Under these conditions, it is at this point that the peak voltage across the capacitor reaches 1.4V. This seems to be the threshold at which the capacitor dielectric film starts to come undone, though dielectric absorption effects could be playing a part. Perhaps a capacitor manufacturer would like to contribute some information on this point.

Experimenting with different values of R and C confirms that the crucial factor is the peak a.c. voltage across the capacitor. The conclusion to be drawn is simply



that you must avoid using electrolytics (including tantalums) as filter elements, for which purpose they are quite unsuitable anyway, due to their wide tolerances, and confine them to coupling duties, where it is easy to arrange for there to be no significant signal voltage across them. Simply ensuring that there is no premature 1.f. roll-off is normally sufficient to take care of this point.

The use of unbiased electrolytics for coupling purposes has a history of at least twelve years, going back to the introduction of the first really practical op-amps. It was clear that the use of +/- power rails, while giving designs a greatly appreciated freedom from d.c. standing currents flowing down signal earths, would demand the use of electrolytics operating under zero-bias conditions.

There were many hasty consultations with capacitor manufacturers before it became clear that reliability would not normally be a problem. This is attributable to the absence of signal voltage across the capacitors in a well-designed coupling arrangement.

Having studied the test-circuit provided by Mr Curl, I can confirm that a non-nullable pulse residual is indeed produced; my own test assembly produced waveforms similar to those accompanying Mr Curl's letter in *HiFi News*<sup>1</sup>. It is not necessary to use exotic and expensive devices such as the AD524 - a conventional instrumentation-amplifier arrangement using TL072s gives exactly the same results in this case, providing close attention is paid to trimming the c.m.r.r. My arrangement is shown in Fig. 2. However, I am inclined to think this is just a complicated way of demonstrating the same effect that Fig. 1 produces. Taking, for example, a 10ms input pulse at point A, the change in voltage across the capacitor under test (c.u.t.) is 5.8 volts peak to peak. the c.u.t. being reverse-biased by about 2 volts for part of the cycle; the capacitor is not being treated kindly. This voltage is measured using A7,8,9, which forms a second instrumentation-amp. across the c.u.t. However, I submit that this ingenious test has no relevance to properly-designed audio coupling networks, as I have explained above. The lifetime of a capacitor used in such a way will be very uncertain.

Consider in what the circumstances this effect could constitute a real problem; the most obvious is the case of a warped record feeding its signal through to a preamplifier. However, it is normally considered that even a badly warped disc is unlikely to produce subsonic levels greater than - 20dB below the general levels in the audio band,<sup>2</sup> and it will be noted that in Fig. 1 it is necessary to use signal levels only

## FEEDBACK

just inside op-amp headroom to induce the lf distortion. Nevertheless, it is always good practice to place the subsonic filtering as early as possible in the audio chain. The preamplifier design that opened this audio can of worms has its subsonic filter immediately after the disc preamp. stage,<sup>3</sup> and in fact the only electrolytic capacitor the signal has passed through before it is the one at the very input, where signal levels average 5mV rms. I would suggest there will be no problems there

One criticism that may well be levelled at my reasoning is that sinewave-and-analyser testing is hopelessly unhip, and that there are all sorts of degradation phenomena that ignore sinusoids but mangle music. I have yet to see proposed any plausible mechanism that could ignore single sinewaves anywhere in the audio band, and yet still affect complex signals.

In the course of writing this reply, I re-read the reference Mr Curl cited. In some ways the articles by Jung and Marsh<sup>4</sup> are impressive, including as they do a comprehensive survey of capacitor theory and construction. culminating in some pictures of distortion residuals very similar to those I obtained with the circuit of Fig.1, though they nowhere make the point that no-one in their senses would make a high-pass filter with electrolytics. From this they go on to state that "When music is the a.c. signal, the sonic degradation is one of compression or a restriction of the dynamic range." I find this statement remarkable, if not actually frightening, as it implies that the capacitor is either turning up the gain in the presence of a low signal, or turning it down in the presence of a high one. I do not believe that any such effect could be measured with any type of capacitor, and I fear that this is another example of an unjustified conceptual leap beteen a known physical phenomenon, and an extremely speculative conclusion. I regret to say that such conceptual pole-vaulting is common in the hi-fi press, and is in danger of making the whole field an object of ridicule to those involved in serious engineering.

I cannot believe that I am the only one who finds it disturbing that people make such extraordinary claims without any attempt to explain how such a mechanism could conceivably operate, or making even the roughest numerical estimate of its magnitude.

I should like to say that I agree whole-heartedly with the views expressed by Mr Peter Baxandall in the correspondence columns of HiFi News. It has become the accepted norm to pay a quite undue amount of attention to people simply asserting that suchand-such an effect occurs (it really does sound better . . .) without any sort of objective evidence. Any 'new effects'' allegedly discovered

need the following before becoming even slightly respectable.

a) A set of double-blind, properly-conducted (no easy matter) listening tests to show that the effect really does exist, verified by a rigorous statistical analysis.

b) A theoretical mechanism for the operation of the effect that is at least logically consistent, if not actually plausible.

This second condition in particular is almost always lacking in the wilder statements made about audio design.

If I can now address Mr Armstrong in particular, I must say the gold flashing on his telephone leaves me unmoved. I assume it is there to enhance reliability in unfriendly environments, as given the millions of telephones installed their reliability is of great importance. I would suggest that it is unlikely that his 'phone company are attempting to scale the higher pinnacles of hi-fi reproduction.

Secondly, I am also unimpressed by the wire-crushing thumb-wheels (or was it thumb-crushing wirewheels?) on his electricity meter. These are to ensure an accurate absolute measurement under highcurrent conditions, whereas in audio a gain variation of a few tenths of a dB are quite unimportant, always assuming that they are not level-dependent, of course.

As for Mr Armstrong's defective head amp., miraculously taking up its p.c.b. and walking after a shot of expensive capacitors, all I can say is that as far as I am concerned this kind of anecdotal evidence is worse than useless. If Mr Armtrong really has found a new psychoacoustic effect, then I would dearly like to know how it works, so I can use it. However, my own suspicion is that it's one more case of "experimenter effect"

One more thing. I try not to take all of this too seriously, but I think must decline to be labelled a pendant just because I insist that science and engineering are about reality and repeatable measurements rather than unsupported assertions. I am an engineer, and I hope that my approach to engineering is scientific, because I cannot think of any other that would work.

You might as well accuse an accountant of pedantry when he fails to embezzle his client's money.

D.R.G. Self Bow

London WC

References

J. Curl, Letter, 'Front End', HFN/RR Aug, 1985, p15. 2. T. Holman "Dynamic Range Requirements of Phonographic Preamplifiers.", AUDIO July, 1977. pp72-79. 3. D.R.G. Self, "A Precision Preamplifier.", Wireless World Oct,

1983

4. Jung & Marsh, "Picking Capacitors" AUDIO Feb, 1980, pp52-62. 5. P. Baxandall, "Views" HFN/RR July, 1985, pp15-17.

## PRECISION PRGAMPLIFIER

I agree with Mr Armstrong (November, 1985 Letters) that Mr Self does appear pedantic, even didactic, in print, but think that Mr Self has a point in which, however, there is confusion of engineering and other factors.

Ultimately, uncontrollable variations in manufacture result in what are, on a scale of perfection, gross differences between individual items of components, let alone the differences to which Mr Curl draws attention (EWW Nov, 1985) for capacitors. On this scale of perfection every item within auditoria and studios, performance, recording or transmission, including links, and reproduction, including rooms, has a similarly highly variable effect on what we can hear, as well as undoubted variations in our own biological systems on different social and emotional levels and time scales.

In the mid-fifties I knew several professional musicians and as I got to know them it became plain that, despite acute musical appreciation of the 'sound' of auditoria and of different performers' effects on performances, when it came to radio and record reproduction they were largely indifferent to sound quality. What, it seems, they had been taught to look for were the minds and emotions of the composer and performer(s) at work and it was these that they were listening for and identifying with. As with language, it was 'the meaning' rather than detailed syntax and sound that they responded to.

So for those who like realistic quality of sound (in other than live performances), whether we put into any new system any capacitors at all or even specially selected ones may perhaps produce the ultimate of what can be heard, but each single system will have an individual sound, unlikely to be perfect reproduction of the original. But if we can accept this sound then we can relax and enjoy as many as possible of the subtleties of performance rather than be obsessed with those of the sound alone. Particularly, we have to beware of a psychological

readiness increased by highly personal criticism and advertising to accept new and expensive techniques and attitudes as inevitably better, rather than just different!

Far more insidious is the overall rise in basal and transient redundant sound as well as greatly increased radio and mains-borne noise and interference, even with competent filtering. David White Llangefni Gwynedd

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## MAXWELL

The consistence of Ivor Catt's misrepresentation of Maxwell's laws is remarkable. Whatever deficiency they do contain, if any, it is certainly not at the elementary level claimed in the issue of November 1985. ('The Hidden Message in Maxwell's Equations').

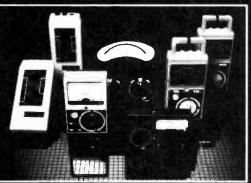
The basic problem remains apparent ignorance of vectors and the role they play in Maxwellian theory. Ivor Catt seems to think that Maxwell's laws are some kind of elaborate hoax supported by an establishment conspiracy to suppress 'alternative' theories. He also believes that equations (9) and (10) in his latest diatribe represent the views of the conspirators. If they did, he would indeed have a point. But, sad to say, the windmills that this exuberant knight errant is tilting at are significantly different from the reality of Maxwell!

The mistake he makes is fundamental and disastrous. It is entirely necessary to modern em theory that the E field vector is perpendicular to the H vector. Why, then, do equations (9) and (10) not show this? Without the direction property of a vector, em theory would fail to account for such simple phenomena as reflection. Ivor Catt attaches some mystical importance to Z<sub>0</sub>; anyone who was properly conversant with EM theory would not. Z<sub>0</sub> is derived from the magnitude of the E and H vectors; their directional property is eliminated and most that is useful in the theory with it.  $Z_0$  is not a 'primitive': it lacks directional information.

Ivor Catt's difficulties with the expression of physical concepts in mathematical form do not seem to be confined to electrical matters. True enough, if he walks along the plank far enough in the direction (hooray for direction!) 'v', 'h' does indeed decrease but so does 'x'. Sorry, Ivor old son, but you are wrong again, as you walk along the plank it is because it is going backwards underneath you that it leads to that sinking feeling. Dermod O'Reilly Antwerp Belgium

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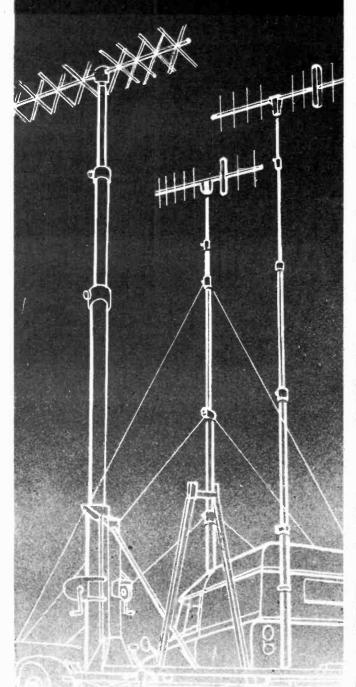
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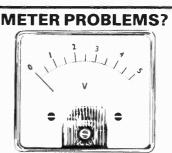


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## **CIRCLE 24 FOR FURTHER DETAILS. ELECTRONICS & WIRELESS WORLD FEBRUARY 1986**

# **Compact disc** mastering

John Watkinson explains away the confusion of p.c.m. adapters for use with video recorders, contrasting three different formats and detailing the more advanced codes of convolutional interleaving used in the smaller v.c.rs.

o cut a Compact Disc a digital recording must be made which contains exactly the same sample values as will appear on the disc. Unlike vinyl disc cutting, where the cutter operator has control over level, the Compact Disc cutter faithfully transfers every digit from the master tape. The disc mastering recorder is designed to produce that tape.

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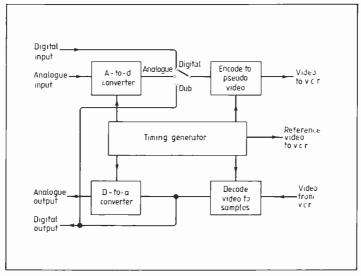
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There are currently less than ten places in the world where Compact Discs are cut for duplication, so it is usually necessary to send the master tape to the disc plant. The robustness and convenience of a tape cassette are ideal for this journey.

The bandwidth of a digital stereo signal is about 2Mb/s, and some years ago the only devices able to record this bandwidth economically were video recorders. Digital data were encoded to resemble a tv waveform sufficiently well to fool an unmodified v.t.r. Originally, video recorders were open-reel devices, but once the U-matic cassette and its smaller relatives Betamax and VHS came into being they were soon adopted for digital audio recording. Digital mastering recorders for CD work today on the same principle.

A digital audio recorder which uses a video cassette recorder is depicted in Fig.1, in which the digital audio unit is often referred to as a p.c.m. adaptor. The unit has five main sections. Central to operation is sync and timing generation, which produces sync pulses for control of the video waveform generator and locking the video recorder, in **ELECTRONICS & WIRELESS WORLD FEBRUARY 1986** 



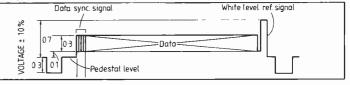
addition to producing sampling rate clocks and time code.

An analogue to digital converter allows a conventional analogue signal to be recorded, but this can be bypassed if a direct digital input is available. Similarly, a d-to-a converter is provided to monitor recordings and this too can be bypassed by the direct digital output. Also visible in Fig.1 are encoder and decoder units that convert between digital data and the videolike signal known as pseudovideo or composite digital.

A typical line of pseudo video is shown in Fig.2. The line is divided into bit cells, and within them black level represents a binary zero and about 60% of peak white represents a binary one. The restriction to 60% is because most video cassette recorders use non-linear preemphasis misinterpreting the pseudo video. Clearly the bit rate must be an integer multiple

of line rate, and the sampling rate used will be derived from the frequencies in the tv standard. Thus the sampling rate of the disc was determined by video standards even though the disc itself has no video signal structure. The frequency of 44.1kHz is derived as follows.

In the 525/60 monochrome tv standard there are about 245 useful (i.e. not blanked) lines in each field, giving a line rate of  $245 \times 60 = 14,700$  lines per second. If three samples per channel (left and right) are stored in one video line the rate of 3  $\times$ 14,700 = 44,100Hz is obtained. This sampling rate may also be obtained on the 625/50 tv standard by using 294 lines in each field, since  $294 \times 50 = 14,700$ .





Our Compact Disc series, introduced in the January 1985 issue, has so far featured: Principles of optical storage, March & April 1985 • Channel code and disc format, May & June 1985 Compact Disc players, August & November 1985.

Fig. 1. Block diagram of p.c.m. adaptor. Note the dub connection needed for producing a digital copy between two v.c.r.s.

Fig. 2. Typical video signal from p.c.m. adaptor. Data swings over only 0.3 volts to avoid activating pre-emphasis. Peak white pulse prevents a.l.c. increasing gain.

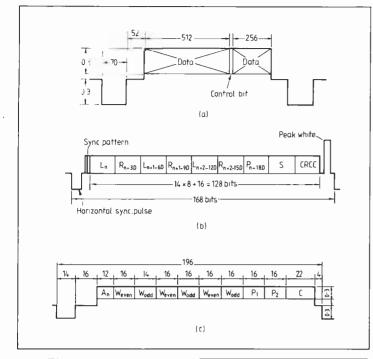


Fig. 3. Video line from PCM1610 has control bit that determines the use of pre-emphasis and selection of 44.1 or 44.0559kHz (a). Note peak white ref. In video line from PCM-F1 in 16bit mode (b) and address at beginning of video line from JVC unit (c).

## Diversity of standards

Unfortunately the diversity of television standards has led to a complex situation for p.c.m. adaptors. The Compact Disc is a world wide standard, and it would be a desirable goal for mastering recorders to record also to a standard format.

The official CD mastering format is that produced by the Sony PCM1610, and this only exists in one version, which uses the 525/60 monochrome standard because of its Japanese origin. Thus even in Europe, discs are mastered on 525/60 video recorders so that master cassettes recorded in the USA or Japan can be cut in Europe and *vice versa*.

The first problem arises with the introduction of NTSC colour broadcasts. The field rate was reduced to 59.94Hz to move harmonics of the new colour subcarrier away from the sound subcarrier inherited from the monochrome standard.

If a p.c.m. adaptor is slaved to an NTSC generator, the sampling rate falls to  $44100 \times 59.94/60 = 44055.9$ Hz and the timecode generator enters a mode called 'drop frame' whereby the timecode can continue to read real seconds and minutes even though there are no longer a whole number of fields in a second.

The CD sampling rate and timecode are irrevocably locked,<sup>2</sup> and therefore a master tape with 44055.9Hz sampling rate and drop-frame time code will be rejected by the disc cutting machine. Compact Disc and NTSC audio are incompatible. But digital sound tracks from PAL-synchronized broadcasts *are* compatible because the precise 50Hz field rate of PAL can be locked to the 44100Hz rate by a ratio of 882:1.

The position is further complicated by the existence of further standards. One of these is the EIAJ standard for consumer p.c.m. adaptors. These devices were originally intended for the top end of the hi-fi market place and enable a domestic v.c.r. to become a digital stereo

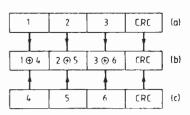


Fig. 5. In this one 35-line interleave block of 1610/1630 format, block is divided into three sections of  $11\frac{2}{3}$  lines each, two data, one parity. Three passes through the interleave memory are needed to create the signal structure (1, 2, 3). Large L/R interleave allows interpolation if dropout exceeds  $11\frac{2}{3}$ lines.

recorder. The consumer would, however, expect to be able to use the v.c.r. for regular tv recording as well. Thus the EIAJ format is in fact two incompatible formats: the first a sampling rate of 44055.9Hz using the 525/59.94 NTSC timing, and the second 44.1 kHz sampling with 625/50 timing. Both are intended for use with Betamax or VHS recorders.

The consumer division of Sony sold the EIAJ-format PCM-F1. Built with custom l.s.i. chips made economic by volume sales, and with battery capability, it cost only a few hundred pounds. With a matching portable Betamax v.c.r., the

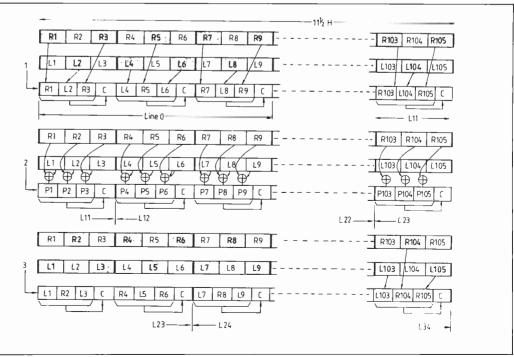


Fig. 4. In the PCM1610/1630 format, error correction is via an exclusive-or term computed from the two samples shown. c.r.c. character detects errors, parity term corrects. Redundancy is 100%. SL-F1, digital audio recordings could be made on location.

By contrast, the PCM1610 was intended for professional use, and relatively small volume production meant it was implemented with s.s.i. chips, which made it large and heavy and very expensive. Sales were somewhat damaged by the PCM-F1: a peculiar situation for Sony since they made both!

As a consumer product, the PCM-F1 was not ideally suited for Compact Disc mastering. There are no direct digital outputs, only an analogue output, and for this reason not too much trouble was taken with d.c. offsets in the converters. There is also an  $11\mu$ s timing error between channels, as a result of using single converters multiplexed between left and right.

These problems have been overcome in adaptor units supplied by independent companies such as RTW and Harmonia Mundi Acustica. They modify the PCM-F1 to give a direct digital output and use digital filters to remove offsets and timing errors, and then produce a direct digital output that is compatible with the 1610 and/or with the AES/EBU digital interconnect standard.

To add to the confusion, Sony then introduced the PCM701, a semi-professional version of the F1, which is rack mounting and non-portable. They then dropped the F1 which provoked such an outcry that they had to reintroduce it.

In addition to the 1610/Umatic and the F1/701/Betamax rivalry, there is a third contender from JVC for mastering which uses industrial VHS transports. Some disc pressing plants accept the JVC cassettes.

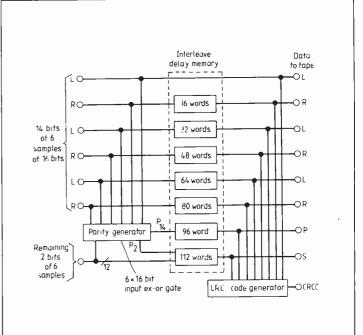
## **Formats contrasted**

All three p.c.m. adaptors use the video waveform on a binary channel, with black representing zero and 50 or 60% of peak white representing 1. However, consumer recorders with a.l.c. would increase the gain of such a signal, and so to prevent this the JVC system uses a peak white pulse once per field. The PCM-F1 uses a peak white pulse once per line. The PCM1610 is intended for use with semi-professional U-matic recorders which have no a.l.c. and has no peak white pulses. Typical waveforms are shown in Fig.3.

All three systems use extensive interleaving to combat burst errors caused by tape dropouts. The PCM1610 has the most basic interleave/correction system which subdivides each field into seven blocks of 35 lines each, and interleaves within the blocks. The error correction method is a cross-word code. The PCM-F1 and the JVC system both use the convolutional interleave approach and more advanced codes.

The cross-word code of the PCM1610 is shown in Fig.4. Input samples 1 to 3 form a code word at (1) with a c.r.c. character. Samples 4 to 6 form a second code word at (c) with a c.r.c. character. The exclusiveor terms of the sample pairs shown form a third code word with a c.r.c.c. at (b). In the case of an error, the c.r.c. fails, but does not locate the error. All samples in the codeword are presumed faulty. For example 4,5 and 6 are declared faulty. Sample 4 is obtained from the exclusive-or of the first symbols in codewords (a) and (b) (10 (104) = 4), and so on. The system is not very efficient and the amount of redundancy is equal to the amount of data.

The interleave over a 35-line period includes a left/right channel interleave (Fig.5). Dropouts up to  $11\frac{2}{3}$  lines long are fully correctable, since this magnitude will not destroy more than one of the three related code words. For example if line 0 is



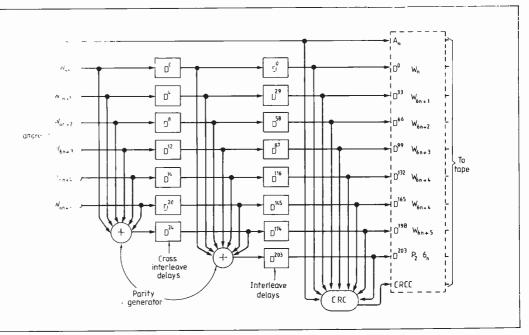
corrupt, P1, P2 and P3 from the end of line 11 and L1, R2, L3 from the middle of line 23 are used to correct R1, L2 and R3, and so on. If the dropout continues up to  $13\frac{1}{3}$  lines, two code words of the related three will be destroyed and correction is impossible. Interpolation will then be used.

For example, if lines 0 to 12 are destroyed, L4, R5 and L6 will be uncorrectable (amongst others) but because of the interleave in line 23, L3 and L5 can be used to recreate L4, R4 and R6 can be used to recreate R5, and L5 and L7 can be used to recreate L6.

If the corruption is more severe, previous samples can be

Fig. 6. In 16bit mode, 14 of the bits of each sample are stored as words (L, R) and the extra two bits of each sample stored as 12bits of another word. Parity is generated on the 16bit data, and 14 of the parity bits are stored in the P-word. Remaining two bits are added to the 12bits of sample data to complete the S-word.

Fig. 7. Interleave diagram of JVC format showing parity generated before and after an interleave to produce crossinterleaving.



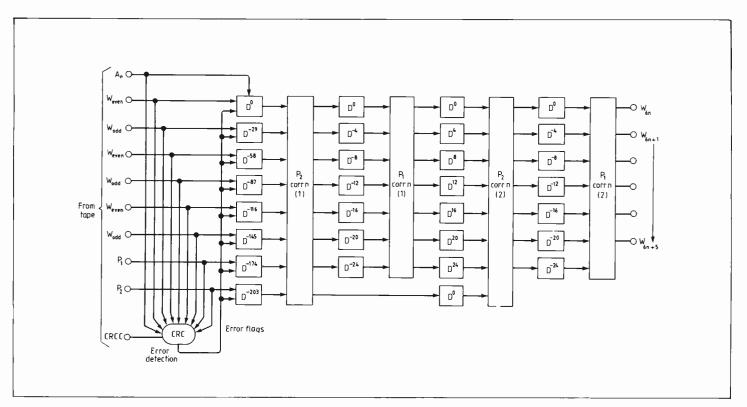


Fig. 8. De-interleave and correction diagram for JVC format. Depending upon distribution of errors, it may be better to correct with either P1 first or P2 first: multiple re-interleave and deinterleave allows both choices. See text.

held to substitute current samples, but eventually the machine has to mute to prevent noise. In practice, dropouts are usually much smaller than  $11\frac{2}{3}$ lines, but the system has to interpolate if a random error occurs near a burst. The simple code of the PCM1610 relies on the relatively large trackwidth of the U-matic format to give good SNR. It is not suitable for VHS or Betamax video cassette recorders even if the a.l.c. is defeated.

## **Convolutional interleave**

Both the PCM-F1 and the JVC system use a much more sophisticated approach to allow the use of smaller-format v.c.rs. They use convolutional interleaving which spreads the effect of burst errors more evenly.

The PCM-F1 works in two modes. In 14bit wordlength mode, the machine uses both Badjacent error correction and erasure (pointer) correction, and this is very powerful against random errors near to burst errors. In this mode the PCM-F1 conforms to the EIAJ standard for 14bit consumer p.c.m. adaptors. However, in 16bit mode, which is necessary for mastering, the 14bit format is retained and the extra two bits in each of six samples and one parity word are stored in place of the 14bit B-adjacent redundancy. Thus

the power of the error correction system is reduced. The Badjacent decoding system will not be discussed further here as it does not apply to Compact Disc mastering. In 16bit mode the PCM-F1 reverts to a system shown in Fig.6.

Simple exclusive-or parity produces a P word from six samples. After interleave, a c.r.c. character is added to the end of each line. If the c.r.c. fails for a given line, all samples on the line are flagged bad. After de-interleave, this results in single errors with flags in many different P-code words, and correction is possible. However, the presence of a random error due to noise in the vicinity of a burst error due to dropout may cause two error flags in one Pcode word, which is uncorrectable. Interpolation will then be necessary.

The most advanced system is that of the JVC machine<sup>2</sup>, which uses cross interleaving. The cross-interleave is formed with simple parity only, and with c.r.c.c. after interleave to act as a pointer. In this respect the interleave resembles that of the DASH format<sup>3</sup> more than the cross interleave of the Compact Disc, which uses Reed-Solomon redundancy without an additional c.r.c.c.. The arguments for cross-interleaving given in the June article4 on Compact Disc format still apply.

process. On replay if a line gives a c.r.c. error, all samples are flagged bad. Fig.8 shows the correction process; after the first de-interleave, the P2 symbol can correct single errors, but not multiple errors.

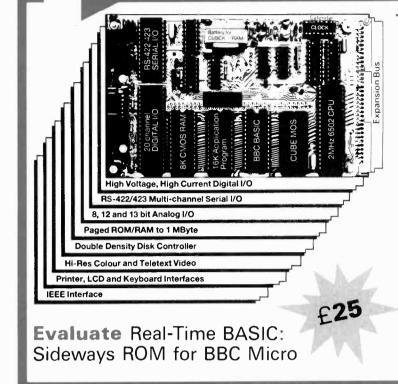
The principle of cross interleave is that double errors in one interleave show up as single errors in the cross interleave and vice-versa. Thus further deinterleave allows the P1 term to correct single flagged errors. On re-interleaving to P2, many of the double errors will have been converted to single errors in the P1 process, so at the second attempt P2 can fix the remaining single errors. A final de-interleave to the real-time sequence allows P1 a further chance at correction, but the probability of this stage operating is small.

The powerful error correction of the JVC system allows operation on the smaller VHS type v.c.rs and although JVC do not advertise the suitability of Betamax for commercial reasons, it should be equally compatible.

One problem with domestic v.c.rs is that the low linear tape speed results in dismal audio tracks (analogue) making time-code recording difficult, not a problem with the U-matic machine.

Fig.7 shows the interleave continued on page 62 ELECTRONICS & WIRELESS WORLD FEBRUARY 1986

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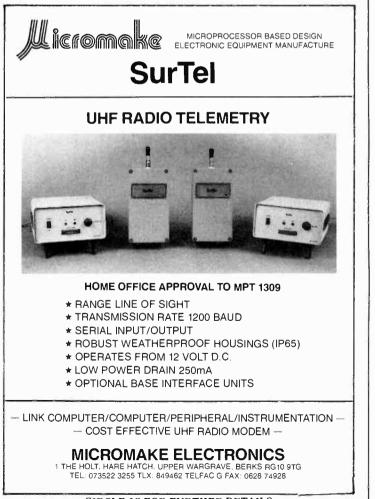
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TSC3660	<b>40-300</b> 470-860	10-30VHF 16-36UHF	80dB (1000mv)	+ or - 5dB	5d8 VHF - UHF	27-42V 18VA~
TSC3660SM	40-300 470-860	10-30VHF 16-36UHF	60dB (1000mv)	+ or5dB	5dB VHF UHF	25-45V 13VA
TSC3665	40-300 470-860	10-30VHF 16-36UHF	60dB (1000mv) VHF 65dB (1800mv) UHF	+ or - 5d8	5dB VHF	24-42V 24V A
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EZV9	40-300	9dB	1d8	EZU9	470-860	9dB	1dB
EZV12	40-300	12dB	1d9	EZU12	470-860	12d8	1dB

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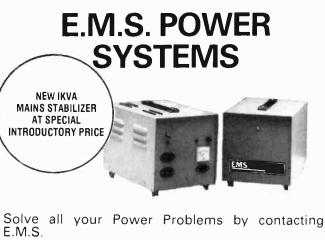
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## by J.L. Linsley Hood

# Synchrodyne a.m. receiver

# 2 — Oscillator circuit, construction and commissioning

In the first part of this article, I discussed the design requirements for a high quality a.m. radio receiver, having good sensitivity, low demodulator distortion, freedom from whistles and with adjacent-channel selectivity which is user-adjustable.

One of the most satisfactory ways of achieving these design requirements is the use of a 'synchrodyne' (direct-conversion) receiver, of the form shown in Fig. 1, in which the incoming signal is heterodyned with a local oscillator held in frequency and phase synchronism with the incoming carrier. This allows direct recovery of the modulation imposed on that carrier, and renders adjacent-channel signals as higher-pitched audio tones — depending on the frequency separation between the wanted and unwanted carriers - which can be removed by appropriate post-demodulator audio filtering.

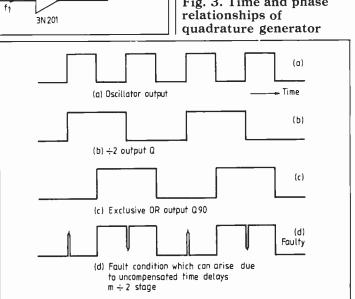
The essential feature of this kind of receiver system is the maintenance of a local oscillator in synchronism with the incoming carrier: this is achieved by a phase-locked loop consisting of phase sensitive detector 2, the voltage-controlled oscillator (v.c.o.), and the 90° phase-shifting arrangement, shown in the lower part of Fig. 1.

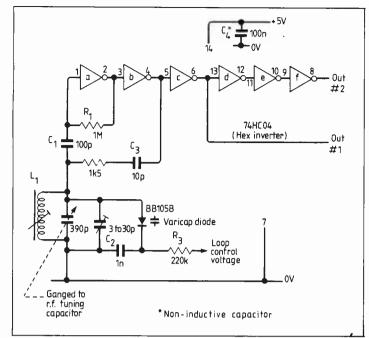
This p.l.l. function can be achieved economically, in a manner which is usable over a wide frequency range, by the use of high speed c.mos integrated circuits, as shown in Fig. 2. In this an oscillator, of which the output frequency is voltage controllable to some extent, for example by the use of a variable-capacitance diode in parallel with an LC tuned circuit, is fed to a divide-by-two stage, and thence to an exclu-

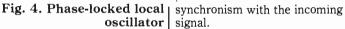
Signal strength Phase det 1 a f 20 Mute outout Phase shift cincuit Fig. 1. Basics of directr.f. 90° Local amplifier conversion process oscillator V. C. O Tune p. l. l. æ Phase det 2 74H04 74H74 74H86 Fig. 2. Phase-locked loop using practical devices Voltage Divide Exclusive controlled by two OR oscillator Positive feedback ★ ⇒ Amplifier Phase detector مومومو Varicap diode ME 1496  $\approx$ 220k TL071 Amplifier Input r.f. Fig. 3. Time and phase signal f1 relationships of **RN 201** 

sive-or gate, whose logic (voltage/time) sequence is shown in' Figs. 3 (a), (b) and (c).

This circuit arrangement generates two outputs, in quadrature, both of which are at half the frequency of the controlled oscillator. If one of these, say the output from the exclusive-or gate, is taken to one of the inputs of a suitable phase-sensitive detector, and this is used to provide an amplified and filtered control voltage to the voltage-controlled oscillator, then the other output from the circuit of Fig. 2, will, when the loop is in lock, be in accurate phase and frequency







The circuit employed for the oscillator, and which uses a 74HC04 high speed c.mos hex. inverter, is shown in Fig. 4, and the quadrature-generating layout of divide-by-two stage and exclusive-or gate is shown in Fig. 5.

There is, unfortunately, an inherent snag in this type of

system, due to the unavoidable time delay introduced into the second input fed to the exclusive-or, as a result of propagation delay in the divide-bytwo stage (a 74HC74 dual flipflop). This can impair the simultaneity of the rising and falling edges of the waveforms of Fig. 3(a) and 3(b), and lead to the type of fault waveform shown in Fig. 3(d). Compensation for this small, unwanted, time delay is provided by the use of the small sequential propagation delays of the three unused inverter stages of the 74HC04. This gives a clean quadrature output waveform, without unexpected glitches.

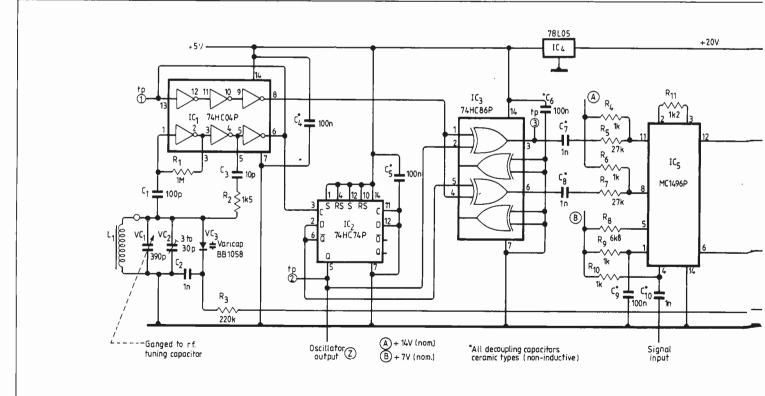
The use of the 74HC04 hex. inverter as an oscillator is very satisfactory from the point of frequency stability, and, although I have not sought to discover how high an output frequency is possible with this kind of circuit, it will certainly work at frequencies well above 10MHz, which is more than adequate for present needs, where the required operating frequency range of 3.2 MHz-1100kHz will give, following frequency division, a tuning range of 1.6MHz-550kHz.

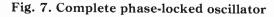
A point which should be

noted, as a possible pitfall for the unwary, into which I walked myself, is that the pin connections for the 74HC86 quad. exclusive-or are not the same as those for the 74C86/CD4070. I have shown the required pin connections in Figs. 4 and 5.

There is a wide range of possible phase detectors usable in the phase-locked loop shown in Fig. 2. I have chosen the LM/MC1496P (14-pin, d.i.l.) double balanced modulator/demodulator for this purpose, because it is relatively inexpensive, widely available, and designed specifically for this type of application. There are some constraints in its use. particularly when employed as a synchronous demodulator for radio signals, which I will discuss later. However, in the p.l.l. application, its performance is quite trouble free.

I have shown the complete circuit of the phase locked oscillator, having a synchronous output in phase with the incoming signal, in Fig. 7. Since phase-opposed outputs are available from the 74HC74, these have been used to generate push-pull drive signals to the 1496 demodulator, since symmetrical operation of this





symmetry in the loop.

Considering this circuit in detail, the first inverting gate of IC1 is biassed into its linear region by R<sub>1</sub>, and positive feedback is applied via C<sub>3</sub>. 'Squegging' in the oscillator, which can happen with the 'HC' series inverters in this circuit, though not with the lower speed c.mos, is prevented by the small resistor R<sub>2</sub>.

The operation of the LM/MC1496 can be explained with reference to the internal circuit diagram of the i.c. shown in Fig. 6. In this, a longtailed pair of transistors, Tr<sub>1</sub> and  $Tr_2$ , are fed from separate, high dynamic impedance, constant current sources  $CC_1$  and  $CC_2$ . This allows the conversion gain, and internal balance to be controlled by the external resistor 'R<sub>x</sub>' which will normally be very low in comparison with the source impedances,  $(CC_1 \text{ and }$  $CC_2$ ), but high in comparison with the dynamic emitter output impedances of  $Tr_1$  and  $Tr_2$ .

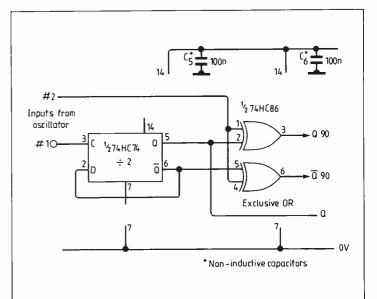
Effectively, therefore, a signal applied to the E input will appear, because of long-tailed pair action, as two equal but opposite amplified current outputs at the collectors of  $Tr_1$  and  $Tr_2$ , where it will be equally

i.c. helps preserve lock divided by the matched transistor pairs, Tr<sub>3/4</sub> and Tr<sub>5/6</sub>, before being recombined at the output pins 6 and 12.

> Because of the high degree of matching which is possible in transistors manufactured in i.cs, in the absence of any inputs to pins 8 and 10, any signal applied at the E input will be combined with an equal and opposite signal due to longtailed pair action of  $Tr_1/Tr_2$ , and will result in a null output at pins 6 or 12. Similarly, in the absence of any input to E or  $\overline{E}$ , any signal applied to the oscillator inputs Q or  $\overline{Q}$  will be nulled at the output pins.

> However, if signals are applied simultaneously to the signal and oscillator inputs, the output at pins 6 or 12 will be the sum and difference frequencies of these two, and in the particular case of applied signals which are in phase, the output will be a d.c. shift, superimposed on the frequency sum component.

> The particular circuit layout required for the use of the 1496 is shown in Fig. 8(a), in the case of a single supply line operation, with the required d.c. input biassing obtained from a simple potential divider chain across the d.c. supply line.



Complete symmetry of operation, where this is desirable, can be obtained by small adjustment to the forward bias applied to  $Tr_1$  and  $Tr_2$ , using the type of circuit shown in Fig. 8(b). This is unnecessary in the case of the p.l.l. circuit of Fig. 7, where the normal internal balance of the 1496 i.c. is quite adequate.

The output of  $IC_5$ , in Fig. 7., is taken via a pair of unity-gain buffer stages,  $IC_{6a/b}$ , to a variable-gain amplifier stage,  $IC_7$ , of which the h.f. response is rolled off by  $C_{13}$ . A d.c. offset pot.,  $VR_2$ , is used to set the output of IC<sub>7</sub>, (TP6), initially, to half the supply line voltage (+10V), with the gain control pot.  $VR_3$  approximately at the mid-point position.

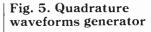
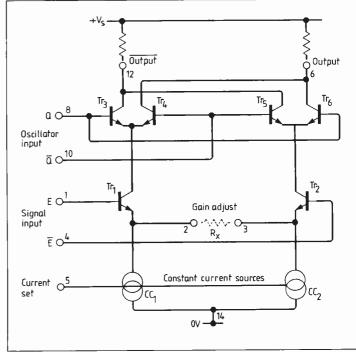
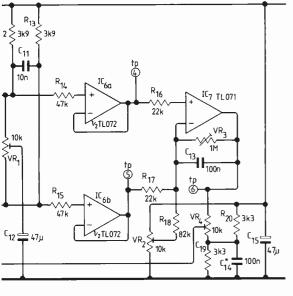


Fig. 6. Internal layout of LM/MC 1496 balanced modulator





Range	L <sub>osc</sub>	L <sub>RF</sub>	Approx, pri./sec. coupling ratio
<u>MW</u> (0·5 to 1·65 MHz)	 Huر66	266µH	20T:100T (20%)
<u>LW</u> 100 to 350kHz	1·5mH	5•9mH	80T:700T (12%)

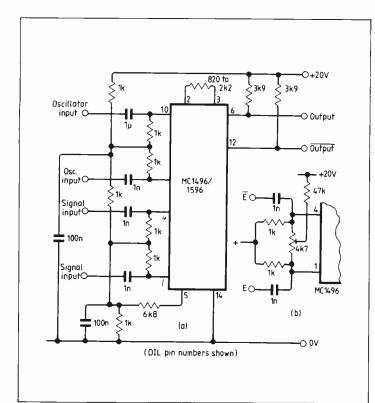


Fig. 8. Use of MC 1496/1596 as double balanced demodulator, with single-supply operation is shown at (a). Method of nulling spurious responses is at (b).

The potentiometer  $VR_4$  is used to set the proportion of the output control voltage applied to the 2-10pF Varicap diode,  $(VC_3)$ , and initially this pot. can be set to maximum output. The particular type of diode employed, provided that it has a suitable capacitance range, is unlikely to be particularly important. I used BB105B units, simply because they were to hand.

The circuit, with the exception of the 'HC' c.mos i.cs, is not particularly critical in respect of the d.c. supply voltage, but it is suggested that a +20 volt line shold be used. However, since the high speed c.mos i.cs are very critical in respect of applied voltage, especially if a high degree of local oscillator stability is desired, the d.c. supply to these three i.cs is separately stabilized by a low-power voltage regulator i.c., IC<sub>4</sub> (78L05).

## Commissioning the p.l.l.

Since the correct operation of the p.l.l. is crucial to the satisfactory performance of the receiver, it is suggested that, if a suitable oscilloscope and signal generator is available, the circuit should be tested, before the remainder of the circuit is put into operation, by tuning the oscillator into synchronism with an unmodulated r.f. output from a signal generator, of some 2-20mV amplitude, injected into pin 4 of  $IC_5$  and small adjustments made to  $VR_2$  and  $VR_3$  until the best phase coincidence is obtained, on lock, between the signal input and the frequency divided oscillator output, as monitored at TP2.

In the absence of an oscilloscope, correct performance, while in lock, can be seen if a voltmeter is connected between TP6 and the 0V line, when a voltage excursion of 1-2 volts, on either side of the mean output potential — depending on the signal generator output — should be observed as the signal generator, or oscillator tuning, is swung a few hundred Hertz on either side of the central lock frequency.

A modulated signal may then be applied and  $VR_1$  adjusted to give the least audio output signal present at TP6. If an oscilloscope is not available, this adjustment could be made with a pair of high impedance headphones connected between TP4 and TP5. Similarly, small adjustments could be made to  $VR_2$  and  $VR_3$  using the same technique, since phase coincidence at TP2 implies phase quadrature (with minimum a.f. output) in the switching waveform applied to IC<sub>5</sub>.

As a final step, when the receiver is complete, and working satisfactorily, the values of VR<sub>2</sub> and VR<sub>3</sub> may be 'tweaked' to give the maximum signal strength indication on a signal, somewhere near the middle of the m.w. band, with the aerial sensitivity control adjusted to give about a half-scale reading, since the greatest value of the a.g.c. indication, other things being equal, will indicate the best phase coincidence at the MC1496 signal demodulator (IC<sub>8</sub>).

Again, when the receiver is finally complete, the maximum output setting of  $VR_4$  may probably be determined by the need to avoid too great a difficulty in locking on to a strong signal at the top (low tuning capacitance) end of the m.w. band.

Hart Electronics, Ltd., of Penylan Mill, Oswestry, Shropshire, have offered to make available a kit of parts for the assembly of this receiver.

## Windows and performance monitoring

On a desk top, you can have access to many information sources and work aids at the same time. At first, having a computer only meant that the number of items on the desk were reduced. The computer could only run one program and its screen was only useful for displaying text and simple diagrams.

Displays such as the one on our front cover mean that the whole desk can be cleared. The Sun workstation used to produce this display — a typical mixture of windows, 'icons' and 'pop-up menus' runs as part of a network, giving access to many different programs and data bases, apparently all at the same time.

Modern v.d.us such as this one give very high resolution colour displays, without flicker, that can be viewed for long periods. Using windows in this way makes managing tasks much easier so the user is soon familiar enough with the system to take on more and more activities.

As the number of tasks increases, it becomes more and more important for the user to understand how the system is handling the load. With this in mind, the workstation has built-in performance monitoring giving a graph-type display, as on the right of the picture, or an analogue meter display as shown in the top left.

Processor and memory use can be displayed, but equally importantly, information about the network linking the workstation to other systems. Monitoring of Ethernet data packets and network load is possible and to make efficient use of the network, the number of collisions can be indicated.

The Sun-2 series workstations use a 68010 processor and the Sun-3, used to produce our cover, can run some two million instructions each second using a 68020 16.67MHz processor with 68881 co-processor for floating-point operations.

The next article will deal with the receiver's r.f. and demodulator

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3JP1 3JP1 3JP2 3JP7 VCR517B 10:00 VCR517C 10:00 9 00 12:00 5:00 10:00 6:00 6:00 8:00 8:00 10:00 15:00 35:00 20:00 7420 744 B7G Skiried 0.50 B9A Unskirted 15 00 25.00 63.32 58 07 58.07 65.00 56.83 113.12 12.00 12.50 8.00 7450 7451 7453 7454 7460 7470 7472 7472 7473 7474 7475 7476 7480 0.40 B9A Skiried 0.50 B9D 0.55 Im Octal 0.40 Loctal 0.55 Nuvisior base CRT sockets Prices on 7423 7425 7427 7428 7430 7432 7433 7433 74180 74190 74191 74192 Prices on application I/C sockets Texas low profile 8 pin 14 pin 16 pin 1 BA700 TBA700Q 1 BA750Q TBA800 TBA920 TBA99Q TCA270Q TCA270Q Int Octor Locial 0.55 Nuvisior base 2.00 193 194 195 196 10ր 10ր 10ր 38 P1 38 P1 38 P1 38 P1 7438 7439 cans all sizes().40 0.48 Terms of business: CWO. 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# Forth in control

The computer language Forth is particularly suited to the control of machinery, argues David Sands, who explains how it may be implemented in control applications.

eople tend to adopt Forth then scans the user's line languages and then defend them fervently. For example, Pascal enthusiasts expounds its virtues and ignore the drawbacks such as tedious de-bugging and editing. Basic, the single most popular language in the world, is not standardised, suffers from a multitude of dialects and is slow, unstructured and messy. A favourite target for scholars of better languages it nevertheless is arguably more efficient in terms of returns on programming effort, especially for shorter calculating programs.

The truth is, of course, that there are "horses for courses"; different languages are suited to different tasks. Forth is particularly suited to the control of machinery. It has some shortcomings, which I accept but which become less important once the full power of Forth is realised.

## What is Forth?

Without wishing to embark on a tutorial of Forth, I would just like to outline the fundamentals for the benefit of readers not acquainted with the languages. Note: In all my examples of exchanges between man and machines I will underline text typed in by the user. I won't keep mentioning the need for the return key. The computer's response will be in upper case but not underlined. My comments will be in lower case. There are probably four

fundamental principles.

## 1 — Command line

numbers are Commands typed in by the user, separated by spaces, finishing with return. to-digital converter **ELECTRONICS & WIRELESS WORLD FEBRUARY 1986** 

acting on each word, and if everything works out OK then Forth types OK.

## 2 -the stack

All arguments are passed between procedures on a data stack. You can have variables, but they are not used much. Part of the reason for this is to maximise the performance of the system. Almost any language will first convert a formula to items on a stack. Because Forth users are interested in speed of execution they are prepared to keep account of the stack themselves. Suppose you wish to add 3 and 4 together and print the result:

## >45 + . (return) 9 OK

Forth scans the user's input line and first puts 4 on the stack, then puts 5. The Forth word ' + then takes two values off the stack (5 and 4). adds them together and puts the answer on the stack, the value 9. The Forth abbreviation '.' then prints it. There is now nothing on the stack. If we try to print again we get:

## >. O STACK UNDERFLOW!

### which is not ok.

But the Forth data stack doesn't exist to make everyone think like a machine. At worst post-fix notation is an unfortunate sideeffect. Imagine that we wish to calculate the total content of two tanks of chemical. Each tank has a transducer in it with quite a complicated service routine, as follows:

1 Select transducer and energise.

2 Allows filters to settle. 3 Command the analogue-

4 Wait for the ADC to complete. 5 Read the ADC. 6 Scale the result according to the calibration factor

of the transducer. 7 Leave the scales result on

the stack. Each tank has it's own service routine, called LEFT-TANK and RIGHT-TANK. The total of both tanks is given by:-LEFT-TANK RIGHT-TANK + Now LEFT-TANK and **RIGHT-TANK** are not values! Nor variables. We are not doing PRINT X + Y. They are whole procedures which leave their results on the stack. There can even be other words between LEFT-TANK and RIGHT-TANK as long as they don't take off or put on extra items on the stack for example:

LEFT-TANK BLA-BLA RHUBARB RIGHT-TANK + .

### 3 — the dictionary.

Instead of a handful of keywords as in Basic, there are about 150 or more words, which are organised in a "dictionary" Forth interprets a user's word by searching down the dictionary until it finds it, then carries out the activity which is in the "definition" of the word. If the word isn't in the dictionary then Forth tries to convert it as a number. If it isn't a valid number either, then Forth rejects it as an error. Most low level words in Forth tend to be abbreviations, or

single characters, for example the Forth word for "print" is just a full-stop (.) whereas higher level procedures have highly descriptive names.

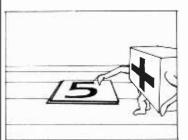
## 4 — definitions.

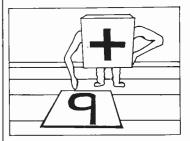
This principle is fundamental to Forth users. It is the building-



David Sands is a consultant in industrial automation and software, with a small company, Sands Technology, in Cambridge. In 1979 he was one of the first to import personal computers from the USA. In the same year he designed a low-cost speech synthesizer for personal computers. Two years later he designed a talking dashboard for a car. He has also worked on naturallanguage translation using speech input and output, and on low-cost robot arms.







The Forth word "+" takes the two top numbers from the stack and replaces them with their sum.

## Summary

Forth is a highly productive language in terms of programming effort. Communication between programmer and machine is improved by the development of a vocabulary specific to the application. Programs produced with Forth have higher integrity and are more efficient than programs produced with other methods. Data types are few but highly suited to digital hardware. The use of the stack is an efficient way of passing arguments. In fact there are now single chip microcomputers available with Forth built in, such as the 65RF11. New microprocessors are being developed with stack oriented archi-tecture.

The heart of a statistical word. The process control circuit imparts a v which can be controlled confidence by Forth, as described in the text. New proc

block approach to programming.

Forth, straight off the disk, consists of a "kernel" of some 150 or more "words" or commands, most of which execute machine code whenever they are used. The programmer must define new words in terms of words in terms of words he/she has already define plus words from the kernel as necessary. These new words are added to the existing dictionary. Hence the programmer begins by defining short concise procedures which can be tested individually. In this way the programmer builds up a range of fully tested modules which are then used to construct more powerful procedures, and so on until the final application programme is complete, which can be just one word. The modular approach imparts a very high degree of confidence to the final

New procedures are created

using a colon sign, followed by the name of the procedure, then the definition ending with a semi-colon:

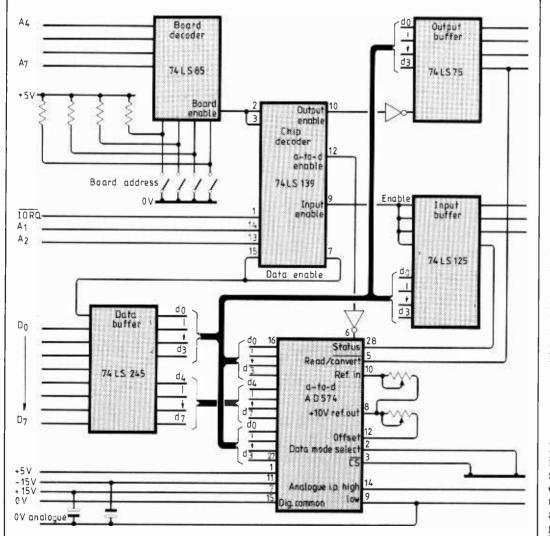
### : TANKS?

LEFT-TANK RIGHT-TANK

Now all you have to do is type TANKS? to yield the total in both tanks, or alternatively TANKS? can be included in the definition of some grander procedure.

## **Control applications**

To illustrate the above principles more fully I can outline part of a genuine application. The system being programmed is a dedicated computer which reads from linear displacement transducers checking tolerances on motor components. Deviations from tolerance are displayed on a colour screen in bar graph form and are later analysed statis-



tically for trends, machine performance etc. This is known as Statistical Process Control or 'SPC'.

A number of transducers are connected to a multiplexer and analog-to-digital converter. The ADC is an Analog Devices AD574 used in 12-bit mode on an 8-bit microprocessor bus (actually a Z80), on I/O ports O and 1. The read/convert input of the ADC is driven from bit 0 of port 4. Now, suppose we are not just starting on the software but that the hardware is not proven either, being a bread-board prototype.

## Hardware development

Assuming that sufficient of the system is running to support Forth, or that a processor emulator is being used which is running Forth, then things can be typed in at the keyboard during debugging of the hardware. Checking of a prototype module or circuit addition might proceed as follows:

## >04 OUT OK

This sends the value O out through port 4 to set the ADC to convert mode. (check with logic probe).

### >4 OUT OK

This sets the ADC back to read mode. (check with logic probe) Suppose there are problems with the wiring. We can therefore define a word to keep writing to port 4 while we look for the missing signal with an oscilloscope. You can't do this with an ordinary monitor program. You can't do it nearly as quickly by writing a test routine in any other language. Bear in mind that you might need to devise many test routines during the development of some hardware. To check the above read/convert line you might enter:

## >:TEST1 1000 0 DO 0 4 OUT 1 4 OUT LOOP; OK

>TEST1

Luckily Forth is fast enough to issue the OUT signals with sufficient regularity to be visible on an oscilloscope. If Basic were used you might be trying to view a 10 microsecond trace generated every 10 milliseconds by BASIC. This would dim the

trace by a factor of 1000 making it invisible. Forth is therefore a powerful engineer's tool.

## Hardware service modules

Hardware devices are best programmed in stages or modules. In the case of my example of the Analog Devices AD574 Analog-to-Digital converter first let's get control of the read/convert line:-(Note in practice, programs are entered on to disk, and edited, before being compiled by Forth).

: CONVERT 1 4 OUT (SET CONVERT MODE) 00 OUT (START CONVERSION)

When the ADC has converted a value, the 12-bit result is available in two parts: the upper 8 bits on port address 0 and the lower 4 bits with trailing zeros on port address 1. In my application I wired ADC data bits 0 to 3 to my bus on bits 4-7, and ADC bits 4-7 to bus bits 0-3. The code to read the ADC was therefore as follows. Note that hexadecimal numbering is preferred in the context of hardware:-

: READ

0 4 OUT (SET READ MODE) 0 IN (GET HIGH BYTE) OF AND (MASK LH NIBBLE WHICH IS THE MOST SIGNIFICANT 4 BITS) 100 \* (SHIFT LEFT TO BITS 8-11) 0 IN (GET HIGH BYTE AGAIN) FO AND (MASK RH NIBBLE WHICH IS BITS 4-7) OR (LOGICAL OR WITH BITS 8-11) 1 IN (GET LOW BYTE WHICH IS BITS 0-3 PLUS 4 ZEROS) OR (LOGICAL OR WITH BITS 4-11) - (APPLY OFFSET SO 7FF -READINGS FALL BETWEEN + 2048 & - 2047)

The ADC is fed from a TESA signal conditioner which converts the transducer outputs into a DC voltage in the range +&-10 volts. The amplifier in the signal conditioner has a choice of two gains, which I select by outputting on bit 1 of port 4, bit 0 of which is already in use. It is necessary, therefore to add, the value of this bit to the output whenever selecting read or convert modes of the ADC. To do this we need a variable to hold a value of 0 or 2. When the variable's name is used its address is put into the stack. The Forth word '!' stores a value into the address, i.e. into the variable. The Forth word @ fetches a value from the variable and leaves it on the stack.

VARIABLE SCALE

The rest of the code is then modified so:

:CONVERT SCALE @ 4 OUT (SET CONVERT) 0 0 OUT (START CONVERSION) READ SCALE @ 1 OR 4 OUT (SET READ) 0 IN OF AND 100 \* 0 IN FO AND OR 1 IN OR 7FF -

Finally, before using the ADC I must set up the multiplexer in the signal conditioner by outpouring the required channel on port 2. e.g. 2 2 OUT selects channel 2.

If the amplifier needs a settling time the signal conditioner issues a busy signal which I input on port 6 bit 0. Servicing the signal conditioner is coded like this:

: CHAN
2 OUT
BEGIN
6 IN 1 AND
0 = UNTIL
;

The BEGIN-UNTIL loop traps UNTIL bit 0 of port 6 changes to 0 (false). Now I can build a 'bigger' word thus:-

: ADC	
CHAN	
CONVERT	
READ	
:	

Note that the line: 2 OUT is not complete. It requires a further item from the stack and this is presumed to be on the stack before ADC is invoked. To select and read channel 2 we would therefore write

2 ADC

ADC contains the word READ which leaves the value of channel 2 on the stack. ADC is therefore a word which requires one value on the stack and leaves one value behind on the stack. To demonstrate the **ELECTRONICS & WIRELESS WORLD FEBRUARY 1986** 

usefulness of this we could have a condition whereby a value of, say, more than 800 on channel 4 is an alarm condition.

Suppose the diameter of a certain component were being checked by two transducers mounted each side of the component. The two transducers are required so that if the shaft were not straight then any reading produced by one transducer due to nonstraightness would cancel in the other. Suppose the transducers are on channel 5 and 6. Then we could define a single word for diameter to read both transducers and add them together thus:

: DIAMETER 5 ADC 6 ADC + ;

From now on whenever we want diameter we need only use the name; e.g.:

> DIAMETER . 500 OK

An associated word might be:-

: TC	OLARGE
800	>
•	

This leaves a 1 (true) on the stack if the argument is more than 800 units, or a 0 (false) if not. In a later definition yhou might enter

DIAMETER TOOLARGE IF ABORT THEN.

What is now happening is that Forth statements are taking on the readability of an English sentence and becoming less cryptic. At the same time we are beginning to forget the complexities of the lower levels. We can afford to! They are fully tested and reliable.

## **Machine control**

Out-put to a machine is most commonly achieved with the following devices:

Triacs - used to switch contactors and solenoids e.g. solenoid valves.

Digital-to-analog converters used to issue a varying voltage to control such things as DC motors.

Transistors (e.g. hexfets) used to switch low voltage DC devices such as stepping motors and bright lamps on the machine front.

In an application a few years ago I designed and programmed a dedicated system to check the performance of hydraulic control valves as fitted to cars with power assisted steering.

The machine had a hydraulic pump from which oil was fed to the steering valve via solenoid valves controlled by my system. One problem with output ports is that although they latch (i.e. memorise) what you send to them, their contents cannot be read back. They are therefore a kind of write-only memory (wom?). If you wish to change the state of one bit on a particular port without altering any of the other bits then ideally you read the port, after a bit, then write it back. Because you can't do this it is necessary to keep account of each port in a variable. My application used an 8-bit microprocessor, a Z-80. I had port 7 controlling 8 triacs, ports 8 and 9 controlling 16 DC lamps. The triacs were programmed thus:

VARIABLE TRIACS

:ACOUT **DUP TRIACS!** 7 OUT

Any argument to ACOUT is first DUPlicated. One copy is stored in the variable TRIACS and the other is output on port 7. Various descriptive commands can now be defined:

: OILON TRIACS @ 10R (SET BIT 0 TO 1) ACOUT (OUTPUT AND SAVE IN TRIACS AGAIN) OILOFF TRIACS @ FE AND (SET BIT 0 TO 0 MASKING IT OUT) ACOUT

**CLAMPON** TRIACS @ 4 OR ACOUT

CLAMPOFF TRIACS @ FB AND ACOUT

AIRON TRIACS @ OR ACOUT

: AIROFF TRIACS @ F7 AND ACOUT

etc. etc.

The DC outputs on parts 8 and 9 can be handled 16 bits at a time to suit Forth's 16 bit standard data size. Simply split the 16-bit value into high and low bytes and output separately on each port.

# **Compact disc mastering**

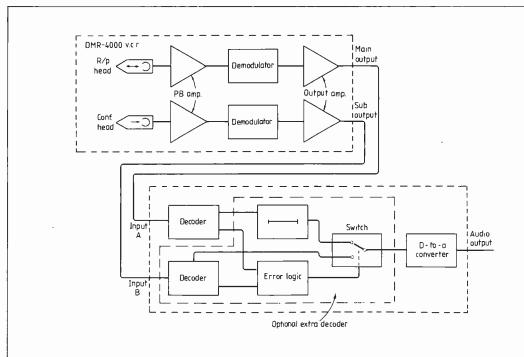
continued from page 50

Fig. 9. The DMR4000 has extra rotary heads for confidence replay. If an additional decoder is installed in PCM1630, the machine can continue to play with a clogged head. Error status will switch output to the error-free decoder, whether playback or confidence head. Delay compensates for displacement heads. The PCM-F1 has no timecode functions and for Compact Disc cutting F1/701 tapes have to be digitally dubbed to PCM1610 format using the RTW or Harmonia Mundi Acustica interfaces.

The JVC system buries the timecode and user bits in the pseudo-video waveform, so the poor analogue bandwidth of VHS audio tracks ceases to be a problem. An adaptor is available to produce timecode from the JVC p.c.m. adaptor.

Two of the greatest drawbacks of the use of v.c.rs for digital audio have been the lack of confidence replay, i.e. offtape monitoring during record, and the tendency of heads to clog due to a combination of inaccessibility and repeated passes during edit operations.

In the replacement for the venerable PCM1610 and VO5850 U-matic system, Sony have solved both problems. The U-matic recorder is no longer an industrial v.c.r. with the chroma



circuits and dropout compensator disabled, but a purposebuilt machine with extra replay heads in the drum for confidence replay, and an automatic head-cleaning mechanism (DMR4000). The PCM1630 generates the same tape format as the PCM1610, but can switch the replay section to the confidence output of the DMR4000. As an option a second replay channel can be fitted to the PCM1630 as shown in Fig.9.

During replay, the record/ playback heads drive one replay channel, and the confidence heads drive the second channel. Should one head become clogged, error detection circuits will choose data from the other heads. Significantly, the d.-to-a. converter of the 1630 uses oversampling by a factor of two to improve phase linearity.

In the next two parts of the series we discuss problems and techniques in editing digital recordings on rotary head machines.

### References

1. See Fig.6 of ref.4.

2. Professional-use PCM audio processor with high efficiency error correction system, Yamada, Fujii, Mariyama & Saitoh. AES Convention, Los Angeles 1980.

3. The DASH Format, J.R. Watkinson. Broadcast Systems Engineering October 1985.

4. Channel code and disc format, J.R. Watkinson, *Electronics & Wireless World*, June 1985, pp.80-2.

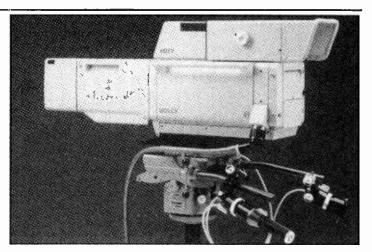
# HDTV Camera

A television camera, which sets new standards in picture recording quality, has been developed by Robert Bosch GmbH, on behalf of the Heinrich Hertz Institute for Telecommunications Technology in Berlin. The development work was sponsored by the Federal Ministry for Research and Technology. To date five of the experimental cameras have been delivered, namely to the Heinrich Hertz Institute, the University of Dortmund, Braunschweig Technical University, the **Telecommunications Research** 62

and Development Centre of the German PTT in Darmstadt.

HDTV (high definition television) is a new television standard which aims to achieve even better picture resolution, that is, even clearer television pictures, on larger screens with a width/height ration of 5:3 (previously 4:3). When this improvement in quality will reach domestic television screens, however, remains to be seen, as it would require not only further development of all the equipment required in the television studio but also the allocation of new frequencies for transmission of the programmes, by satellite for example.

Nevertheless, the new technology could soon find its way into production studios



because HDTV productions unlike those from other systems — can be used in a "multimedial" way, which means that as well as being broadcast they can also be transferred to video discs or 35mm film. HDTV technology would therefore enable production studios and television stations to become considerably more competitive.

# **Timing by remote** control

# This versatile self-contained Z80 timer controls eight appliances with 100 on-off settings using coded r.f. bursts injected in the mains supply.

his project was origi- define the operating procedure simple electronic replacement for an electromechanical immersion-heater timer, which had finally siezed after vears of unreliable service. Then I decided that, to be really useful, the timer should be capable of controlling more than just an immersion heater; it should have a timing resolution of one minute; it should be able to store several different switching times for each appliance; and above all it should be self-contained (some advertised controllers need to be hooked up to a home computer).

It was clear to me that a microprocessor was the only economical way of achieving these requirements. I first considered using a single-chip microcomputer, to cut down construction time. One such device, Texas TMS1601A. is mask-programmed to operate as a four-channel timer, but unfortunately it doesn't lend itself easily to remote control with the operating program supplied. Also, it only provides a four-digit display (I wanted to display on and off times together) and can only control four appliances.

Single-chip microcomputers containing eprom, such as Intel's 8748 series, are still more expensive than multi-chip systems, and difficult for home constructors to find. I therefore settled for a general-purpose microprocessor with external memory, since eight-bit processors are widely available at very low prices. Standard eproms are also cheap and they can easily be programmed using a home computer with a suitable attachment.

Having to write the software myself gave me the chance to

nally conceived as a to suit my own whims. The software will be described in sufficient detail for changes to be made by constructors with different ideas of how a timer should operate.

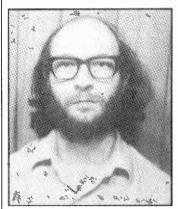
The final design is based on a Z80 processor and has several features not usually found in commercially made timers.

- One appliance is plugged into a 13A mains socket mounted on the control unit. A further seven may be remotely controlled by means of coded r.f. bursts injected into the mains wiring<sup>1</sup>.
- Up to 100 on-and-off settings may be stored. Each

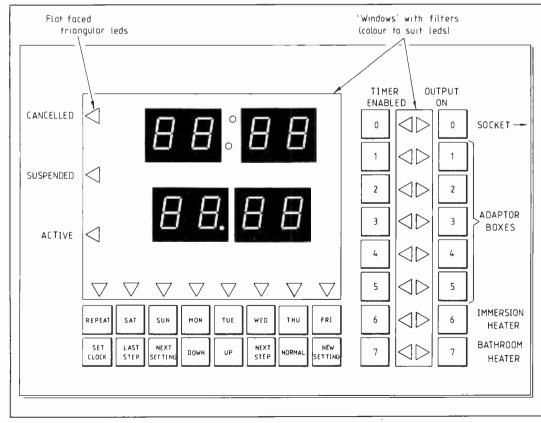
setting can control any combination of appliances, and will operate on any combination of days of the week. It may either repeat the weekly cycle indefinitely, or stop automatically on any specified last day.

- Settings are suspended rather than cancelled on the last day, and may be reactivated without being reentered from scratch. They may also be suspended or cancelled manually at any time.
- All the stored settings, or just those for a particular channel or a particular day, or all repeated settings, may be examined and altered in

## **by Peter Ferris**



The author developed an early interest in electronics, building his first radio at the age of 12 and going on to maintain all the school's many television sets. Having dropped out of a B.Sc. course at London University, he worked his way up through industry, testing, fault-finding and then designing analogue control systems and test equipment. He taught himself about microprocessors the hard way, be designing and building a Z80 development system from scratch, and is now employed as a senior design engineer with Dowty Electronics, where he is working on Z8000-based control systems.



# **REMOTE TIMER**

turn. The display in this mode shows on and off times, day, channel and either active or suspended. For non-repeated settings the last day flashes.

- Settings are entered and altered step by step, with the relevant part of the display flashing at each stage as a prompt. A 'backstep' key allows mistakes to be corrected without having to start again.
- Individual digits of the on and off times are altered with 'up' and 'down' keys. The numbers wrap-round when zero or the upper limit is reached; impossible times such as 27 hours cannot be entered.
- Each channel has an output on/off key and a timer enable/disable key, with leds to indicate status.
- Like most digital watches but unlike most timers, the clock may be corrected in two steps if it is within half a minute of the right time.

Most of these features are provided by the software and incur no additional cost. Those that need extra hardware, such as the remote control, could simply be left out if not required.

The control unit is constructed on three printed-circuit boards: the first contains the processor and memory and most of the logic; the second holds the keyswitches, displays and display drivers; and the third is shared by the optically coupled triac switch, the power supply and the remote-control transmitter.

## **Processor board**

The circuit of the processor board is shown opposite. Circuit  $IC_{2a}$  decodes the upper two address lines to divide the processor memory map into four parts, of which two are used for the eprom and ram. The eprom may be either a 2716 (2Kbyte) or a 2732/2732A (4Kbyte). The timer program fits into 2Kbyte, but as the processor spends much of its time in the halt state, it could be given additional tasks such as a program for decoding the Rugby time-code radio transmissions.2,3.

Circuit  $IC_7$ , a standard Z80 peripheral device, is a fourchannel programmable coun-

ter/timer circuit (c.t.c.). Channel 0 divides the system clock down to 500Hz, which is further divided by channel 2 to provide a 20Hz interrupt for the timekeeping function and the display flasher. Channel 1 generates a 601Hz interrupt for display multiplexing; it is also used as a clock for shift register IC<sub>13</sub>. Channel 3 is spare. CPU address line A<sub>2</sub> is used directly as a chip enable for the c.t.c, which therefore occupies every address in the i/o map which has  $A_2$  zero.

Circuitry IC<sub>5</sub>, IC<sub>8a,b</sub>, IC<sub>2b</sub> decode addresses for the other four i/o ports. The decoder is inhibited when  $A_2$  is low to avoid bus contention with the c.t.c. Lines  $A_3$  and  $A_4$  were arbitrarily chosen for the select inputs. Signal IORQ is used as a data strobe in both directions; it is not necessary to use RD or  $\overline{WR}$ , because the software will never try to write to the input port (IC<sub>6</sub>) or read from the output ports (IC10, 11 and 13). However, signal M, is gated through IC<sub>8a</sub> to inhibit the decoder during the interrupt acknowledge cycles, when  $\overline{M}$ , and IORQ are both low and the address bus retains the program counter contents.

The key matrix is driven by  $IC_{5c-f}$ , their open-collector outputs avoiding the possibility of damage if keys in different rows are pressed together.

The parallel-to-serial data converter is IC<sub>13</sub>, clocked continuously at 601Hz, but as the serial input on pin 10 is tied low the output is normally a stream of zeros. When the processor is required to switch an appliance on or off, it writes a byte into the parallel inputs. Each bit from  $D_1$  to  $D_7$  represents the required state of one of the remotely-controlled appliances (1 for on, 0 for off). The H input of  $IC_{13}$  is tied high to provide a start bit, so  $D_0$  is latched by  $IC_{12b}$  to control the eighth (or zeroth) appliance directly. The write strobe pulse also triggers the monostable  $IC_{9b}$ , which enables the transmitter for the time required to transmit one byte.

The other half of  $IC_9$  is used as a pulse-width modulator to control the display brightness. It is retriggered at 1.66ms intervals when the multiplexing software moves on from one digit to the next. With the control at maximum the pulse is slightly shorter than the trigger interval. The  $\overline{Q}$  output is therefore low, and the display enabled, for most of the time. As the control resistance is reduced the pulse becomes shorter, reducing the display duty cycle and hence the apparent brightness. To match the response of the eye, a logarithmic potentiometer is used with the wiper tied to the clockwise end.

This arrangement also protects the display leds if a fault occurs which stops the display being multiplexed. Under fault conditions,  $IC_{9a}$  is no longer triggered. Its Q output stays high, disabling both the segment driver ( $IC_{11}$ ) and the digit decoder on the display board. Without this protection, the 60 mA segment current would flow continuously in one digit or group of leds, instead of being time-shared between the eight digits and four groups of leds.

Oscillator frequency is halved by  $IC_{12a}$  to provide a 1MHz clock for the c.p.u. and c.t.c. The normal t.t.l. high level is insufficient for the c.p.u. So R<sub>5</sub> pulls the logic 1 level up to +5V. The halt indicator and R<sub>1</sub> are optional. If fitted, the led appears to glow dimly most of the time (it actually flashes at a rate too fast to see), but it visibly blinks off when the clock minute increments, and sometimes when a key is pressed.

## **Display and keyboard**

The circuit of the display/keyboard is on page 66. External connections are numbered to correspond with the processor board circuit.

Integrated circuits 101 and 102 decode the four-bit digit number and drive one digit or group of leds at a time, via transistors 101 to 112. The choice of transistor used is important; a d.c. gain of at least 100 is required at a collector current of 500mA. Power disipation is low because each transistor is only conducting for one twelfth of the time.

Transistors 113 to 120 each drive one segment or discrete led of the digit or group of leds selected by the digit decoder. Resistors 125 to 132 set the current to about 60mA peak (5mA average), which gives an acceptably bright display without resorting to Darlington drivers, especially if high-efficiency leds are used. Although the colon between the hour and minute digits is always on, its leds are pulsed via  $Tr_{101}$  so that the colon brightness tracks the rest of the display.

The 32 keystrokes are connected as a four by eight matrix, which is scanned by pulling each row line low in turn, and then reading the eight column lines together. Column lines with no keys pressed are pulled high by resistors on the processor board. Debouncing the keys is left to the software (to be described later).

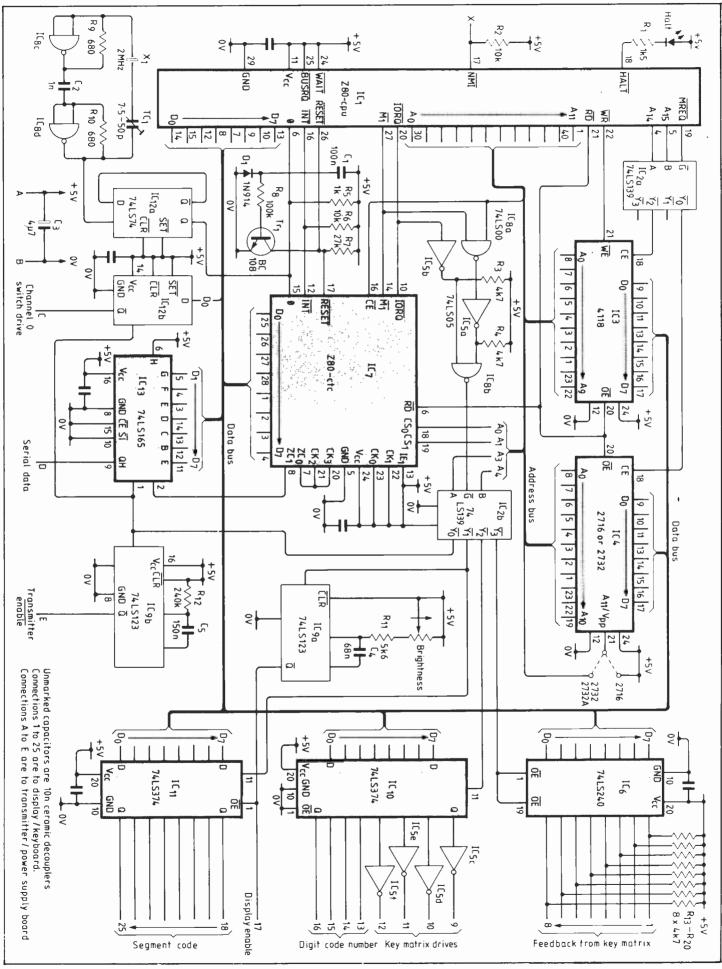
## **Power supply**

The power supply is of conventional design (shown on p.67 with the optocoupled switch and remote-control transmitter). The five volt regulator, IC<sub>203</sub>, should be firmly bolted to the metal box for heatsinking. It should not be insulated from the box because the 0V line, which is connected to the metal surface of the i.c., needs to be earthed as a return path for the transmitted r.f. signal. The 7805 is available either in a TO3 metal can, or in a TO220 plastic package with a metal tab. Either type is suitable.

A voltage doubler generates a raw 20V supply for the transmitter output stage. From this,  $IC_{202}$  derives a stable 12V rail for the oscillator and driver stage.

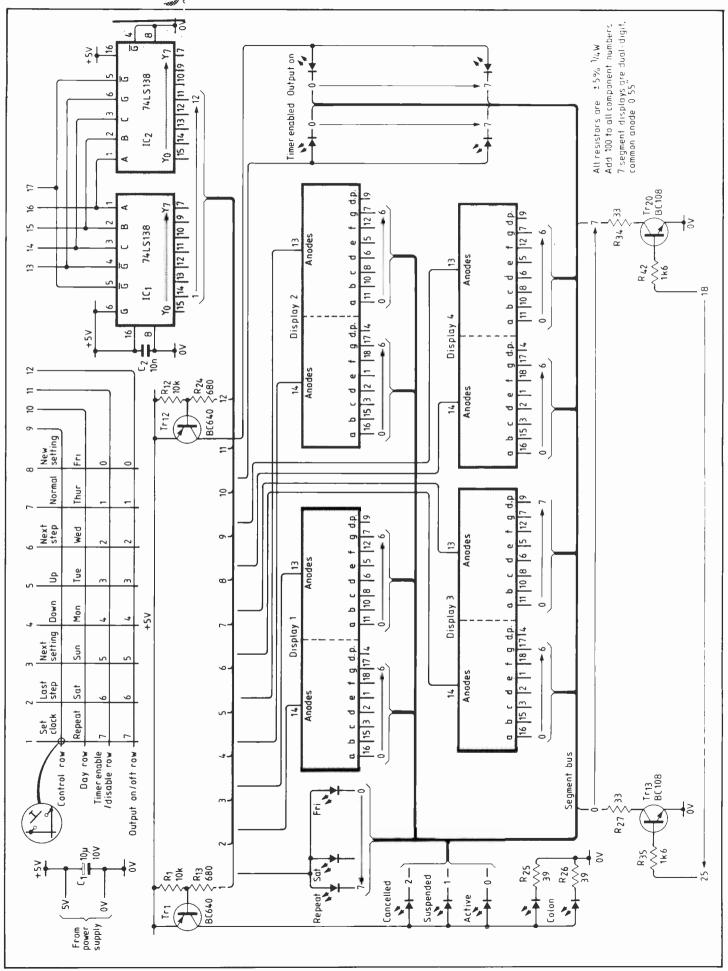
The mains filter unit is optional, and various types may be suitable depending on the environment. The prototype timer operated correctly for several weeks without a filter, but then consistently went havwire when an electric drill was plugged into the other half of the double power point where the timer was plugged in. (The timer behaved itself when the drill was moved to the next power point on the ring main.) Fitting a filter as shown in the circuit completely cured the problem.

The fuse in the mains plug has to be rated according to the largest appliance likely to be plugged into the control unit (i.e. probably a 13A fuse), and this would not protect the mains transformer if, for example,  $D_{204}$  or  $D_{205}$  went shortcircuit. The separate low-



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current fuse (F1) is therefore essential, particularly because the unit will be left on and unattended for long periods. A slow-burning fuse is required, to cater for the relatively high surge current at power-up.

## **Optocoupled** switch

An optically-coupled triac driver,  $IC_{204}$ , also contains a zero-voltage-crossing circuit. The output stage is itself a small triac and for loads of less than 20 watts, such as a portable radio, the external triac and  $R_{221}$  may be omitted. (Connect pins 6 and 4 of  $IC_{304}$  directly to the live input and output lines respectively).

Remember that high-current triacs can have significant off-state leakage currents, especially when hot; a 5 watt load connected to a 15A triac may still have several tens of volts across it when the triac is off. Ideally, the triac's r.m.s. current rating should be about 50% higher than the maximum load current, but in practice it is possible to use, say, 15A triacs for most receivers, with one or two lower-current ones for appliances such as radios or electric blankets.

Snubber components  $C_{211}$  and R<sub>220</sub> are included to limit the rate of rise of voltage across the triac when an inductive load is switched off, which could otherwise retrigger the triac as soon as it is switched off. Their optimum values depend on the maximum load current and the critical dV/dT rating of the particular triac used, though 1  $k\Omega$  and 47nF/500V is a widelyused combination. They may be omitted if only resistive loads are to be controlled, or if the phase difference between voltage and current is only a few degrees.

## **Remote control transmitter**

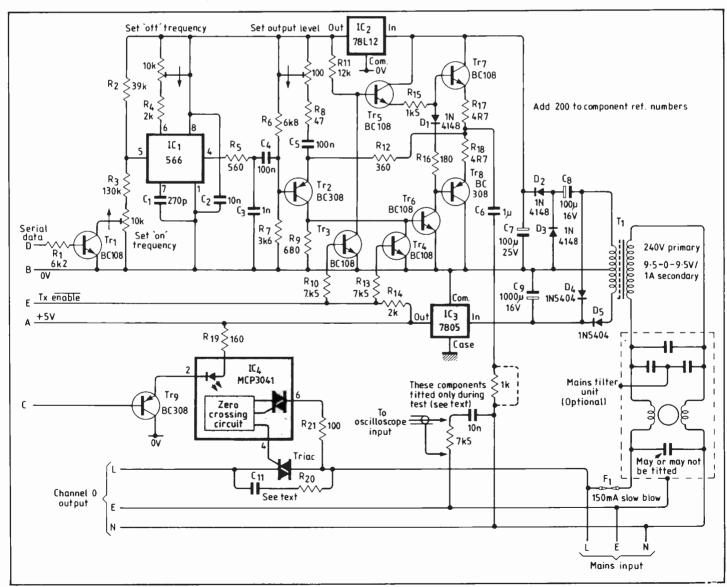
The principles involved in using domestic mains wiring to carry data signals are discussed in reference 1, so I will not go into too much detail here. The neutral-earth pair of wires is used for safety.

Frequency-shift keying is used for data transmission, i.e. two carrier frequencies represent logic 0 and logic 1. On page 67, IC<sub>201</sub> is a voltage-controlled oscillator whose frequency is switched by the serial data. The triangular waveform from pin 4 is filtered by  $R_{205}$ and  $C_{203}$ , so that the power amplifier delivers a reasonably pure sinewave to the mains wiring. Transistors 203 and 204 keep the output stage biased off except during transmissions.

The choice of carrier frequencies depends on the impedance/frequency characteristic of the mains wiring, which varies considerably from house to house. Measurements made at several locations suggest that an impedance peak can be expected somewhere between 100 and 500kHz; a simple method of finding the peak will be described later, with other tips on testing and setting up the system.

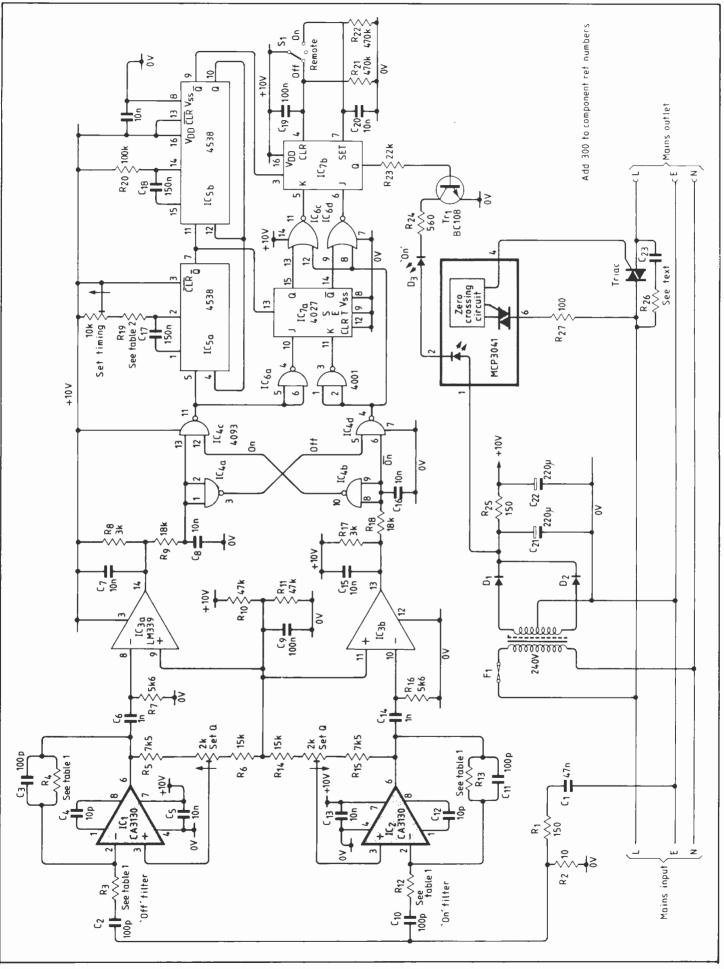
## **Remote control receiver**

A typical f.s.k. receiver might consist of: a bandpass filter with -3dB points at the two carrier frequencies; a phaselocked loop to recover the original serial data; a serial-toparallel converter (usually a uart chip which can detect transmission errors); and a decoder, to determine whether the received command is intended for this receiver, and if so whether to switch the appliance on or off. If two or



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# **REMOTE TIMER**



more appliances are to be switched together, a separate data word must be transmitted for each one.

Since a receiver is required for each remotely-controlled appliance, I rejected this approach on the grounds of cost. Instead, I designed a novel receiver circuit which offers good immunity from mains-borne interference at a modest outlay.

The circuit is shown opposite. A Wien-bridge bandpass filter,  $IC_{301}$ , is tuned to the lower (off) carrier frequency.

The comparator  $IC_{303a}$  serves three purposes. First, the base/collector junction of the inverting input transistor acts as a clamp diode, holding the negative peaks of the waveform to about -0.6V. Secondly, the positive peaks of the shifted waveform are compared with the half-supply-voltage bias on the other input; if the peak-topeak amplitude exceeds the bias, the comparator output switches low. Thirdly, the open-collector output stage acts as a rectifier; C<sub>307</sub> is charged rapidly by the output transistor, but discharges more slowly through R<sub>308</sub>.

The output therefore remains low, although with some ripple, when a large enough signal is received. The filter  $R_{309}$  and  $C_{308}$  is to remove short pulses caused by spikes on the mains wiring.

The higher (on) carrier frequency is detected in the same way by  $IC_{302}$  and  $IC_{303b}$ . The other two comparators in  $IC_{303}$  are not used. (If you're wondering why I didn't use the LM393 dual comparator, which has nearly identical characteristics, check the advertised prices — the LM339 quad version is usually cheaper.)

The normal operation of the rest of the circuit is best understood by referring to the waveforms, above. The serial data word always starts with an onbit. The next seven each represent the required state of one of the remotely-controlled appliances. The stop bit is always an 'off'.

The start bit triggers monostable  $IC_{305a}$ , via  $IC_{304b,c}$ . The Q output of  $IC_{305a}$  immediately triggers the second monostable,  $IC_{305b}$  the Q output of which is fed back to prevent retriggering of either monostable during the receive sequence.

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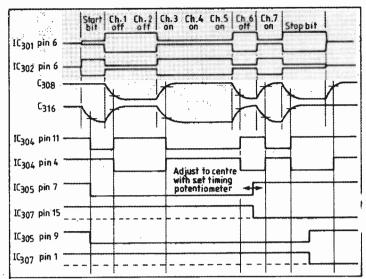
The first monostable is adjusted so that its pulse finishes in the middle of the data bit corresponding with the particular receiver (channel 6 in the case of Fig.5). This trailing edge latches the incoming data bit into  $IC_{307a}$ , from where it is transferred to  $IC_{307b}$  by the trailing edge of the second monostable pulse during the stop bit.

Extra gating circuits are included to provide noise immunity. To trigger the monostables, a burst of mains-borne noise has to be at or very near the on-frequency for a long enough time to discharge C316 below the logic 0 threshold. It must have no significant component at the off-frequency, since that would block the onsignal at IC<sub>304c</sub>. At the end of the first monostable pulse, the noise must still be present, and at only one of the carrier frequencies; if neither or both are present, the J and K inputs of  $IC_{307a}$  would both be low, and the latch would not change state. At the end of the second monostable pulse the noise must still be there, but now only at the 'off' frequency; otherwise, the J and K inputs of  $IC_{307b}$  would be held low via  $IC_{\rm 304d}$  and  $IC_{\rm 306c,d},$  and the output wouldn't switch. Even if a noise burst managed to get over those three hurdles, there is of course a 50% chance that it would only succeed in switching the output to the state it was in already.

As with the conventional receiver, the output may fail to switch if noise occurs during a signal transmission. To overcome that problem, the control unit repeats each timed transmission, ten seconds after the stored switching time.

## Local control

The three-position switch allows the appliance to be switched on and off locally, overriding the remote control. This is not strictly necessary when the appliance is to be powered via a mains socket mounted on the receiver box, as the appliance could be plugged directly into a wall socket or left unplugged. But some appliances, such as an immersion heater or a bathroom heater, must be wired directly into the receiver because power sockets aren't allowed in bath-



rooms. The switch is then the only means of local control (or any control if the transmitter fails). A switch is used rather than a pair of push-buttons because it has the advantage of providing absolute immunity from mains-borne interference including from mischievous members of the household trying to switch the bathroom heater off when you've just settled down for a long soak! Capacitors C319 and C320 prevent noise picked up on the switch wires reaching the SET and CLR inputs to the latch. They are connected to opposite supplies to ensure that if a power cut occurs while the switch is set to 'remote', the appliance is switched off when power is restored.

The d.c. supply for the receiver is derived from a miniature mains transformer. The receiver circuit is tolerant of quite wide variations in supply voltage, and no regulator is necessary. Load current is about 17mA for the receiver itself, plus a further 15mA through the optocoupler and panel leds when the output is switched on.

### To be continued

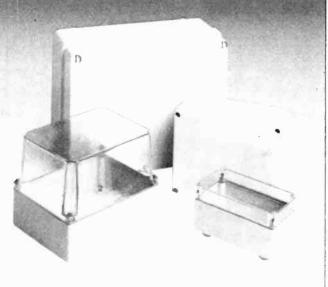
**Postscript.** I have been asked several times why the multiplexing interrupt rate is 601Hz rather than a round figure like 600Hz. The reason is that the c.t.c. channel has a four-bit prescaler, which divides the 1MHz c.p.u. clock down to 62.5kHz. This is further divided by the programmable eight-bit counter. Dividing by 105 (decimal) gives 595.24Hz; dividing by 104 gives 600.96Hz, and that's as close to 600Hz as it's possible to get. The serial data word always starts with an onbit; the next seven each represent the required state of one of the remote appliances; the stop bit is always an 'off'.

References

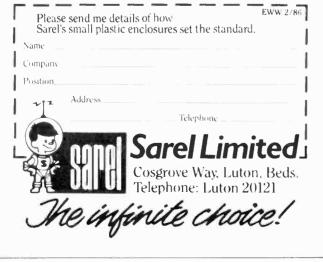
1. The Intelligent Plug, N.McArthur, A. Wingfield and I.Witten, *Wireless World*, December 1979 2. Micro-controlled radio-code clock, N.E. Sand, *Wireless World*, June/July 1982

3. Rewbichron 2, J. Robinson, Radio & Electronics World, April/May 1983

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by R.F. Coates

# 68000 board —

## Bob Coates continues his description of the Kaybug monitor software with a look at its system calls

he monitor offers 26 system calls or subroutines for user

programs to draw upon. These handle keyboard input and screen output functions but also include number base conversions and a 32-bit divide routine.

The calls are invoked by setting up the appropriate entry conditions and then executing a trap #11 instruction followed by a word-sized number indicating which call is being made.

All calls except *renter* when completed return to the user program and continue with the next instruction, as if a jsr instruction had been used.

Table 1 lists the calls available in version 1.0 of the monitor along with the number that follows the trap#11 instruction.

A more detailed description of each call is now given.

renter: this call terminates a users' program and transfers control back to the monitor, giving the monitor prompt.

Entry conditions: none.

Exit conditions: not applicable.

outch: outputs a character in  $d_0$ , b, then checks whether 'break' <del> or 'hold' <conreceived. If 'break' received, does not return to caller but aborts user program and returns to monitor. If 'hold' received, waits until a different character is received before  $a_6 = address$  of string terreturning to caller.

Entry conditions:  $d_0.b = Ascii$ character to be output.

Exit conditions: all registers preserved.

sendch: outputs character in then returns to caller without checking for 'break' or 'hold'.

Entry conditions:  $d_0.b = Ascii$ character to be output.

Exit conditions  $d_0.b$ . undefined, other registers preserved.

outs: outputs a space character. Uses outch (see comments about 'break' and 'hold'). Entry conditions: none.

Exit conditions: all registers preserved.

**spaces:** outputs a number of space characters as indicated

Entry conditions:  $d_0.w = number$  of spaces re-

Exit conditions: all registers preserved.

pdata1: outputs Ascii string pointed to by  $a_6$  and terminated by a null byte (00). Sends all bits of each character unmodified. Uses outch.

Entry conditions:  $a_6 = address$ of start of string.

Exit conditions:  $d_0$ , b = 0;  $a_6 = address$  of string terminator+1; all other registers preserved.

pdatam: As *pdata1*, but takes account of the display mode currently in use.

Characters with bit 7 clear, i.e. normal Ascii characters, are always output. But the outputting of characters with bit 7 set depends upon the display mode selected.

In 80-column mode, characters with bit 7 set are ig-**ELECTRONICS & WIRELESS WORLD FEBRUARY 1986** 

trol-S> characters have been 40-column mode, characters have bit 7 stripped and are output as normal characters.

> Entry conditions:  $a_6 = address$ of start of string.

> Exit conditions:  $d_0.b = 0$ ; minator + 1; all other registers preserved.

> query: outputs a '?' and sounds bell/buzzer/bleep. Uses outch.

Entry conditions: none.

Exit conditions: all registers preserved.

outhex: outputs contents of lowest four bits in d<sub>0</sub> as an Ascii hex character. Uses outch.

Entry conditions:  $d_0 = hex$ value.

Exit conditions:  $d_0 =$  undefined; other registers preserved.

out2hx: outputs contents of lowest eight bits in do as two Ascii hex characters. Uses outch.

Entry conditions:  $d_0 = hex$ value.

Exit conditions:  $d_0 =$  undefined; other registers preserved.

out4hx: outputs contents of lowest 16 bits in  $d_0$  as four Ascii hex characters. Uses outch.

Entry conditions:  $d_0 = hex$ value.

Exit conditions:  $d_0 =$  undefined; other registers preserved.

out8hx: outputs contents of d<sub>o</sub> as eight Ascii hex characters. Uses *outch*.

Entry conditions:  $d_0 = hex$ value.

Exit conditions:  $d_0 =$  undefined; other registers preserved.

crlf: outputs a carriage return and line feed. Uses outch.

Entry conditions: none.

Exit conditions: all registers preserved.

getch: waits for character to be received from input.

When character received, checks whether 'break' or 'hold': if 'break', does not nored and there is no output. In return to caller but aborts user

Call	<u>no. Name</u>	Function
0000	) renter	Exit users' program and rementer monitor
0001	outch	Output character with pause check
0002	sendch	Output character without pause check
0003	outs 3	Output a space
0004	spaces	Output d0.w spaces
0005	i pdata1	Output string
0006	pdatam	Output string according to column mode
0007	query	Print '?' and sound bell
0008	outhex	Convert d0 nibble to ASCII and print
0009	) out2hx	Print two hex characters in d0
0004	out4hx	Print four hex characters in 'd0'
0008	3 out6hx	Print eight hex characters in d0
0000	crlf	Print carriage return and line feed
0000	) getch	Get character with Pause check
0006	readch	Get character without pause check
000F	gethex	Get hex character
0010	l conhex	Convert character to hex
0011	g Pch	Get and echo character
0012	get2	Get two ASCII characters
0013	hexin	Get string of hex characters in
0014	. getadr	Get address
0015	5 range	Get start/end addresses allowing previous values
0016	5 strend	Get start and end addresses
0017	binbcd	Binary to bcd conversion
0018	bcdbin	Bcd to binary conversion
0019	dîvîde	32-bit two's complement divide

72

# by d<sub>0</sub>.w. Uses *outch*. quired, 1 to 65535.

program and returns to the monitor. If 'hold', waits for another character to be received (other than 'break' or 'hold') before returning to caller. Returns with Ascii character in  $d_0.b$ , bit 7 forced clear.

Entry conditions: none.

Exit conditions:  $d_0.b = 7$ -bit Ascii character; all other registers preserved.

**readch:** waits for character to be received from input and returns with it without checking for 'break' or 'hold'.

Entry conditions: none.

Exit conditions:  $d_0.b = 7$ -bit Ascii character; all other registers preserved.

gethex: gets Ascii character and converts it to hex, returning with hex value in  $d_0$  and 'c' bit in status register clear. If character was not a hex character, i.e. not 0-9 or A-F, then returns instead with Ascii code and 'c' bit set.

Uses *getch*: see comments about 'break' and 'hold'.

Entry conditions: none.

Exit conditions: (hex character)  $d_0.b =$  hex equivalent of character, s.r. 'c' bit=0; (non-hex character)  $d_0.b =$  7-bit Ascii character, s.r. 'c' bit=1. All other registers preserved.

**conhex:** as *gethex*, but does not get character from input. Enter instead with Ascii character in  $d_0$ .b.

Entry conditions:  $d_0.b=7$ -bit Ascii character.

Exit conditions: (hex character)  $d_0.b =$  hex equivalent of character, sr 'c' bit = 0; (nonhex character)  $d_0.b =$  7-bit Ascii character, sr 'c' bit = 1. All other registers preserved.

**gpch:** gets character from input and echoes to output. Returns with it in  $d_0$  b; if lower case, converting it to upper case. Uses *getch*.

Entry conditions: none.

Exit conditions:  $d_0.b = 7$ -bit Ascii character (upper case); all other registers preserved.

get2: gets two Ascii characters. Calls gpch twice and assembles the two characters in  $d_0.w$ .

Entry conditions: none.

E x i t c o n d i t i o n s :  $d_0.w, b_{15}-b_8 = first Ascii$ character;  $b_7-b_0 = second Ascii$ character.

**hexin:** gets string of characters from keyboard, converting them to hex and ELECTRONICS & WIRELESS building in  $d_1$ . Returns after a non-hex character with the last eight hex characters entered in  $d_1$ , leading zeroes being inserted if necessary. Register  $d_0$ contains the non-hex terminating character and  $d_2$  the number of hex digits entered. Uses *gbch*.

Entry conditions: none.

Exit conditions:  $d_0.b = non-hex$  terminating character;  $d_1.l = eight$  hex characters;  $d_2.l = number$  of hex characters entered; all other registers preserved.

**getadr:** prompts operator to enter an address. Returns with address in  $a_0$  and number of characters entered in  $d_2$ . Rejects odd addresses. Uses *gpch*. Entry conditions: none.

Exit conditions:  $a_0 = address$ entered;  $d_2 = number$  of characters entered.

**range:** prompts user to enter a beginning and end address, giving the option of re-using the last used values (as used by the PR command). If values entered, updates ram registers *begadr* and *endadr* with new values. If just < cr> entered, registers left unmodified.

For Kaybug version 1.0, begadr is at  $400000_{16}$  and endadr at  $400004_{16}$ .

Entry conditions: none.

Exit conditions: *begadr* and *endadr* updated if new values entered; all registers preserved.

**strend:** prompts user to enter block start and end addresses (as used by LW command). Returns with start address in  $a_2$ and end address in  $a_1$ .

Entry conditions: none.

Exit conditions:  $a_1.1 =$  end address;  $a_2.1 =$  start address; all other registers preserved.

**binbcd:** converts a 27-bit binary number in  $d_0$  to eight packed b.c.d. digits in  $d_1$ , with 'c' bit in status register clear.

If input value exceeds maximum allowed  $(5F5E0FF_{16})$  then d<sub>1</sub> is undefined and the 'c' bit is set.

Entry conditions:  $d_0.1 = 27$ -bit binary number.

Exit conditions (valid input):  $d_1 = 8$ -digit packed b.c.d. result, s.r. 'c' bit = 0; (invalid input)  $d_1 =$  undefined, s.r. 'c' bit = 1. All other registers preserved.

**bcdbin:** converts an 8-digit packed b.c.d. number in  $d_0$  to binary equivalent in  $d_1$ .

verting them to hex and Entry conditions:  $d_0.l =$  ELECTRONICS & WIRELESS WORLD FEBRUARY 1986

#### **Motorola S-format files**

This format provides a means of Ascii-encoding binary object files for transfer between systems.

31		s	·** ÷	¥	e An forske		5)) 1310-	i gigan se se se ga ga anta anta anta anta			
	s	n	S	ze	Add	ress		Data	C	Checksum	
	·	in yn Jan ar	•••••		491.940) 7	ŵs	*12 .:\$ *				
S n	(1) the second s										
size				i	indicates the number of bytes (hex) in this						
add	address record (address, data and checksum). is four or six hex characters for record types 1 and 2 respectively. For types 0 an 9 it is 0000.										
data				i	is the block of ascii encoded binary data.						
<b>checksum</b> Each record typically contains a 16-byth block, but may be more or less. is the one's complement of the sum of the size, address and data.											

Each record is followed by carriage return and line feed. This example shows the S-file produced for the object code for example 1 (to be given in the next article).

S00900006578616D3120FA

S21440040070007200207C0000010052800601000A45 S208400410558860F670

\$903000FC

The header record holds the name of the object file as it was on the development system which produced this, 'EXAM1'.

eight-digit packed b.c.d. number. Exit conditions:  $d_1.l = binary$ result; all other registers preserved. **divide:** integer-divides the number in  $d_0$  by the number in  $d_1$ . Result is in  $d_4$  with remainder in  $d_5$ . All numbers are in 32-bit two's complement binary. Sign conventions used are:

dividend	divisor	result	remainder
+	+	+	+
+	_	-	-
-	+	-	-
-	_	+	-

Entry conditions:  $d_0 = divi-$ 

dend;  $d_1 = divisor$ .

Exit conditions:  $d_4$  = result;  $d_5$  = remainder; all other registers preserved.

#### **Exception handling**

The way in which interrupt exceptions are handled by the 68000 is to go to a specified location according to the exception designation, fetch a long

Assignment	Ram addr	ess	
Level 1	400016	ו ו	W
Level 2	40001C		lex
Level 3	400022	( Autovector	1.
Level 4 Level 5	400026	interrupts	ın
Level 5 Level 6	40002E 400034		us
Level 7	40003A		
rever ,	400034	, I	th
Trap #0	400040	1	
Trap #1	400046		fo
1rap #2	40004C		lm
Trap #3	400052		1
Trap #4	400058	Trap	si
Tiap #5	40005E	Pinstructions	ca
Trap #6 Trap #7	400064 40006A	instructions	
Trap #7 Trap #8	400070		e
Trap #8	400070		a
Trap #10	400076		1
viup wio	400011	′ I	th
Vector 54	400082		
Vector 65	400088		lus
Vector 66	4 O D O 8 E	lices	1
Vector 67	400094	User	al
vector 68	40009A	interrupts	ju
Vector 69	4000A0		1 "
Vector 70 Vector 71	4000A6 4000AC		th

| word (four-byte) address from it d re-start processing from at address.

There is a slight problem th the Kaycomp in that these ception vector addresses are the eprom space and so a er program cannot alter em.

To circumvent this difficulty, each interrupt vector that ay be used on the Kaycomp, a -byte block of ram is alloed. The exception vector in rom contains this address d so processing will jump to is location.

The six-byte space allows the er program to insert a 'jump solute, long' instruction, the mp destination address being at of the user's exception sere routine.

The exception processing sequence will thus be

1. Get vector and go to that address.

2. Execute jump instruction at that address.

3. Continue processing at jump destination address.

Ram 'jumpers' are provided for all autovector interrupts and eight user interrupt vectors are also catered for. These are for vector numbers from 64  $(40_{16})$ to 71  $(47_{16})$  and would be used by 68000 peripherals interrupting on  $IPL_1$ .

Trap instructions with vector numbers from 0 to 10 may also be used in user programs. These are handled in the same manner as interrupts. Trap numbers 11 to 15 are reserved for use by the monitor.

A complete list of these 'jumpers' is given in Table 2.

The remaining assigned system exceptions, such as Bus Error, Address Error etc, cannot be intercepted by the user program but can cause a return into the monitor with an appropriate message displayed.

#### To be concluded.

The DMS8832 memory module for this project is available under the type number EDH8832C (with the suffix - 15PC for the 150ns version) from MicroCall, Thame Park Road, Thame, Oxfordshire; tel. 084421-5405.

## by Tom Ivall

# D.B.S. — a plan in search of some users

s David Withers mentioned in the December issue (p.75) the most recent ITU pronouncement on direct broadcasting by satellites came at last year's World Administrative Radio Conference. This meeting, WARC 85, formally approved the d.b.s. plan previously worked out in detail at RARC 83, a regional conference specially convened in 1983 for ITU Region 2, which broadly means the Americas.

A similar situation occurred in 1979 when the big WARC 79 world conference formally agreed to the d.b.s. plan put together for ITU Region 1 (Europe, Africa, USSR) and Region 3 (Far East and Australasia) at WARC 77, which had been entirely devoted to satellite broadcasting. As a result we now have a complete world-wide plan - in frequencies, channels, powers, orbital positions etc. all signed and

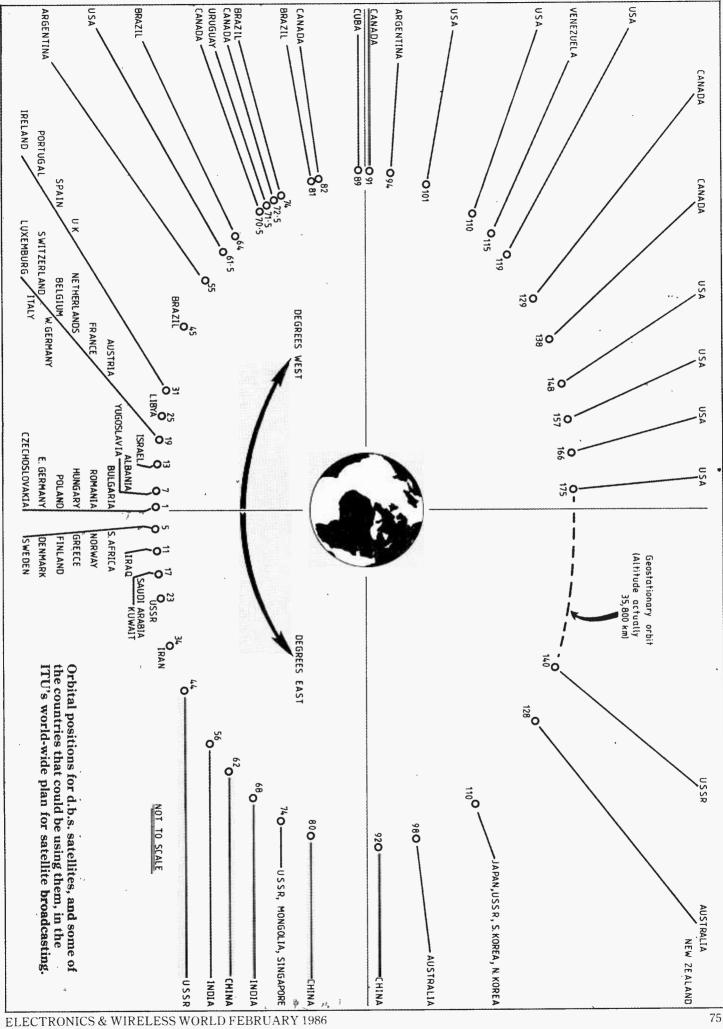
sealed and just waiting for someone to start using it for full broadcasting operation.

The Japanese came near to starting the first d.b.s. service in early 1984 when they launched a satellite from their Tanegashima Space Centre and placed it in geostationary orbit in their allocated orbital position of 110°E. But, as was reported in E&WW, faults rapidly developed in the transponders' travelling-wave tubes and the whole project had to be downgraded to an experimental status.

Meanwhile, we have seen the unexpected development of what has become known as 'semi-d.b.s.' or 'quasi-d.b.s.'. Transmissions from communications satellites, originally intended purely for distribution of television and sound programme feeds within the broadcasters' own networks, have become, willy-nilly, a sort of broadcasting in themselves. When cable tv systems use

these programme feeds the signals are in any case 'broadcast' to a number of widely-spaced head-end receiving stations. And when reception of these transmissions by individuals was legalized, the comsats thereby became broadcasting stations in an even fuller sense, while still doing the job they were originally intended for. D.J. Standen's article in the December 1985 issue describe this 'quasi-d.b.s.' phenomenon in detail.

International planning for satellite systems has actually been under way for two decades or more. The ITU held its first conference on extraterrestrial radio communication as long ago as 1963. This was when comsats were first beginning to establish themselves as practical relay stations. The second World Space Telecommunications Conference followed in 1971. Then came WARC 77 and **ELECTRONICS & WIRELESS WORLD FEBRUARY 1986** 



RARC 83 as mentioned above. What has emerged, however, has been somewhat confusing. The planning criteria and methods used at WARC 77 and RARC 83 are markedly different from each other. No doubt this is due to the lapse of time between the two events, the development of spacecraft and radio technology during this period, and the different interests and assumptions of the two groups of people concerned.

#### 

### ... the whole situation here is still confused and uncertain and likely to remain so for some years

Both conferences started from the 12-GHz frequency band specifically allocated to satellite broadcasting. Within this, Region 1 is allowed to use 11.7 to 12.5 GHz, Region 2 has 12.2 GHz, while Region 3 can operate in the two sub-bands 11.7 - 12.2 GHz and 12.5 -12.75 GHz. From these allocated blocks of frequencies WARC 77 and RARC 83 worked out frequency channels, wave polarizations, orbital positions, coverage areas or 'footprints' and minimum and maximum radiated powers.

WARC 77 divided up the 800 MHz between 11.7 and 12.5 GHz into 40 channels spaced at equal intervals of 19.8 MHz. Each of these channels is 27 MHz wide, so there is some frequency overlap, but this is taken care of by opposite directions of circular polarization. RARC 83, on the other hand, split up the 500 MHz from 12.2 to 12.7 GHz into 32 channels, each of 24 MHz width. The obvious channel overlapping here is again dealt with by opposite directions of circular polarization. The spacing between transmissions with similar polarizations is 29.16 MHz.

In both plans the channels are grouped into 8 channelfamilies. But in WARC 77 each family consists of five frequencies spaced at four-channel intervals, while in RARC 83 each family has four frequencies spaced at these intervals.

But perhaps the most marked difference between the two regional plans is in the orbital positions, or 'slots', chosen for the satellites in the geostationary orbit. In WARC 77 these slots for Regions 1 and 3 satellites are a regular 6° apart. In RARC 83, however, they are not equally spaced but have a variable spacing which ranges from as close as 1° to as far apart as 11°.

Then in WARC 77 each country in Regions 1 and 3 has been assigned just one orbital position, and it shares this with other countries. An exception is the USSR which has five slots because of its huge geographical area. But in RARC 83 several countries have more than one slot. The USA, for example, has eight in all.

Another difference between the two schemes is in the number of channels assigned to each country. WARC 77 gives each country a standard ration of five channels, whereas in RARC 83 there is considerable variation. Some small Region 2 countries have as few as four channels while, for example, the eastern region of the USA alone has as many as 128.

In the power flux densities specified, however, the two plans are very similar. At the outer edge of each 'footprint' WARC 77 lays down a requirement for a minimum of – 103 dBW/m<sup>2</sup>, while RARC 83 specifies a minimum of – 107 dBW/m<sup>2</sup> or a little higher in some situations.

The different structures of the two plans are significant in relation to their capacity to deliver programmes and their flexibility of use in different situations. What does this mean in practice? As an illustration, E.R. Reinhart of the Satellite Television Corporation in the USA has anlaysed the differences shown up by projected implementation of WARC 77 in Africa and RARC 83 in Latin America. He concludes that Latin American countries would not only get greater numbers of channels for broadcasting but would also have greater flexibility for system implementation than would be possible for the countries of Africa.

The accompanying diagram gives some idea of what the whole d.b.s. plan looks like in

terms of orbital positions for different countries around the geostationary orbit. Because there are 150 or more countries in the world the diagram does not attempt to show them all it would be very difficult to read. Instead we have arbitrarily selected the fifty or so countries that can be expected to be the first in the field in starting up d.b.s. services. On the assumption that these are likely to be the richest, most industrialized nations, the broad criterion has been to take those at the top of the league table of Gross National Product per capita. This means roughly the major capitalist and communist countries. An exception to this rule of thumb is India, which has been included because of its outstanding pioneering work in satellite broadcasting. It was the first country in the world to set up an experimental d.b.s. system - the famous Satellite Instructional Television Experiment of 1975 (reported in this journal).\*

Television transmission standards used for d.b.s. will depend on decisions outside of those laid down in the ITU world-wide plan. They could be, for example, predominantly NTSC in North America and Japan and predominantly Cmac packets in Western Europe, but the whole situation here is still confused and uncertain and likely to remain so for some years.

Britain, of course, is still a long way from establishing any kind of d.b.s. service. The government-inspired Unisat consortium collapsed last year; a new group called Britsat has put up another proposal and the government is reported to be considering this and other current suggestions. Meanwhile, it looks as though France or Germany may be the first to launch a d.b.s. service in Europe. This would be the outcome of Eurosatellite, a Franco-German manufacturing consortium which is building two similar TDF-1 for spacecraft, the France and the TV-Sat 1 for West Germany. Electronic component manufacturers in Germany have already developed and put on the market various new components for receiver designs intended to work from TV-Sat transmissions.

\*December 1975, p.549

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design manufacture and supply	DU36/87 0.06 E184 0.00 PY500A 210 3E29 19.00 6F12 1.50 12E1 18.95 DY802 0.70 E186 0.95 CQV03105.95 354 0.70 6F14 1.15 12JSG1 0.55 DY802 0.70 E186 0.95 QQV03105.95 354 0.70 6F14 1.15 12JSG1 0.55 DY802 2.80 E190 1.00 10.00 4B32 18.25 6F15 1.30 12K7G1 0.70 E920 2.80 E191 6.50 QQV03-20A 5F4GY 3.35 6F17 3.20 12K8GT 0.80
POWER AMPLIFIERS HIGH POWER ASSEMBLIES CONTROL CIRCUITRY	EA76         225         EL95         0.80         27.50         5UAG         1.40         6F23         0.75         120/01         0.80           EA800         0.80         EL504         2.70         QQV03/25A         5V4G         0.75         6F24         1.75         12SC7         0.85           EB10         0.60         EL504         2.70         QQV03/25A         5V3GT         0.95         6F33         10.50         12SH7         0.85           EBC30         0.60         EL504         2.70         QQV03/440A         523         2.80         6FH8         17.80         12SU7         1.70         12SU7         0.70           EBC90         0.90         E1821         8.20         Q28.50/49.50         524G         1.25         6G48A         1.95         12SU7         1.46           EBF80         0.60         E1822         9.95         QV03/12         5.75         524G         1.45         6G48A         1.95         12SU7         1.45           EBF80         0.80         EL180(SE)4 50         SP61         1.80         6/30L2         0.90         6H6         1.60         12Y4         0.70           EC52         0.65         EM80         0.85<
for application in	ECC81 0.99 EV86/87 0.60 UBF80 0.70 6AK5 0.55 6J567 0.90 1966 10.35 ECC83 0.65 EV88 0.65 UBF89 0.77 6AK8 0.66 6J6 0.85 1945 39.55 ECC83 0.65 EV88 0.65 UBF89 0.77 6AK8 0.66 6J6 0.85 1945 39.55 ECC84 0.60 EZ80 0.70 UCC84 0.85 6AL5 0.60 6J6 V.80 2.80 2051 0.80
INDUSTRY PUBLIC ADDRESS HI-FI	ECC85         0.60         E.G.W         0.50         UCF80         1.30         6AM5         4.20         GJS65         6.40         20P1         0.65           ECC88         0.80         0.450         1.30         UCH82         2.50         6AM6         4.20         GJS65         6.40         20P1         0.65           ECC88         0.89         0.4501         1.30         UCH81         0.75         6AM6         1.50         6K7         1.45         25L6         0.40         5.50         25Z4G         0.76           ECC840         0.90         6Z33         4.20         UCH81         0.75         6AN8         2.50         6K06         4.60         35W4         0.80           ECR80         0.85         G233         4.20         UCH81         0.35         6AO4         4.40         6K06         4.60         35W4         0.80           ECR80         0.85         G237         3.20         UCH81         0.35         6AO5         1.30         6L6GC         3.70         807         1607.240*           ECR801         1.05         G237         3.95         UF85         0.95         6AS5         4.11         6L6GC         3.70         812A
available	ECI.80         0.70         ML6         2.80         UY82         0.70         6A/6         1.25         6O/76         1.30         832/A         8.90           ECI.82         0.75         N78         9.90         UY85         0.85         6A/46         1.30         856/A         5.05           ECI.82         0.76         N78         9.90         UY85         0.85         6A/46         1.30         856/A         5.05           ECI.85         0.80         OA2         0.70         VR105/30 1.25         6AX5GT         1.30         856/A         9.80           ECI.86         0.90         OB2         0.80         VR150/30 1.35         66A/6         ESJ7         1.80         93/A         19.80
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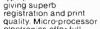
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Answering machines are very popular these days and we are pleased to offer a new model - the Betacom LR3 — which features a double tape, one to record your announcement and the other to record the messages. Unlike single tape answering machines, it is necessary to record the outgoing message only once and each caller will hear it repeated.

The outgoing message may be changed by using the inbuilt microphone and the erase facility will clear the incoming message tape. If you are at home, a monitor allows you to hear the caller and if you wish to speak, use your telephone as normal. A fast forward cue search locates the next message and there is a marker sound to indicate the end of the day's messages and also a volume control. The Betacom LR3 is compact and in a smart grey and mushroom plastic. It is extremely good value at the special price of only £79.95 inc. p&p and two free tapes.

It is easily connected to the power supply with the lead provided and a BT Approved jack plug is attached. The Betacom LR3 is BT Approved and Betacom offers a 12 month guarantee with a fast and efficient after sales service. If you are not satisfied return within 7 days for a refund to Betacom, D.M. Baylin Ltd., 76 Marylebone High Street, London, W1M 3AR

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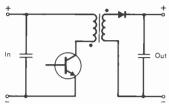
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# **Power supplies from Sweden**

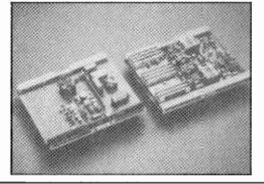
A range of 32 switch-mode supplies and a made-to-measure service based on a further 19 basic designs has been launched by Rifa. Most of the supplies use a 'flyback' circuit. This has only one switching transistor and one diode.



A transformer with a specific inductance stores energy while the transistor is on. The energy cannot be passed to the secondary output as it is blocked by the diode. When the transistor is cut off, the stored energy is discharged with opposite polarity allowing the diode on the secondary side to conduct. This technique allows considerable reduction in component-count as is shown by the picture. Both circuits perform the same task.

The standard range includes d.c.-to-d.c. converters in ratings from 20 to 250W, with up to four outputs; a.c.-to-d.c. power supplies from 60 to 240W; customized units from 5 to 250W.

The range has become available as a result of reorganization within the Ericsson group. Rifa, a subsidiary has taken over the power supply division of its parent which originally produced supplies for Ericsson's own products. Rifa AB, Market Chambers, Shelton Square, Coventry CV1 1 DJ. EWW 209 on reply card.



# **CP/M on a PC**

For those who have grown fond of their CP/M software and yet want to graduate to a computer running PC-DOS or MS-DOS, rescue is at hand from G-Tek, an American company who have produced a CPemulator. This package makes your IBM PC, or similar, think that it is being run by a Z80 with CP/M. Two parts are used; a conversion utility to load CP/M programs onto IBMcompatible discs, and a 'bind' utility that attaches the CPemulator to the programme. All the software developed on a CP/M computer can then be run on a PC-DOS computer without altering a byte. £199 (+ vat) from Bytron Ltd, High Street, Kirnnington, S. Humberside, DN39 6YZ. EWW 222 on reply card.

# Video effects computer

An instrument from Fairlight, the Australian makers of the Computer Music Instrument, has done to visual images what their synthesizer did to music. It is a very versatile device for manipulating images. It has a wide range of applications but, we suspect, is most likely to be used in providing the weird effects that seem to go with pop music. The Computer Video Instrument (CVI) can take an image from a "live" camera, tape, film, a slide or still picture and manipulate the image in real time. It is also provided with a stylus-sensitive pad that enables drawing or writing to be superimposed on the image. Its

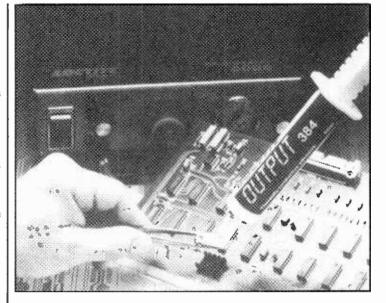
repertoire includes a palette of over four thousand colours. Its output can be stored on video-tape and then recalled for further manipulation or combination with other images.

Background and foregrounds can be created and made to move or have live images moving through them. Three planes are used in a single CVI but more than one can be cascaded to provide a number of different planes.

A chroma-key facility enables still images to be panned or zoomed or stretched or a moving sequence can produce the sort of multi-image associated with stroboscope pictures. Any shape may be used as a 'cut out' or matte to control areas for different foregrounds or backgrounds, patterns or other effects. A live image can be frozen and then manipulated using all the other facilities. An audio input may be used to control an image and this can be combined with freeze effects, 'trails' split or mirror images to produce some spectacular effects.

Using the input pad it is possible to pull an image out of shape and add colours or details; Dr Jekyll no longer needs to use a lot of changes in make-up to change into Mr Hyde: it can be done for him on the machine! Lettering or other 425 seals fasteners and locks adjusting screws against vibration. Ultra-performance Tak Pak 382 is an improved version of an adhesive for wire tacking and bonding smaller components. Black Tak 410 is a 'toughened' cyanoacrylate adhesive used for strain relief and the mounting of large units and covers. It is claimed to have a peel and impact strength superior to many epoxy resins. Further additions to the

"PCB Assembly Line" range include a silicone sealant that does not tarnish copper or brass and can be used for encapsulating components, a freezer aerosol, a non-conductive solvent for cleaning, a contact lubricant and cleaner and spray-on protection shield for p.c.bs against humidity and environmental pollution. It may be soldered through for spot repairs. Loctite UK, Watchmead, Welwyn Garden City, Herts AL7 1JB. **EWW 211** on reply card.



# Adhesives for electronics

A range of products specifically aimed at the electronics industry have been produced by Loctite. Chipbonder is for the rapid placement of surface mounted components so that they are held in position prior to soldering. The bonds are completely cured after 4 to 5 minutes of heat treatment or in 30 to 45s when exposure to lowintensity u-v light is followed by 120°C heat. The resultant bonds are flexible and shock-resistant and can withstand wave soldering. The adhesive is coloured fluorescent pink to aid inspection.

Output 384 is a thermallyconductive adhesive for heat dissipation. It may be used to replace tapes, mechanical fasteners and non-conductive adhesives and is claimed to have a better heat-conductivity than zincoxide grease. It sets in 30s and is fully cured in one to four hours. Other adhesives are all

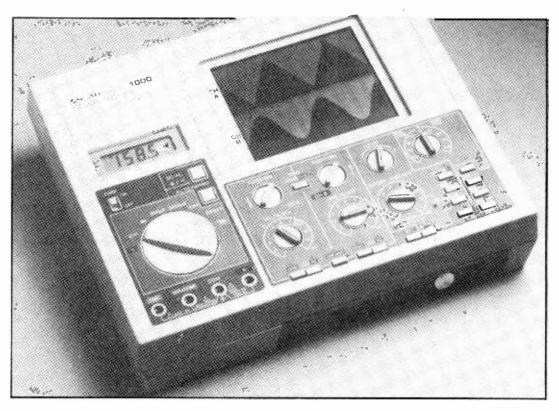
cyanoacrylate products. Assure

static devices can be added to an image using a "cut and paste" technique. Many geometrical functions can be called up for the creation of charts, graphs and diagrams, which can have any of 54 textures applied to them.

All this fits into a 19in rackmounting box with a remote control console. The unit provides connectors for synchronization input and outputs and has two video inputs which may be composite or RGB. An RS232 communications port enable control from an external computer. £4950 (+ tax) from Syco Video, 20 Conduit Place, London W2. EWW 213 on reply card.

# LCD oscilloscope

A hand-held, battery powered, 3.2MHz oscilloscope features a dual-trace liquid crystal display. The display consists of a 128 by 160 dot matrix with a display area of about 76 by 95mm. The internal memory is battery-backed for waveform storage which allows later waveform analysis. Features of the Y-axis operation include a scale of four vertical divisions for each channel, a nine-range sensitivity from 10mV to 5V/division and a frequency accuracy of ±3dB or less from d.c. to 200kHz. The X-axis has 10 divisions with 20 sweep speeds from  $5\mu$ s to 5s/Div: there are continuous and single sweep measurement modes and positive, negative and switchable trigger modes. Soar Model 1000 also incorporates a full-function autoranging digital multimeter. Cost just under £1000, exclusively from Advance House of Instruments, Raynham Road, Bishop's Stortford, Herts CM23 5PF. EWW 214 on reply card.



# Serial line converter

Used one at each end of a data channel, the Serial line converters from CDOS enable data transmission to be established over longer distances than the R8232 ports of the devices to which they are attached. This is achieved by converting the transmission level to RS422 standards. Full duplex operation is possible, with handshaking in either direction. The converter does not modify protocol or data word format, which are determined by the equipment connected to the channel.

20mA current-loop send and receive channels can be added by plug-in modules, which provide either active or passive. transmitter or receiver functions; four modules in all.

RS422 extends the range of serial communication from the 30m of RS232 to one km. Current loop transmission extends this even further; up to 5km.

The transmitter and receiver modules can be adapted to cope with data from any serial source; RS232, t.t.l. and CCITT/EIA levels. The output from a uart or a.c.i.a. can be connected directly. The devices come in a case with a mains power supply but the double-sided board may be extracted and built directly into equipment. C.D.O. Systems Ltd, Unit 65, Corby Workshops, Corby, Northants NN17 1YB. EWW 210

# **Universal PCM transcoder**

For use on North American and European digital transmission telephone lines, the Harris HC-5560. The four standard p.c.m. coding schemes can be accommodated: Alternative mark inversion (AMI), High-density bipolar three (HDB3), Bipolar with six zero substitution (B6ZS) or with eight zero substitution (B8ZS). HDB3 is the recommended CCITT standard. The device can be used in most equipment that can communicate with T1, T1C, T2 or PCM-30/CEPT lines, including multiplexers, channel service units, echo cancellers, digital cross connects and p.c.m. repeaters.

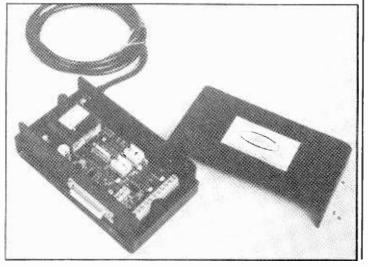
The device operates from a single 5V supply with a 100mA maximum current drain. It can encode and decode simultaneously; includes an alarm signal driver; can operate asynchronously with loop-back control and transmission error detection. HC-5560 is priced at £9.29 in quantities of 100. Harris MHS Semiconductor Ltd, Eskdale Road, Winnersh, Berks RG11 5TR. EWW 215 on reply card.

## **Surge suppressors**

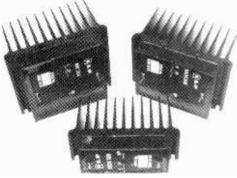
A new type of power integrated circuit is capable of diverting dangerous transient energy away from sensitive electronic circuitry. Called a Surgector, this RCA monolithic device is a thyristor with a special diffused gate section which acts as a zener diode. This permits the anode voltage to turn on the device, giving it a very fast voltage rise time; lightning, for example, can rise at 1kV/ms. The Surgector device clamps the voltage by its zener action until the integral thyristor turns on and shunts the excess voltage away. In most cases, the protected circuitry never sees a voltage greater than 130% of its normal working voltage. Twoterminal devices have off-state voltage ratings of 30, 58 and 225V.

They switch in nanoseconds and can handle peak surge currents of 300A. The three-terminal Surgector adds direct access to the gate of the s.c.r. which allows the device to be triggered with a user-supplied voltage-level detector. Current devices are uni-directional but a bidirectional 225V device is about to be released. They are specifically designed to protect telephone equipment. However, the small (TO202) size and low cost make then equally suitable for computers, alarm systems, cars, tv, outdoor lighting, c.a.t.v. and many other applications. RCA Solid State, Lincoln Way, Windmill Road, Sunbury on Thames, Middlesex TW167HW. EWW 223 on reply

age. Two-Ye off-state TW167HW. **EWW 223** on reply ,58 and 225V. card. NICS & WIRELESS WORLD FERRUARY 10



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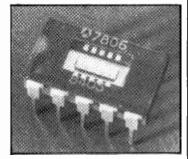


# Write-once proms

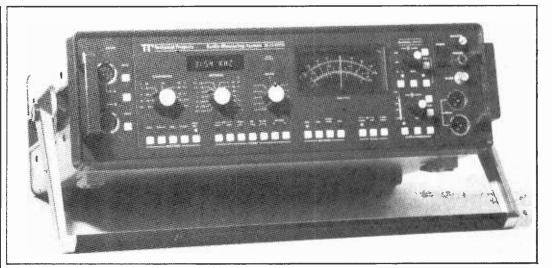
Intel argues that the time consumed by extracting a u-v eprom, erasing it and then reprogramming it is too expensive to be worth it. So they have brought out a family of proms without a window so that once programmed they cannot be erased. What's more they have developed a Quick-Pulse programming algorithm that enables the devices to be programmed in 100th of the time required by u-v eproms - about four seconds. The software is implemented in Intel's own universal programmers as well as those sold by Data I/O. The proms are manufactured in plastic as opposed to the ceramic packaging of their erasable predecessors, which makes them robust and lower cost. The proms are electrically and pincompatible with the eproms and are distinguished by the prefix "P" instead of "D". The range comprises the P2764A, P27128 and P27256. Intel Corporation (UK) Ltd, Pipers Way, Swindon, Wilts SN3 1RJ. EWW 220 on reply card.

# Charge-coupled character reader

A 256-photodiode linear c.c.d. image sensor (TH 7806) is suitable for small optical — character recognition systems and desk-top computer interfacing. Each photoelement is  $13\mu$ m square and is optimized for an even response in the 400 to 1100nm spectral range. The maximum data rate is 2MHz. A noise dynamic range of 6000:1 allows sufficient grey-scale depth to enable the easy and reliable discrimination of smudges, etc, from the printed matter.



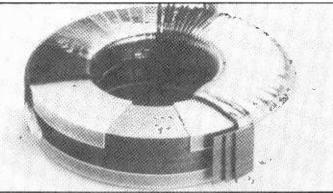
The sensor comes in a ten-pin d.i.l. package and can have a guaranteed accuracy to within  $50\mu$ m. The option of a Z-type epitaxial substrate prevents the deterioration which may be caused by infra-red components in the illumination source and ensures high performance whatever the source. Thomson-CSF Components and Materials Ltd, Ringway House, Bell Road, Basingstoke, Hants RG24 0QG. EWW 221 on reply card.



# Audio measuring system

The ubiquitous microprocessor can be found lurking within the MJS401D, resulting in fast and accurate audio measurements. Standard functions allow measurement of level, noise, frequency, distortion and crosstalk. Plug-in options include extra filters, IEEE interface, IMD, wow and flutter. The unit incorporates an MD404B twin-tone oscillator, but this can be omitted in receive-only applications.

The level measurement range is from - 110 to + 40dBm with a claimed accuracy of better than 0.1dB. Distortion measurements is possible down to 0.0008% (-102dB). The dual-range meter display enabled rapid calibration of the equipment under test. The meter automatically switches between the  $\pm 12$ dB range to a  $\pm 1.5$ dB range for adjusting the reference level which is set in 1dB steps from the front panel. The instrument can be programmed externally through the bus to be ranged automatically, allowing readouts of both frequency and level. Distortion and crosstalk measurements are fully automatic. Technical Projects Ltd, Unit 2, Samuel Whites Industrial Estate, Medina Road, Cowes, Isle-of-Wight PO31 7LP. **EWW 219** on reply card.



# **Torodial transformers and chokes**

A range of toroidal transformers includes 87 different standard configurations of from 15 to 990VA. Any other configuration is available to order. The Transduktor range has been designed to maximize the advantages of toroids; lower weight, smaller size, low no-load losses, and less acoustic hum and magnetic interference. Earthed copper foil is used to prevent electrostatic interference and a Mu-metal screen around the outside of the transformer minimizes electromagnetic interference. To reduce this even further the design maintains a constant number of amp-turns per unit circumference of the toroid. This requires the placing of Mylar insulation and earthed copper

screen between primary and secondary windings. The range meets the requirements of most international standards. Various options for mounting the transformers are provided.

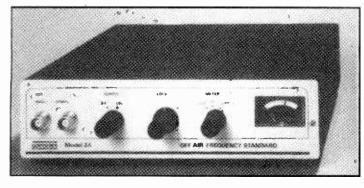
Computerized design allows the company to respond to almost any request with an optimized transformer design. Samples can be provided very rapidly.

The range is complemented by a set of toroidal chokes for switchmode power supplies. These are designed to run at 40kHz with a ripple current of 10%. They are wound on powder cores having a distributed air gap and offer similar advantages to the transformer range. Transduktor Ltd, 443B Holloway Road, London N7. EWW 207 on reply card.

# High-resolution monitors

Colour monitors for data and graphics displays are built around a compact c.r.t. driver board only 140 by 178mm. Miniaturization has been achieved by the use of thick-film hybrid circuits allowing the video amplifiers to be mounted on the c.r.t. baseboard. By separating the power supply, the driver circuit and baseboard are made suitable for use by equipment manufacturers who can obtain the c.r.ts directly and supply their own power supplies.

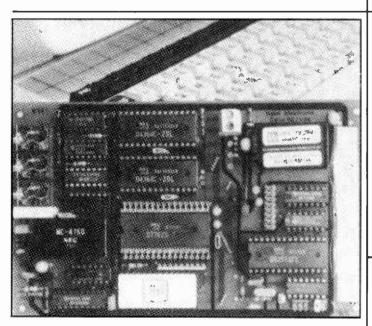
Kent Modular Electronics have based their marketing on a similar system for o.e.m. monochrome monitors and have found it to be successful; they are now transferring their expertise in thick-film applications to the colour monitor. They also manufacture complete monitors and produce a range of monochrome sets in both landscape and portrait format. They recently launched a 10in colour monitor for use in such applications as bank dealer rooms where space is at a premium. Kent Modular Electronics Ltd. Maidstone Road, Rochester, Kent ME1 3QL. EWW 208 on reply card.



# **Off-air frequency standard**

Computer-aided design has been used for the circuitry of the p.c.b. in the Quartzlock frequency standard. The design will operate on the current frequency transmissions of the BBC as well as those to come into operation in 1988. Outputs of 1MHz and 10MHz are accurate to 2 parts in 10<sup>11</sup> long-term and 1 in 10<sup>10</sup> in the medium term application. Uses include calibration, audit and certification of frequency meters, counters, timers, signal sources and generators and radiotelephone test equipment.

The set uses an internal ferrite aerial and a socket for an external aerial is also provided. The p.c.b. can be retro-fitted to earlier Gould Advance and Quartzlock receivers. Dartington Frequency Standards, Moor Road, Staverton, Devon TQ9 6PB. EWW 212.



# **Speech recognition on chips**

A p.c.b. with speech-recognition l.s.i. circuits comes from NEC. The MC4760 analogue interface carries out equalization, amplification and analogue-to-digital conversion. There is a signal processor which performs spectral analysis of the digitized voice input and systems controller to communicate with a host system. This can receive highlevel commands and transmit the results. The board is designed for isolated word recognition and is based on the filter-bank technique with dynamic programming matching. There is a digital attenuator to enable the easy detection of the beginnings and ends of words.

The system features a 'learning and training' mode, whereby extracted features of a voice are stored in a reference memory. When a degree of simularity is established between the input and the stored reference, the results of the comparison are transmitted. A 98% recognition rate is claimed. With a 64Kbyte memory the system has a vocabulary of 128 words; 512 words can be registered with a 64Kbyte memory. The maximum length of utterance is two seconds and the average response time is 0.5s. Available (appropriately) through Dialogue Distribution Ltd, Watchmoor Road, Camberley Surrey GU15 3AQ. EWW 217.

# New 68000 with MMU

An addition to the 6800 family was introduced at the Paris component show by Philips. Known as the SCC68070, the device includes the same central processor as the 68000 but also has a memory management unit (m.m.u.) and direct memory access (d.m.a.) control as well as a serial communications bus (I<sup>2</sup>C bus), RS-232C interface and three counter-timers all on the same chip.

The processor is fully software compatible with the 68000 family and is claimed to be the first processor to include d.m.a. and m.m.u in a single package. The c.mos device is likely to replace the 68000 in many applications where the reduction in component count with the associated lower cost would be advantageous. It also uses less power.

Memory management is used to organise the memory during multitasking where the blocks of memory associated with a specific task are kept separate from any others. Direct memory access allows blocks of memory to be transferred between the on-board memory and peripherals. The inter-i.c. (I<sup>2</sup>C) bus relieves the parallel bus from routine communications tasks where speed is not critical; similarly the RS-232C interface reduces the load on the parallel bus as well as reducing the external chip-count.

Internally the single chip has the equivalent of 100 000 transistors and is built using 2-micron geometry. With an internal maximum clock frequency of 10MHz the device has a surprisingly low power consumption of 1W.

Designed and manufactured jointly by Philips and Signetics, the SCC68070 is to be marketed in the UK by Mullard Ltd, Mullard House, Torrington Place, London WC1E 7HD. EWW 205.

# Superfast 16/32-bit processor

Claimed to be an inexpensive alternative to the multi-chip bit-slice processor, the GI DSP32010 is a second source for the TMS32010. It is said to combine the flexibility of a high-speed controller with the numerical capability of an array processor and can execute five million instruction/s

The efficiency of this 'single-chip computer' is the result of a ''comprehensive and easily programmed instruction set and of highly pipelined architecture.''

# **Super-fast floppy**

A new flexible-disc drive from Epson has a formatted capacity of 4.8megabytes. BM-5 is a mainsoperated 5.25in unit operating at a speed comparable to that of a hard disc drive. It can act as a back-up unit for hard discs and has the advantage over tape streamers of random access to the files. It also has an advantage over hard discs by being a removeable medium. It is possible to have an unlimited number of 5Mbyte floppies compared with a fixed 10Mbyte disc. Interface cards enable the drive to interface with Epson's own range of computers or the IBM PC and its compatibles.

The storage medium is a special 6Mbyte double-sided high density 5.25in floppy disc. The drives internal firmware 'compresses' the data to be stored and expands it Special instructions have been included to speed the execution of digital signal processing algorithms.

High-speed applications include speech recognition and synthesis, radar and optical sensing, and servoloop computations; particularly in robotics. The device operates from a single + 5V. supply and is housed in a 40-pin d.i.l. package. Available through Campbell Collins Ltd, 162 High Street, Stevenage, Herts. **EWW 218** on reply card.

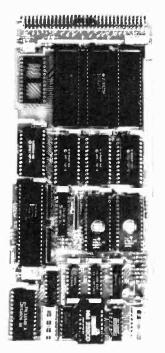
again when retrieved. The drive is in one box and the interface unit in another. The price is quoted as "somewhere in the region of £1,000." Epson (UK) Ltd, Dorland House, High Road, Wembley, Middlesex HA96UH. EWW 216 on reply card.

## **Test sockets**

A series of low-insertion-force sockets has been produced by Aries. Called the Eject-a-dip range they are available in 14, 16, 24, 28, and 40-pin versions. The levers at each end of the socket lock the i.c. into position when inserted and also are used to eject it after the test. Aries Electronics (Europe), Alfred House, Oatlands Drive, Weybridge, Surrey KT13 9LB. EWW 206 on reply card.

# COMPLETE SINGLE BOARD COMPUTER & SUPPORT..

#### Now you can weald the power of the 8051 using BASIC.



Based on the Intel 8052AH single component Microcontroller the CPU comes complete with a unique implementation of the BASIC language enabling direct access to the special function registers, timers and interrupts available on the 8051 device.

The new Cavendish Automation 7030 CPU is one of a complete range of Eurocards providing complete systems capability to OEMs. Support includes static MOS RAM boards (to 128K), Power-Down Control boards, Decoder boards, providing further address line decoding, watchdog, real time clock/calendar plus additional output flags and I/O. Mass storage devices. Backplaines. PSU and battery packs. Drive boards offering power output, signal conditioning and externally gated outputs. Multi channel DAC/ADC. Remote switch modules for power switching, sound or V.I.S. of vision.

Comprehensive documention is supplied with each CPU and the 7030 CPU card requires only a +5v supply and dumb terminal for operation. Many unique features are incorporated and the system allows very fast interactive development of user software for super easy deployment in the target system.

- CPU Card Advantages:
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- \* Twin Serial ports independently configurable (300 19,200 baud)
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- \* Full floating point arithmetic
- \* BASIC utilities may be called from Assembler
- \* Interrupts handled by BASIC or Assembler
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- \* 11 MHz operation as standard \* low cost \* customised options on low quantity

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For further information on the CA 7000 Series Controller contact:

Cavendish Automation Limited. 45 High Street, St Neots, Cambs, PEI9 1BN. Tele (0480) 219457 Telex 32681 CAVCOM G.

CIRCLE 75 FOR FURTHER DETAILS.

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answer, a full investigation is made.

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ASA Ltd, Dept 1 Brook House, Torrington Place, London WC1E 7HN

This space is donated in the interests of high standards of advertising.

# Appointments

Advertisements accepted up to 12 noon 5 February for March issue

ecn

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ELECTRONIC COMPUTER AND MANAGEMENT APPOINTMENTS LIMITED FREEPOST, The Maltings, Burwell, Cambridge, CB5 8BR.

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If you wish to make the most of your qualifications and experience and move another rung or two up the ladder we will be pleased to help you. All applications are treated in strict confidence and there is no danger of your present employer (or other companies you specify) being made aware of your application.

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# Telecommunications Engineering Technicians

#### Openings in Servicing and Maintenance Up to £9,317

Our business is to install and maintain the communications equipment used by the Police and Fire Brigades in England and Wales – some of the latest you will find in operation anywhere.

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The work provides excellent opportunities for extending your technical expertise, with specialised courses and training to keep you up to date on developments and new equipment. There are also opportunities for day release to gain higher qualifications.

Applicants, male or female, must be qualified to at least City & Guilds Intermediate Telecommunications standard and possess a current driving licence. Some travelling will normally be involved. Registered disabled persons can of course apply.

The Home Office is an equal opportunities employer.

Salary will be on a scale £6,810 to £9,317 a year with generous leave allowance and pension scheme. Starting salary may be above the minimum and relocation expenses may be payable for some of the posts.

Good prospects for promotion. If you are interested in working with us, please write for further details and application forms quoting reference EWW/1 to: Miss M Andrews, Home Office, Directorate of Telecommunications, Horseferry House, Dean Ryle Street, London SW1P 2AW.

**Directorate of** 

Telecommunications



ELECTRONICS TECHNICIAN GRADE 7

PHYSICS DEPARTMENT THE CITY OF LONDON POLYTECHNIC invites applications for the above post in its Physics Department, located in Jewry Street, EC3 close to LIVERPOOL STREET and FENCHURCH STREET STATIONS and ALDGATE UNDERGROUND.

The successful candidate will assist in the provision of an electronics service to all sections of the Department and will be r esponsible to the Superintendent of Laboratories for the overall operation and maintenance of electronics services, the design, development, construction, modification and repair of electronic equipment and occasional training of other technical experience is expected.

Salary on a scale from £10,617 p.a. to £11,757 p.a. (including London Weighting of £1,365 p.a.).

For further particulars and an application form please write, on a postcard, to the Staff Records Officer, City of London Polytechnic, 117 Hounsditch, London EC3, quoting reference no. 86/2.

(148)

# **Home Office**

### LRT BUS ENGINEERING LIMITED MOBILE RADIO REPAIR PERSON

This vacancy is in the Radio Repair Workshops of our Bus Engineering Works at Chiswick, West London.

The successful applicant should have a thorough knowledge of Radio Communications principles, specifically VHF-UHF transmission and reception using AM-FM-PM and Data Modulation Modes and will be required to work on various types of equipment, including hand-held units, and should therefore be well experienced in this field. A good understanding of general electronics is also required.

A City and Guilds Certificate or ONC in a relevant discipline is desirable, though not essential.

We offer an attractive benefits package, which includes FREE TRAVEL on LRT bus and underground services and a contributory pension scheme.

Interested? The phone Ann Clark on 01-994 3641, Ext 182 or write to the Personnel Director (Bus Engineering Limited), 566 Chiswick High Road, Chiswick, London W4 5RR quoting reference BELW/1/WW.

(149)

# BRITISH ANTARCTIC SURVEY Electronic Engineer

The Survey requires an Electronic Engineer to operate and maintain a computer and microprocessor controlled HF radar, known as the Advanced lonospheric Sounder. at Halley 76°S 27°W in the Antarctic. He will be responsible for the system hardware and should be able, with training, to modify the system software necessary.

The appointment will be for a period of 38 months, commencing in April 1986 and the successful candidate will sail for Antarctica in October, returning to the UK in May 1989. Qualifications: Degree or equivalent in electronica, electrical engineering or other appropriate subject. Some relevant experience preferred.

Applicants, to work mainly overseas, must be physically fit, single, male and aged between 21 and 35.

Salary: Grom £6252 per annum plus an allowance of £946 per annum for periods south of Montevideo.

For futher details an an application form, please contact:--

The Establishment Officer, British Antarctic Survey, Madingley Road, Cambridge CB3 0ET

Telephone: Cambridge (0223) 61188, Ext. 235 Please quote reference: AS 4/86 Closing date for applications: 10 February 1986



# Appointments

# Field Service Engineer

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SONY BROADCAST LTD is well established as one of the world leaders in the professional broadcast television industry, with branches throughout Europe, the Middle East and Africa. Our wide range of sophisticated products includes Cameras, VTRs/VCRs, Camcorders, Editors and the new High Definition Video System. Applications are now invited from experienced engineers to join our Field Service team based at our international headquarters in North Hampshire.

Responsibilities will include the commissioning, service and repair of our full range of video products. This will involve overseas travel throughout our market area and extensive customer contact at all levels. Candidates should possess a formal electronics qualification, together with a minimum of 2 years experience gained either in operational television or its allied manufacturing industry.

Full product training will be provided and generous relocation assistance where appropriate. We offer an excellent benefits package including an attractive salary, Company car, free private medical cover and a Company Pension/Life Assurance Scheme.

If you are interested please write to, or telephone: David Parry, Personnel Officer.



#### Sony Broadcast Ltd.

Belgrave House Basing View. Basingstoke Hampshire RG21 2LA United Kingdom Telephone (0256) 55 0 11

(133)

## AIR TRAFFIC ENGINEER – GRADE 2

Applications are invited from persons aged 25 years or over who hold a minimum of ONC (Eng.). City and Guilds Telecommunications Technician (Course 270/271) up to and including T3. TEC Certificate/Diploma in Telecommunications or equivalent technical gualifications, for the post of Air Traffic Engineer Grade 2 on the staff of the Isle of Man Airports Board. Candidates should have a sound knowledge of electronics and be experienced in the maintenance of Airport Communications, Radar, Nav. Aids. CCTV or data processing systems.

The post is permanent and pensionable on a non-contributory basis (save for a contribution of  $1\frac{1}{3}\%$  towards family benefits) and has a salary scale of £8,070–£9,462 per annum. The post involves shift work including some Saturday and Sunday working for which an additional allowance is payable. A grant of up to £1,500 towards the cost of removal and relocation expenses is available to applicants not resident in the Isle of Man and arrangements exist for the transfer of certain pension rights.

The duties of the post include the installation, maintenance, repair and calibration of electronic equipment and systems concerned with Air Traffic Control and Operations at Ronaldsway Airport. Further details can be obtained from the Airport Director (Tel: 0624 823311).

Application forms and further details of the post can be obtained from the Secretary, Civil Servica Commission, Central Government Office», Douglas (Tel: 0624 26262 Ext. 2835) by whom applications should be received within two weeks of the date of this advertisement.





## **ELECTRONIC ENGINEER**

Samuelson Communications are leaders in audio and video rental in the UK. Our expanding Technical Services Department which provides technical support in all areas of our operation, require both bench and field engineers.

Your duties will include the maintenance or operation of a wide range of Monitors, VCR's, Video Projectors and associated equipment. Ideally you should be qualified to HNC level with serveral years practical experience, specific product training will be provided, salary by negotiation. If you like to join our team, please contact Janet Smith, Samuelson Communications Ltd.

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Humphries Video Services is a leading Videocassette duplication and video facilities plant. We currently have a vacancy for a video engineer with experience in the maintenance of all VCR formats, e.g. VMS, BETAMIX etc; and also a working knowledge of 11" cformat, BUVect would be useful. Applicants should ideally be qualified to BTEC ONC level or equivalent. Salary in the range £8000 to - £10,000 p.a. Please telephone or write for application form

> HUMPHRIES VIDEO SERVICES LTD., 42 Station Road, Merton Abbey, London SW19 Tel: 01-542 5661

(134)

#### University of Aberdeen DEPARTMENT OF PSYCHOLOGY Departmental Superintendent Technician (Grade 8)

required for the Department of Physchology to supervise technical staff in various research and teaching laboratories and workshops and be responsible for the design, development and use of electronic instrumentation in the Department's teaching and research programmes. The Department has a range of mini and micro computers as well as video, audio and physiological instrumentation. Experience in computer interfacing and programming and in designing digital and analogue circuitry is required.

Applicants should have a degree or HNC in electronics and normally have had a minimum of 15 years' relevant experience.

Salary on scale £10,315 - £11,217 per annum, with appropriate placing.

Application form and further particulars from The Personnel Officer, University Office, Regent Walk, Aberdeen AB9 1FX (Tel 40241 Extn 5351), to whom the completed form should be returned by 31 January 1986. (Ref T437) (135)

# **Opportunities in Professional Broadcast Engineering**

Established 8 years ago as a part of the highly successful Sony Corporation, Sony Broadcast Limited is now recognised as a world leader in the professional broadcast industry. Our Hampshire headquarters and overseas branches provide sales, distribution and engineering support to a marketing area that extends to Europe, the Middle East and Africa. Our innovative product range incorporates state-of-the-art electronics and includes some of the most sophisticated broadcast equipment available, such as Betacam, a High Definition Video System and the Digital audio range. Applications are now invited from experienced engineers who are looking for a fresh challenge in their career.

#### **Principal Project Engineer**

The successful candidate will join an engineering team responsible for the design and project management of static studio and mobile television broadcast systems. Aged 30 plus, applicants should possess a formal electronics qualification together with a proven track record in broadcast systems manufacture. Some overseas travel will be required.

#### **Senior Sales Engineer**

To join our UK sales team, the person appointed will be responsible for our full range of professional video products. UK and some overseas travel will be required. Candidates should have significant sales experience gained in a broadcast environment.

#### **Senior Engineer Technical Operations**

The successful applicant will be responsible for the technical support of our complex video products, such as the new Sony studio camera. Key activities will also include the optimisation and evaluation of performance, and the conducting of customer acceptance tests. A minimum of 3 years broadcast experience and the ability to effectively maintain customer interface is essential.

#### **Demonstration Equipment Engineer**

To join a small engineering team responsible for the provision of a product demonstration facility to our customers in the UK and Overseas. Responsibilities will also include the provision of support at exhibitions. Candidates should have a track record in the broadcast industry, VTR and editing experience would be an advantage, together with a formal electronics qualification. There will be a requirement for overseas travel.

#### **Graduate Engineer**

We are looking for a newly qualified graduate electronics engineer to join the Research and Development department within Sony Broadcast Limited. The post requires a good honours degree with a bias towards communication theory. The variety of work undertaken offers a stimulating challenge with the opportunity to apply recently acquired academic knowledge to practical reality.

#### **Senior Engineer Systems Production**

The successful candidate will join our Systems Group and supervise a small team responsible for the acceptance of vendor equipment, test and repair of equipment built in-house and the development of "black box" units – primarily interfaces. Applicants should have experience of sound and television principles together with a recognised electronics qualification. Previous supervisory and microprocessor experience would be an advantage.

We offer attractive salaries together with first class conditions of employment, including free private medical cover, and Company Life Assurance/Pension Scheme.

If you are interested please write with details of your career to date and current salary to David Parry, Personnel Officer, Sony Broadcast Limited, Belgrave House, Basing View, Basingstoke, RG212LA, or alternatively telephone our 24 hour answerphone for an application form, on 0256 59583.



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# **RADIO OFFICERS**

who are after initial training will become members of an organisation that is in the forefront of communications technology. Government Communications Headquarters can offer you a satisfying and rewarding career in the wide course (38 weeks if you come straight from Nautical College) which will fit you for appointment to RADIO OFFICER.

Not only will you find the work as an R O extremely interesting but there are also good prospects for promotion opportunities for overseas travel and a good salary. Add to this the security of working for an important Government Department and you could really have the start of something new.

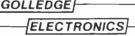
The basic requirement for the job is 2 years radio operating experience or hold a PMG, MPT or MRGC or be about to obtain a MRGC. Registered disabled people are welcome to apply.

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