

# Semiconductor buyer's guide 

## -

BBC micro Mimicontroller

## XY plotter update

## Mobile radio

# Digital feedback <br> <div class="inline-tabular"><table id="tabular" data-type="subtable">
<tbody>
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# 109 N 

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01-02204 9.45


## HT320

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DC Volts: $0.1 \mathrm{~V}, 0.5 \mathrm{~V}, 2.5 \mathrm{~V}, 10 \mathrm{~V}, 50 \mathrm{~V}, 250 \mathrm{~V}, 1 \mathrm{kV}$ ( $20 \mathrm{k} \Omega N$ ). AC Volts: $10 \mathrm{~V}, 50 \mathrm{~V}, 250 \mathrm{~V}, 1 \mathrm{kV}(18 \mathrm{k} \Omega N)$ DC current: $50 \mu \mathrm{~A}, 2.5 \mathrm{~mA}, 25 \mathrm{~mA}, 250 \mathrm{~mA}$. Resistance: $2 \mathrm{k}, 20 \mathrm{k}, 2 \mathrm{M}, 20 \mathrm{Mz}$. AF Output: -10 dB to +22 dB for $10 \mathrm{VAC}(0 \mathrm{~dB} / 0.775 \mathrm{~V}, 600 \Omega)$. Leakage (Iceo) $15 \mu \mathrm{~A}, 15 \mathrm{~mA}, 150 \mathrm{~mA}$. Hee: $0 \cdot 1000$ (Lc Tb). Weight: 410 gms .

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LF351 Bi-FET op amp LF353 Dual version of LF351 LM380N IW AF power amp LM381 Stereo pre-ampIC NE544 14 pin DKL servo driver IC NE555N Multi-purposelow cost timer
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power amp
ULN2283 1W max 3-12V
power amp
CA3089 FM iF amp, detector. mute, AFC, AGC system
CA3130E BIMOS op amp
CA3140E BIMOS version of 741
MC3359 Low current dual conversion NBFMIF and det LM3900 Quad norton amp LM3909N 8-pin DIL LED flasher
KB4412 Two balanced mixers iF amp with AGC for AMSSB ICM7555 Low power CMOSversion of 55 timer
HAll225 Low noise FMIF
HA12017 83dBS/N phono preamp $0.001 \%$ THD
MC1 4412300 baud MODEM controller
(EuroUS specs)
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$61-01062 \quad 1.95$

61-01083 1.95 61-01388 2.75 $61-01496 \quad 1.25$ $61-02002 \quad 1.25$ $61-022831.00$ $61-03089 \quad 2.84$ 61-31300 $\quad 0.80$ $61-31400 \quad 0.46$

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| 10M08AA | 10.695 Filter |
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| CM161 | Min LCD Clock |
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| $14 \times 0.3$ " | IC socket |
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Electronics Pocket Book 99 Practical Electronic Projects More Electronic Projects in the Home
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Using Microprocessors and
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The 6800 Family
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A Design Handbook


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# GOTO Sweden 

The new computer for Swedish schools described in these pages last month must have whetted quite a few appetities (ours included), pointing the way as it undoubtedly does towards the next-generation educational computer over here.

The fetchingly-named Compis is a 16 -bit job with 128 K of memory plus a separate graphics processor giving a 1280 by 800 pixel display, with word-processing and communications facilities thrown in, all for a price of around $£ 1,000$. Set beside that, the BBC Micro, for all its qualities, begins to look a little old fashioned.

One interesting feature of the new machine is the decision by its makers to adopt a version of Comal- 80 as the standard language in preference to Basic. Comal is an interpreting language which provides a bridge between Basic and the highly structured compiling language Pascal.

Basic has long been the subject of criticism by programming purists on the ground that it puts no premium on tidy thought. And a quick glance at the contributions from readers in some of the home computing magazines provides a good deal of support for that argument: everywhere you see listings festooned with Goto statements and peppered with peeks and pokes. Short of laboriously tracing each program through line by line, it's impossible to discover
what is going on
With one or two honourable exceptions, the Basic on most home computers is indeed pretty awful. And it seems to be the untidiest features of the language that the average schoolboy learns first. Walk into your local computer store, and like as not you'll find a group of 12 -year-olds working on a program which says

## 10 PRINT "KEVIN IS A WALLY"

## 20 GOTO 10

Only one home computer has been offered in Britain with a standard language other than Basic: the late lamented Jupiter Ace, which came with Forth.
In its several versions, Forth is probably the next most popular language after Basic. But how useful is it to the average keyboard hacker? It's fine for what it's good at - real-time hardware control and whatnot - but as a general-purpose medium for duties such as drawing pictures and processing text strings, it's nothing like as convenient as Basic.
So now that computer studies have taken the place of the classics as a discipline for the brain, it will be interesting to see what benefits Scandinavia obtains from switching to Comal. For when the eventual replacement for the BBC Micro comes along, perhaps we shall want to do the same.

## Robots in the lab

Those interested in using small robots in laboratories are invited to join the Laboratory Robotics Club. The Club has been formed by representatives from the Laboratory of the Government Chemist and from other industrial and academic laboratories.

At the Laboratory of the Government Chemist, the Automation and Instrument Development Group is developing an automated work station based on a small robot arm. This will be capable of such operations as weighing, homogenizing, dispensing and transfer of liquids, centrifuging, filtering, stirring and heating.


A digital PAL decoder designed by the BBC was one of a number of new developments shown at a series of open days staged recently by the Corporation's designs department.

Unlike a conventional colour tv decoder, which uses bandpass and notch filters to separate the colour information from the luminance signal, the digital decoder uses comb filtering. By making use of the spectral separation between the brightness information and the colour sidebands, comb filtering makes it possible to retain high frequencies which would otherwise have to be thrown away to prevent unacceptable patterning. The result is a sharp picture almost free of the usual twinkling dots on colour boundaries.
Good quality PAL encoding and decoding is of importance in broadcasting, for until the day when the 'islands' of digital processing within the broadcasting chain finally coalesce into an all-digital whole, a signal may have to be decoded and recorded many times before it reaches the public. And any defects in the process will be compounded
The new unit uses 8 -bit conversion, with 12 -bit internal processing to avoid rounding errors. The comb filter which separates chrominance from luminance operates in both the horizontal (line-by-line) and temporal dimensions. The results are very impressive, even on very demanding test signals such as the 'zone plate' pattern of concentric rings and the BBC's new SLUG test signal. SLUG, which stands for saturation and line-up generator (and this equipment was on

## BBC design

display too), produces a lurid multi-coloured chequerboard designed to reveal maladjustments of phase and saturation in colour monitors

BBC engineers pointed out that the digital decoding process adopted in their new decoder is not the same as that used in the new digital television sets from ITT: in those, decoding is by the conventional method, although in the digital domain.
Also in the television field was a range of new u.h.f. receivers. BBC engineers have found that most commercially produced receivers perform little better than domestic television sets. For one thing, they adopt (for reasons of cost) the intercarrier sound technique, which necessitates sacrifice of the higher vision frequencies to avoid sound-on vision interference.

The BBC's own receivers are necessarily much more complex. At the top of the range is a synthesized receiver designed to provide a programme feed for television relay stations. This features an interdigital filter tuned to the wanted channel at its front end, an image-cancelling mixer, an exalted-carrier detector and some specially manufactured surface acoustic wave filters to ensure a good amplitude and group-delay response. In the mixer, the signal is fed in quadrature to a pair of schottky diode ring mixers, the output of which is combined so as to cancel the unwanted image. Provision has been made for a digital stereo sound output.

For less demanding applications another u.h.f. receiver is available: this comes in synthesized or voltage tuned versions with local or remote tuning and it too features a lowdistortion demodulator and special s.a.w.fs.

The BBC has for some years been using a digital sound-insyncs technique for distributing tv sound: besides saving the cost of separate music lines this ensures that the sound cannot get lost in transit. But now the technique has been taken a step further to provide stereo sound. The system uses a sampling rate of approximately 32 kHz (twice line frequency) with 14

# s on show 

bits per sample. The data stream is transmitted during the line sync. pulse and burst of four-level pulses. Before transmission to the public this signal would, of course, be stripped off.
One development aimed at making life easier for television production staff is a new timecode converter. To help in identifying edit points, an 80 -bit time signal is distributed around the studios and is recorded by the v.t.r. on an audio track. But when the producer goes way with a cassette of the day's takes, this signal is usually left behind, since most domestic v.c.rs have only one sound channel. The new equipment inserts the time code into the vision signal on a suitable line in the vertical interval. The data can then later be extracted and displayed, even at slow-motion or in stop-frame. Having chosen his edit points off-line, the user can store the codes on a floppy disc from which they can be recovered back at the studio.
maintenace staff have a small telemetry receiver fitted with a dot-matrix text display.
The same system is being used for safety monitoring of men working alone on isolated sites. The engineer has to push a button every so-often - and if he forgets, the rescue team is dispatched!

With the introduction of 24hour broadcasting on Radio 2 it has become more difficult for the engineers to ensure good transmission quality, since there is now no time at which they can test the equipment. However, they believe they have now obtained a tacit agreement from the programme controllers for an occasional half-minute of engineering tests. These would no doubt have to be carried out during the small hours of the morning, since there is as yet no way of disguising a frequency sweep as programme material.
But Designs Department have now constructed some equipment for making short tests of this sort, based (like several other items on display) on a BBC Microcomputer. This low-cost system scans the network for a signal to start the test, which consists of a


BBC Television and the domestic radio services now have a very large number of transmitting stations dotted about the country, and the great majority of these now are unattended. Maintenance of the networks is carried out by a small number of crews operating from regional centres; and these crews need immediate warning of any mishaps at the transmiitter sites.
Such warnings are sent by automatic reporting equipment which dials up the local monitoring centre and sends a code number corresponding to the fault. To alert the crews, this information can now be broadcast as a data signal on one of the v.h.f. radio networks. The code number is converted into Ascii text and sent as a low-rate d.p.s.k. signal on a 76 kHz subcarrier. For a read-out device,
frequency run and a 15 s period of silence for noise measurements. Afterwards it displays the results.

For distributing radio programs a new radio link operating in the $1487-1492 \mathrm{MHz}$ band was shown. The BBC has been allocated four of the ten 500 kHz channels in this band and is using the 'tamed f.m.' technique developed by Philips to squeeze in a $676 \mathrm{kbit} / \mathrm{s}$ digital stereo signal. This system, like one or two other exhibits, has now been licensed to a commercial manufacturer

Developments in studio equipment, both in radio and television, have been proceeding rapidly with the growth of digital processing and the evolution of new EBU interfacing standards. But analogue equipment, which still comprises the bulk of the broadcasting chain, has not been forgotten.


## Acorn means business

Acorn Computer, manufacturers of the BBC and Electron micros are about to launch a series of business computers. There will be eight models in the series At the lower end is a word-anddata processor with a specification similar to that of a BBC micro with the View and Viewsheet programs and a built-in disc drive, capacity 640 Kbyte . The 'flagship' of the series will be the ABC 300 . Software compatible with the IBM PC, It offers a desk-top manager with icons. For those unfamilar with the terminology, the screen is arranged like the surface of desk so that in order to write a memo, instead of reaching for a memo pad, you point an arrow on the screen to the picture (or icon) of a memo pad. The program then invites
you to write, i.e. type the memo. The model 300 uses the Intel 80286 16-bit processor, and the Digital Research Concurrent operating system supports applications written for PC DOS, MS DOS and CP/ M 86, ensuring that there will be a wide range of software already available. The system can also perform several tasks at the same time and is suitable for the development of integrated, multi-function software packages. The computer has twin floppy drives and a monochrome monitor, while model 310 adds a 10MByte hard disc and colour. All the ABC models can be networked through the Econet system and can have built-in modems. Prices are yet to be announced.

## Voices in the computer <br> selected sales staff at $10 \mathrm{a} . \mathrm{m}$.

Electronic mail is all very well but each user needs to have a computer to operate it. Now BT Merlin have come up with a system very similar to electronic mail but using digitized recorded speech. It can be used from any remote telephone, almost anywhere in the world. Messages can be made available to all who contact the base station or may be given passwords so that only those for whom they are intended can receive them. Messages can alsn be coded so that they are not made available before a specified time. So it is possible for a sales director, for example, to record a message which will be deljvered to
next Tuesday. The p.c.m. digitized speech is recorded onto a Winchester disc which has sufficient capacity for a large number of messages and further disc drives can be added to increase the capacity. Our impression was that the messages were perfectly clear and much better than any synthetic speech which we suppose could be an alternative One minor problem is that the system requires a dedicated minicomputer and this makes the system quite expensive, though claimed by Merlin to be very 'cost-effective'. The entire package is devised and manufactured by Ferranti.

## Space station Columbus

Discussions are currently taking place for the possible extension of the European Spacelab project into some form of orbiting space station. One project, called Columbus, was originally proposed by Germany and Italy. A preparatory programme has been initiated by the European Space Agency. Columbus is being thought of as three modules: a manned pressurized module which would provide a working environment for scientists; a payload carrier or space platform to act as a base for unmanned experiments; and a resource module which would provide power and propulsion for the whole system. Details and even which ESA member states will participate have yet
to be settled.
Meanwhile the American at NASA are forging ahead with similar plans but on a very grand scale. The same threepart system is envisaged but with possibly several manned modules and unmanned platforms. Assembled in spce, the Nasa space station could expand to accommodate a crew of 18 by the year 2010 . NASA is also investigating the part that robotics could play in the running of the station. The platforms would be re-useable carriers for experiments, sensors etc. NASA has invited participation from other space agencies including ESA and it is possible that Columbus could be developed as a European contribution to the NASA
station. Other possibilities for collaboration are also being looked at. The potential practical use for such space stations falls into four categories: those which take advantage of the microgravity and vacuum environment for experimental or commercial purposes; the use of the station as a permanent statellite; the testing of technologies for later use in space; and the servicing of satellites. Microgravity research has already indicated that it is possible to produce highly pure pharmaceutical chemicals by electrophoresis Metallurgy and electronics are though to be other areas where high-value products could benefit from processing in space.

## RFI and computers

The British Standard Institution have published a new standard, BS 6527 covering a Specification for limits and measurements of spurious signals generated by data processing and electronic office equipment. Such equipment can cause interference bourne through the mains cables or direct radiation causing interference to radio reception.
The new standard is the first to specify limits for the levels of these spurious signals in order to reduce to an acceptable level the probability of the incidence of radio interference from sources in both domestic and commercial areas. Measurement of limits is at mains terminals in the frequency range 150 kHz to 30 MHz and of field strength from 30 to 1000 MHz .
The limits and methods of measuring them are based on work currently in progress within the International Committee on Radio Interference (CISPR).
This Standard will help to overcome the anomoly of British computer manufacturer making equipment without any protection suitable for the domestic market and then having to substantially alter the designs in order to comply with the standards in other countries, for example the FCC regulations in the United States. At present there is no agreed international
standard specifying the limits of permitted r.f. interference caused by computer in either the domestic or commercial area, but as such equipment becomes more widespread, the standard
will meet a current and increasing need.

Copies are available from the BSI Sales Department, Linford Wood, Milton Keynes MK14 6LE, price $£ 10.20$.

## TV news from anywhere

No sooner had we got used to the idea of e.n.g. (electronic news gathering), then GECMcMichael come up with another idea, s.n.g. or satellite news gathering. This consists of a small, portable satellite earth terminal which may be flown in a light aircraft to anywhere in the world and transmit news back via any handy communicationd satellite which can receive the up-link frequency of 14 to 14.5 GHz . The s.n.g. terminal can also receive narrow-band signals for audio and so can be given instructions by the home broadcasting station. The whole system is powered by a 12 V battery or local mains, if available.
The compactness of the system has been achieved by the design of an elliptical antenna which combines small
size with high efficiency and a radiation pattern meeting the 2 degree satellite-spacing requirement. The dish measures one metre high by two metres wide with an offset feed, all held in a universal frame enabling the dish to be aimed at the satellite. The transmitted 14 GHz signal is boosted by a fet amplifier to 4.8 W , carried to conical horn feed of the antenna by a waveguide. Another contribution to the small size of the unit is the use of a coder similar to those used in teleconferencing which only transmits the changing information in a tv picture and thus allows a much narrower bandwidth. This can give a blurred image to any fastmoving part of the picture but it is though that this is acceptable in situations where the events would normally be inaccessible.

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MM11L MM11UP MS11JP, MS11KE, MS11LB, MS11LD, MS11MB, MS11PB, MM8AA MM8AB, MM8E, MM8EJ MS8CA, MS8CB MS8DJ MSV11DC, MSV11DD MSV11LK, MSV11PL

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TE16, TS11, TU10, TU58, TU77

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# Music keysfor the BBC microcomputer 

## Two programs - one for playing music using the BBC computer sound generator supplement this discussion of software for the economical polyphonic keyboard interface. Hardware was presented in the July issue.

As described in the July issue, the keyboard communicates with the computer by sending an interrupt through the handshake line $\mathrm{CB}_{1}$ on the user port. This initiates a routine which reads the keyboard in bit-mapped form into eight memory locations, in accordance with the flow diagram given previously. Details are given in List 1. This is a keyboard-read utility which can be used on its own to check functioning of the keyboard hardware, or as part of a more elaborate program (e.g. List 2) designed to exploit the interface more fully.

The main part of List 1 is written in assembly language which is
both the natural way to deal with interrupts and is efficient in memory usage and speed. The default setting of HIMEM in MODE 7 is 7C00 (hexadecimal). Line 440 sets aside 256 bytes for the assembled code. The interrupt routine is in two sections, the first (lines 520 to 750 ) being assembled at HIMEM, the second (lines 760 to 1060) at HIMEM +30 . A short initialising procedure which configures the versatile interface adaptor, v.i.a., and tells the computer where to find the interruptservicing routine is assembled at HIMEM +70 . The variable FLAG1 (line 470) is used as a message to the display routine
(lines 1270 to 1320 ) that an interrupt has been received and serviced and that there is therefore something new to display. Data read in from the keyboard is stored in eight bytes starting at $M^{1 \%}$ (line 450). These eight bytes are initially set to zero by lines 480 to 500 . By referring all these variables to HIMEM, the assembled code is easily relocated should, for example, the user wish to use a different MODE (illustrated in List 2).

I assume that the reader has some familiarity with the BBC assembler and the IRQ vector system. The subroutine SETUP (lines 1070 to 1210) which is

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List 1. Basic with assembly-language program for the BBC microcomputer music keyboard interface stores key information in
bit-mapped form in eight bytes
at HIMEM.
List 2. This program for the
BBC microcomputer uses key information from List 1 to play the computer's internal sound generator and display a map of
the keyboard status.
called at line 1250 , sets the registers of the 6522 v.i.a. (B) (which is memory-mapped from FE60 to FE6F) as follows
-data-direction register B
(FE62) all inputs
-auxiliary-control register
(FE6B) to give single-shot interrupts from timer 1
-peripheral-control register
(FE6C) to give interrupts on positive-going edges of $\mathrm{CB}_{1}$ and set $\mathrm{CB}_{2}$ high
-the interrupt-flag register
(FE6D) cleared
-interrupt-enable register
(FE6E) has $\mathrm{CB}_{1}$ and timer
1 interrupts enabled.
This routine also stores the location (HIMEM) of the user interrupt service vector (IRQ2V) in locations 206/7 (note that the coding assumes that the low byte of HIMEM is 00 ).

On receipt of an interrupt request on $\mathrm{CB}_{1}$, the code at HIMEM is entered (line 520). This stores the $6502 \mathrm{~A}, \mathrm{X}$ and Y registers on the stack and disrupts further interrupts on $\mathrm{CB}_{1}$ (lines 590 - 600). Timer 1 coun-

| 960 | STA \& 206 |
| :---: | :---: |
| 970 | STA FLAG1 |
| 980 | LDA \#\&50 |
| 990 | STA \&FE6D |
| 1000 | PLA |
| 1010 | TAY |
| 1020 | PLA |
| 1030 | TAX |
| 1040 | PLA |
| 1050 | RTI |
| 1060 | J |
| 1070 | P\%=SETUP |
| 1080 | COPT 1\% |
| 1090 | LDA \#O |
| 1100 | STA \&FE62 |
| 1110 | STA \&FE6B |
| 1120 | STA \&206 |
| 1130 | LDA *\&FO |
| 1140 | STA \&FE6C |
| 1150 | LDA \#\&7F |
| 1160 | STA \%FE6D |
| 1170 | LDA \#\&DO |
| 1180 | STA \&FEGE |
| 1190 | LDA *HIMEM DIV256 |
| 1200 | STA \&207 |
| 1210 | RTS |
| 1220 | J |
| 1230 | NEXT 1\% |
| 1240 | e\%=2 |
| 1250 | CALL SETUP |
| 1260 | CLS: PRINT: PRINT: PRINT: PRINT |
| 1270 | REPEAT : UNT IL ?FLAG1 $=0$ |
| 1280 | FOR 1\%=0 TD 7 |
| 1290 | PRINT~M\%?I\%; " "; |
| 1300 | NEXT |
| 1310 | ?FLAG1 = $1:$ PRINT |
| 1320 | GOTO1 270 |
| Li | stz |
| 100 | Made7 |
| 110 | \#FX9, 10 |
| 120 | \#FX10,10 |
| 130 | INPUT"VOLUME 0-15 (ppp-ff) "V\% |
| 140 | IF (V\%) 150RV\%<0) G0TO130 |
| 150 M | MODES |
| 160 | H M $M E M=\$ 5500$ |
| 170 | FLAG3=HIMEM+26C |
| 180 | VOL\% $=$ HIMEM + K 69 : COU=HIMEM + \% 68 |
| 190 | DISPLAY=HIMEM+\&AO |
| 200 | T I BLE\%=HIMEM+\&200: TUBLE\%=HIMEM+\&280 |
| 210 | ?VOL\%=15-V\% |
| 220 | VDU23; $0202 ; 0 ; 0 ; 01$ |

230 VDU19,2,8,0,0,0: VDU19, 1,0,0,0,0 240 DIMA\% (11)
250 FORI\% OOTO11: READAY (I\%) : NEXT
260 DATA $88,422, \& 22,488,488,222,422,488,488,222$,
$288,489,26 \mathrm{C} 30,26 \mathrm{BOB}, 26 \mathrm{AFO}, 26 \mathrm{C} 50,26 \mathrm{C} 38,26 \mathrm{B1O}, 26 \mathrm{AFB}$
\& $6 \mathrm{C} 58, \& 6 \mathrm{C} 40, \& 6 \mathrm{~B} 1 \mathrm{~B}, \& 6 \mathrm{C} 4 \mathrm{~B}, 86 \mathrm{C} 60$
$270 \mathrm{~A}=\mathrm{L} N(1760)$ : $\mathrm{B}=\mathrm{L} \mathrm{L} N(2) / 12$
280 FORK\%=OTOS
290 FORJ\%=0TO
300 READ PAXY
310 FORI\%=0T03:FORPAT\%=0TO7
320 PAXX? ${ }^{2}$ PAT $=$ =A\% ( $2 * K \%+J \%$ ) : NEXT
$33011 \%=4 * I \%+2 * J \%+16 * K \%$
340 ? (TIBLE\%+11\%) =PAX\%MOD256: ? (TIBLEK+11\%+1) =PAXZ DIV256

350 PAX\%=PAX\% $+838(A+(K \%+6 * J \%+12 * 1 \%-45) * B))$
370 I1\%=INT (X+5)
380 TUBLEY ? $(4 * I \%+2 * J \%+16 * K K)=11 \%$ MOD 16
390 TUBLEK? $(4 * I \%+2 * J Y+16 * K Y+1)=11 \%$ IVIV16:NEXT,
490 TUBLEX? $(4 * 1 \%+2 \# J \%+16 \# K X+1)=11$ KDI
410 FOR PAT $\%=0 T 07: ?($ PAT $\%+\% 6 D 10)=\$ 8 B:$ NEXT
$420 X=125000 /(E X P(A+3 * B))$
430 I $1 \%=$ INT $(x+.5)$
440 TUBLEX?\&60=11 \%MOD 16 :TUBLE \% ? \& $1=11$ \%DIV1 6
445 REM Lines 450 to 1220 inclusive
446 REM are the corresponding lines of List 1
1230 PKㅍDISPLAY 1980 .CH1 LDA TUBLEX, $x$

1240 [ OPTI\% 1980 .CH1 LDA
1250 LDA \#\&15 1990 ORA \#\&CO
1260 LDX *7
1270 JSR \&FFF4
1280 DEX
1290 JSR \&FFF4
1300 DEX
1310 JSR \&FFF4
1320 LDA \#3
1330 STA FLAG3
1340 LDA \#\&FF:STA \&FE43
1350 LDA *O
1360 STA COU
1370 INC FLAG1
1380 - STI SEI
1390 LDX COU
1410 BEQ LASTA
1420 LDA M\% +1,
1430 LDY \#O
1440 .SHIFT ASL A
1450 PHA:STY \& $70^{A}$
1460 BCC RST
1460 BCC RST
1470 JSR SET
1480 JMP OVER 1490 . RST JSR RESET
1490 - RST JSR RESET
1500 . OVER PLA:LDY \& 70
1510 INYE INY
510 INY:INY
530 CPY Wa10
540 BMP SLEIT
1540 JMP SHIFT
1550 . CLEI CLI
1560 LDA FLAG1
1570 BEQ DISPLAY
1570 BEQ DISPLAY
1580 INC CDU
1580 INC CDU
1590 JMP STI
1600 . LASTA NOP: JMP LAST
1610 . SET TXA
1620 ASLA: ASLA: ASLA : ASLA
1630 CLC
1640 ADC 870
1650 TAX
1660 LDA FLAG3
1670 BEQ FLASHA
1680 JSR PLAY
1690 JMP FLASH
1700 . PLAY CMP *3
1710 BNE CH2
1720 LDA TUBLE\%, $x$
1730 DRA *\&BO
1740 STA \&FE4F
1750 JSR SEND
1760 LDA TUBLEK+1, $x$
1770 STA \&FE4F
1780 JSR SEND
1780 JSR SEND
1790 LDA
1790 LDA *890
1800 DRA VOL\%.
1820 JSR SEND
1820 JSR SEND
1830 JMP NECH
1840 . CH2 CMP
1850 BNE CH1
1860 LDA TUBLE\%, $X$
1870 ORA \#\&AO
1880 STA \&FE4F
1890 JSR GEND
1900 LDA TUBLE\%+1, X
1910 STA \&FE4F
1920 JSR GEND
1930 LDA \#\&BO
1940 ORA VOL\%
1950 STA \&FE4F 1960 JSR GEND 1970 JMP NECH

2000 STA \&FE4
2010 JSR SEND
2020 LDA TUBLEY+1, $X$
2030 STA \&FE4F
2040 JSR SEND
2050 LDA $2 \& 0$
2060 ORA VOLX.
$20 B 0$ JSR SEND
2090 . NECH DEC FLAB3
2100 RTS
2110 . FLASHA NOP:JMP FLASH
2120 . SEND LDA \#O
2130 STA \&FE4O
2140 LDY \#\&A
2150 . ROU DEY
2160 BNE ROU
2170 LDA *B
2180 STA \&FE4O
2190 RTS
2200 . FLASH LDA TIBLEZ, $x$
2210 STA \&71
2220 LDA TIBLEZ $\%$, , $X$
2230 STA \&72
2240 LDY *日
2250 . COY DEY
2260 LDA \#\&FO
2270 AND ( $\& 71$ ), Y
2270 AND ( $\& 71), Y$
2290 TYA
2300 BNE COY
2310 LDX COU
2320 RTS
2330 . RESET TXA
2340 ASLA: ASLA: ASLA: ASLA
2350 CLC
2360 ADC \& 70
2370 TAX
2380 LDA TIBLE\%, $X$
2390 STA $\$ 71$
2400 LDA TIBLE\%+1, X
2410 STA \&72
2420 LDY \#B
2430 . COY 1 DEY
2440 LDA *\&F
2450 DRA ( $\& 71$ ), Y
2460 STA ( 271 ), Y
2470 TYA
2480 BNE COY
2490 LDX COU
2500 RTS
2510 . LAST LDA *\&FF
2520 CMP M\%+7
2530 BED FL
2540 LDA *\&日B
2550 JMP DOWN
2560 . FL LDA FLAG3
2570 BED FLA
2580 LDX *\&60
2590 JSR PLAY
2600 .FLA LDA \#\&80
2610 . DOWN LDY 7
2620 . RDUND ETA \& $6 D 10, Y$ 2630 DEY
2640 BPL ROUND
2650 CLI:RTS:
2660 NEXTI\%.
2670 CALL EETUP
2680 REPEATIUNTIL?FLAG1 $1=0$
2690 IFPM\% < >\&FF PRINT "EYNC
ERROR" 1 END
2700 CALL DIBPLAY
2710 GOTO 2680
ters are then loaded to give a time-out after 20 ms (lines 610 to 640). These latter two actions give a 20 ms delay for residual switch-bounce to die out and to allow for slight differences in the closing times of switches if more than one note is altered at a time. This amount of delay seems to be about right, but it is easily altered (up to about 65 ms without any significant change in the coding) by changing the bytes loaded at lines 610 and 630, noting that 4 E 20 is the equivalent of 20 ms (decimal).

The low byte of IRQ2V is altered to point to HIMEM +30 (lines $650-660$ ) and the CB interrupt flag cleared (lines 670-680) before the $\mathrm{A}, \mathrm{X}$ and Y registers are restored from the stack and the return-from-interrupt is performed. Thus interrupts are reenabled during the 20 ms delay to allow the computer to go about its housekeeping duties.

When timer 1 times out, code at HIMEM +30 is entered (line 760). Again, the registers are saved, and port B of the v.i.a. is read and stored in the l.s.b. of $\mathrm{M} \% . \mathrm{CB}_{2}$ is then brought low (lines $870-880$ ) and high again (lines 890-900) thus clocking $\mathrm{IC}_{10}$ in the interface. Lines 840 and 920 are looped eight times in all, the bytes being read into consecutive memory locations by the offset X (lines $850-860$ ). $\mathrm{CB}_{1}$ interrupts are enabled again (lines 930-940); IRQ2V is restored to its previous value (lines 950-960); FLAG1 is reset to zero as a signal to the display routine that the keyboard has been read again; the interrupt flags are cleared and the interrupt routine terminated. The Basic display routine at lines 1270 to 1320 simply loops in line 1270 until advised by FLAGl that its attention is required. It then prints the values of the eight-bytes from M $\%$ in two-digit (line 1240) hexadecimal fields.

To test the interface, run this program and press the bottom note on the keyboard. This should cause the line

## FF 8000000 FE

to be displayed (bytes 0-7). Byte zero should always be FF as explained in the July issue. If the FF appears elsewhere (or not at all) then the interface is out of syuchronization and the reasons for this need to be explored. Circuit $\mathrm{IC}_{10}$ not resetting to zero on switch-on is a possibility, as is
spurious pick-up of electrical noise. However, since sorting out some teething troubles with the prototype, synchronization problems have been rare. Some suggestions for dealing with any that do arise are given later.

Assuming all is well at this point, releasing the key should produce

## FF000000FE

Pressing the lowest $\mathrm{C} \#$ should produce

## FF 0800000 FE

and so on. Notice that the lowest six notes ( C to F ) are taken to bit seven of bytes one to six respectively, the next six notes ( $\mathrm{F} \#$ to B ) are taken to bit six of these bytes, etc. This is to avoid a large number of p.c.b. wire crossings. Thus byte one contains the bits for the four $\mathrm{F} \#$ s interleaved with those of the first four Cs, byte two contains the Gs interleaved with the C\#s, etc. Top C(the 49th note) is
taken to bit zero of byte seven, so playing top C should produce

## FF00000FF

Playing several notes at once should, of course, affect all the relevant bits and thus will be reflected in the values of the bytes displayed. The system responds quickly despite the 20 ms delay it is easy to obtain multiple readings when playing chords unless one very carefully presses the notes evenly. In practice this is not likely to be of consequence since such spurious responses can be rejected by software if necessary. To terminate this program press ESCAPE.

List 1 is all that is necessary to read data from the keyboard into the computer, which can then be used as desired. As an illustration, the program in List 2 uses the data to play the computer's three-channel sound generator. It also displays a representation of the keyboard on the screen. The

Circuit boards for this project will be available from Lancaster ITEC for around £10. Please send an s.a.e. to the ITEC at St Leonard's House, St Leonardsgate, Lancaster, Lancashire, LA1 1NN for further information.

## Computers and Music at Lancaster

The University's Music Department is interested in all aspects of computers as a musician's aid but not so much with synthesizers. Our main current interest is in the potential of the computer as a teaching aid for music theory and aural training, right through from beginners to university/college level. The biggest problem until now has been the lack of a satisfactory means of student feedback to the computer, the typewriter keyboard being unsuitable for this, except at a rudimentary level.

We have now successfully designed two economical accessories which go a long way toward solving the music-input problem - the four-octave music keyboard and a 'rhythm box'. Keyboard software packages for the BBC microcomputer currently under development include
o Keyboard familiarity, in which the computer displays a note on the stave and the pupil has to identify the note on the keyboard.

- Major and minor thirds, in which the computer plays a major and replaying them at different or minor third at random and dis- speeds. These phrases can be plays the lower note on the stave. stored on disc or tape which offers The pupil has to identify major or the possibility for pupils learning,
minor and play the pair of notes on the keyboard after which the computer displays its version. A score of successes is kept.
- Interval recognition plays and displays a random note from anywhere on the keyboard then selects and plays another random note from the same octave but doesn't display it. The pupil has to recognize the interval and play the second note. Again a score is kept.


## - Triad recognition plays a tonic

 triad in close position (any key, root position or an inversion). The computer displays the key name but not whether it is major or minor and the pupil has to play the correct chord on the keyboard. Both the computer's and the pupil's versions are then displayed.Further teaching software envisaged includes recognition of cadences, musical dictation and transposition.

We also have software for playing musical phrases into the omputer through the keyboard
say, the violin to have an accompaniment which is gradually speeded up as the pupil's proficiency increases.

The 'rhythm box' allows rhythm information to be accurately fed into the computer by tapping on the box with a pencil. Its main purpose is to use the computer to give feedback on the type of rhythm exercises normally carried out by hand clapping. It also has some interesting recreational (and perhaps compositional) possibilities. For example, a tune can be played into the computer through the keyboard and its rhythm altered by tapping out a new rhythm on the box.

Our next hardware project is to enhance the computer's sound capability. Direct musical input to the computer from voice or musical instrument is also a possibility. In the more distant future we envisage small computers being used for music transcription, editing, printing, etc. The centre has expertise in music, computing, engineering and other disciplines and is well placed to make real contributions in this field.


## by T. Segaran.

Fig. 1. Use of d.c. enables simple method of multiplexing leds.

Fig. 2. Without multiplexing, 1.c.d. drive simply requires a line for each segment and a common lead, which carries a 1:1 square wave, as in (b). Segment drives are either in phase or antiphase with common line waveform.

## Multiplexing Liquid Crystal Displays

## A short description of the 'black art' of driving l.c.ds.

Although I.c.ds are finding their uses in various products, driving them seems to be a bit of a black art. Many users tend to connect the appropriate driver chips and the displays together, without fully appreciating the intricacies of multiplexing the driver. Below is presented an explanation of various levels of multiplexing.

It is best to start by drawing an analogy with driving leds, which
need a direct current to light them. Multiplexing leds is therefore a relatively simple matter Fig. 1 shows a suitable arrangement. The segment lines (8) and the digit-enable lines define the particular segments that need to be lit up. As long as each digit is addressed about 50 times per second, no flicker will be visible to the human eye.
L.c.ds, however, can be
 tages and have slower response times than leds. Because of this, the multiplexing scheme for l.c.ds is rather more complex. When no multiplexing is needed (because few digits are being driven, say 4) the arrangement shown in Fig. 2(a) is adopted. Each segment of each digit has a unique line running to it from the driver. The backplanes of all the digits are commoned and are also connected to the driver. Hence, a four-digit display will have 32 segment connections and a common connection, which carries square wave of equal mark: space ratio, as in Fig. 2(b). All segments that need to be lit have a waveform which is 180 deg . out of phase with the common, as at (c), while those that do not have a waveform in phase with the common. Figure 2(d) shows the resulting polarizing waveforms across the segments that are selected. The non-selected segments have no net voltage across them.

When two-way multiplexing is needed, the scheme in Fig. 3(a) is adopted. There is a connection from the driver to a pair of segments and there are two backplane connections to each digit.


Thus, for example, in an 8 digit display there would be 32 segment lines and 2 common lines. Notice that the common 2 waveform shown at (b) lags behind the common 1 waveform by 90 degrees. When segment ' $a$ ' needs to be lit up and segment 'b' must be off, then the waveform shown at (c), is impressed on the segment 1 line. The first half of the waveform is 180 deg . out of phase with common one thus lighting up segment ' $a$ ' and the latter half of the waveform is in phase with common two, thus switching off segment ' $b$ '. A similar waveform at (d) on segment 2 lights up segment ' $e$ ' and keeps segment ' $d$ ' switched off. Thus, although one reduces the number of lines by a factor of two, an additional complexity of a special waveform with three voltage levels is needed, to implement two-way multiplexing.

Three-way multiplexing is not commonly used, but four-way multiplexing can be discussed. In this scheme, shown in Fig. 4(a) each segment line from the driver is connected to four segments in each digit. There are four common connections (backplanes) to each digit, each common line connecting to two segments in each digit. Each waveform on the common lines in (b) is 45 degrees out of phase with the previous one. If all four segments connected to the segment 1 line ( $a, d, e$ and $f$ ) need to be lit, then the waveform shown at (c), is output on that



Fig. 3. Two-way multiplexing requires two common lines.

Group of 4-digit Lucid l.c.ds from EE Valve Company
line. Note that this waveform is 180 degrees out of phase with each of the peaks in the common waveforms, resulting in a large polarizing voltage across the four segments, as at (d). If segments ' b ' and ' c ' must be lit, while ' $g$ ' and
' h ' are off, then the waveform shown in Fig. 4(d) is applied to the segment 2 line, resulting in the appropriate polarizing voltage. Figure 4(f) shows the voltage across a non-selected segment. Four-way multiplexing, although reducing the interconnection between the driver and I.c.d. considerably, requires still more complex drive circuitry and four voltage levels. Another disadvantage is that the difference in voltage applied to a selected segment compared to that applied to a non-selected segment is less than in other cases. Table 1 summarizes the drive volts.

Multiplex drive of alphanumeric and graphics l.c.ds follows the same principle used in driving

Table 1.

|  | Static | 2 -way | 4 -way |
| :--- | :---: | :---: | :---: |
| Voltage levels <br> required | $0, V_{\mathrm{icd}}$ | $0, \mathrm{~V}_{\mathrm{icd}} / 2$ | $0, \mathrm{~V}_{\mathrm{icd}} / 3,2 \mathrm{~V}_{\mathrm{icd}} / 3, \mathrm{~V}_{\mathrm{icd}}$ |
| Selected segment | $\mathrm{V}_{\text {icd }}$ | $\frac{3}{4} \mathrm{~V}_{\mathrm{icd}}$ | $\mathrm{V}_{\mathrm{icd}} / 2$ |
| Non-selected <br> segment voltage <br> (r.m.s.) | 0 |  |  |

## LIQUID CRYSTAL DISPLAY



Fig. 4. Four-way multiplexing, needing four common lines.

numenc displays. The number of common lines used can get as high as 8,16 or 32 : the limiting factors are how closely the thresholds on the displays can be determind and how well the refer-
ence voltages for the steps can be defined. For example, when 16 common lines are used, the voltage across a selected segment is $1 / 4 V_{1 . c . d}$ and the voltage across a non-selected segment is $11 / 80$
$V_{1 . c . d}$ The threshold for an I.c.d. segment must be between these two levels and the tolerance on the reference levels must be an order of magnitude better than those levels, i.e. about $0.1 \%$.

# SPEED OF LIGHT <br> Light may, or may not travel at constant speed in vacuo, but it must do when it meets matter. 

The human mind possesses a strong urge to seek after the truth; but is it perhaps a feeling of pseudo-religious fervour which allows us to accept theories and phenomena which are not easily comprehensible or are even paradoxical? One rather perplexing concept falling within this category is the constancy of the speed of light notwithstanding any velocity the light source may have relative to that of the observer.

When my own study of elementary physics brought this seemingly illogical concept into focus, I recall being rather more intrigued than bewildered. And when this concept was linked to relativistic notions and dramatized by the story of a frail old man being greeted by his very young twin brother on his return from a trip round Andromeda, I became fascinated by the possible credibility of such mystical ideas. The
last thing I wanted was anyone to destroy this illusion - and in any case, all the loose ends appeared to be well taken care of by the Special Theory of Relativity. I therefore became very sceptical about articles I read which dared to suggest that some aspects of the theory were perhaps a little suspect. I voiced, rather than seriously considered the point that if the theory were not as stated there appeared to be no other satisfactory explanation of the peculiar way which light seemed to behave. Certainly all experiments appeared to show that the speed of light is constant. And it also appeared logical that the speed of light should be constant everywhere in the universe.

Although I accepted the usefulness of considering the photon in some circumstances, the model I built up in my mind to visualize and explain, say, the prop-
agation of radio waves, was a very electromagnetic one. It seemed so natural that the magnetic field created by the accelerating electrons in an antenna, should combine with the electric field across various parts of it to send forth an electromagnetic wave. Despite these views, however, I was very uneasy about the classical concept of electromagnetic propagation. It was not until I took courage and seriously questioned my electromagnetic model that some rationality dawned.

Although it is often convenient to reason in terms of electromagnetic fields, the important part that matter plays in their propagation can easily be overlooked and forgotten. This, of course, is not the case when one considers the quantum model and that photons actually emanate from within the atomic structure as a whole. Well, the stuff that light is made of
either does emanate from within an atom or it doesn't. If it doesn't then the present most popular electromagnetic theory may suffice, though continue to be confused by the existence of photons. But if it does emanate from within an atom, one must at least try to satisfy the concept of duality by modelling the photon on say a toroidal package of electric and magnetic fields in quadrature. If, however, this makes one nervous, better forget duality and concentrate only on photons.

Whatever forces exist within an atom, there is one inviolate law which states that the initial velocity of photons propagated from an atom is constant, i.e. our accepted value of approximately $3 \times 10^{8} \mathrm{~m} / \mathrm{s}$. It is, of course, fairly well understood that atoms can absorb and generate radiation at very specific frequencies. And, due possibly to the various transition times of photons within an atom's sphere of influence, it is also a fact that the speed of light in air is markedly different from that in, say, water. It is adequately demonstrated, therefore, that all photons are profoundly affected by atoms irrespective of any signature frequencies or densities, when, during their flight through the universe, they enter the space governed by these particles. There may even be room for argument that such photons leaving an atom's influence are not the same ones which entered it. Whether or not this is the case, photons which leave the field of an atom, must surely be subject to the same law which governs photons actually generated by the atoms.

If this postulate can be accepted - and it doesn't exactly violate any existing laws, it becomes quite evident that the speed of light measured by a stationary observer must always be constant, because the relatively dense air through which the photons have been travelling is also more or less stationary with respect to the observer. In fact, every encounter between a photon and an atom, ensures the maintenance of this constant velocity.

Consider now what happens to light emitted by a body moving towards the Earth at say 0.2 of the speed of light. Depending on one's 'faith' (and let's face it, most students, if not mature people, will believe what they are told if the argument is made to look plausible enough), light will either leave the moving source at
1.2 times our accepted value (with respect to Earth) or at exactly our accepted value with all the Lorentz modifications to the source in order to balance the books. The latter value, of course, lands us right in the middle of our present jungle of controversy - espcially with respect to the supposed time dilation. As there are many persuasive and intelligent people of great experience who cannot digest this principle, is it not logical, despite all the rhetoric against their arguments, to conceive that this same principle is not proven beyond all shadow of doubt. If we can tolerate the suggestion that there is just a tiny shred of logic in this view, let us ignore (for a few minutes) the Lorentz complications and consider what happens if we assume that the light from our moving object commences its travel towards Earth at 1.2 times our accepted value.

First choose to imagine that a perfect vacuum exists between the moving body and the outer reaches of the Earth's atmosphere. With no atomic encounters on their journey, the ejected photons would be entering the atmosphere at the same enhanced speed. However, on their very first encounter with an atom of its gas, the velocity of a photon entering the particle will be immediately reduced to the velocity of light with respect to that atom, i.e. our accepted value. To maintain the frequency (increased in this case by what might be confused with the Doppler effect), this lower but normal velocity would produce light of a shorter wavelength and of course, blue shift. This also implies that the light would be of greater energy to compensate for the reduced velocity of the photons.

But a perfect vacuum, of course, does not exist and matter appears to be present in the order of several atoms per cubic centimetre of interstellar space. If one considers that only a single atom exists per cubic centimetre, then to a first approximation, the mean free path of the gas would be in the region of $10^{10} \mathrm{~km}$. Therefore, a photon travelling through space, would, on average, encounter an atom approximately every $10^{10} \mathrm{~km}$. Even by astronomical standards, this is a fair distance - about the same as the diameter of our solar system but is eaten up in about nine hours by the travelling photon. However, if we then consider just the
nearest star, whose distance is more conveniently measured in light years, a mean free path of $10^{10} \mathrm{~km}$ is quite insignificant. This means that light would hardly have left a star before its velocity is changed to that prevailing for the interstellar gas. Moreover, it means that the velocity of its emitted light may have changed thousands of times before it reaches Earth. Another interesting thought is that any two photons leaving the souce at the same time are each likely to encounter their first interstellar atom in anything from a few minutes to a few days after departure. For this reason there would be no defined frontal system. It is true that after several thousand possible changes in velocity, nearby photons should keep roughly abreast; however, there is bound to be some random grouping giving the effect of well how does the nursery rhyme go? twinkle, twinkle....?

Even if any of the famous experiments to determine the speed of light could have been carried out in deepest space, the results would have been just the same as they are on Earth. Photons may well arrive to within one atomic diameter of the apparatus at a velocity substantially different to the speed of light on Earth but the velocity would be immediately changed relative to that of the apparatus on encountering the first material interface.

Any satisfactory measurement of the speed of light in a near vacuum, would, of course, be rather difficult. One possible way would be to pass the light emitted from a star through a segmented glass disk rotating at high speed and noting whether the light passing through the glass overtakes or falls behind the light which travels directly. For accurate results, the disk would have to be an inconvenient distance from the measuring device - perhaps on the next Moon visit we may know. There would, however, be no way of actually measuring the initial velocity of, say, light emitted from stars with a pronounced red or blue shift due to the levelling out process mentioned above.

I am not saying that Lorentz contractions and quirks of time are just fantasy - interpretation of some phenomena suggest they are quite real. However, do we really need them to explain away a universal constancy of light which may not exist. Could not the tail, in fact, be wagging the dog?

The author
After his college studies were inter rupted in 1943 by wartime circumstances, Roy Hodges commenced a career in Broadcasting Engineering by joining the B.B.C. at their Brookman's Park transmitting station. Being only 15 years old at the time, he naturally feit himself a little at the deepend; however, he later transferred to the television service and after 10 years with the Corporation, he joined the Ghana Broadcasting Service and spent 8 years as Engineer in Charge of various departments.

For the next 2 years he was a stu dent at the Brighton College of Technology studying electrical and electronic engineering and within the following year obtained Corporate membership of the IEE. Since then, he has worked extensively in Africa and the Far East with various international organisations in the tields of broadcasting, electronics, produc management and education.

## Optical power

That strange genius Nikola Tesla used to believe that communication by radio waas simple: the real problem was to transmit power. In practice it was not until the 1960s that it was shown possible to keep a model helicopter flying by beaming up microwave power from the ground. Even today it is doubtful whether any practical use is made of power transmission - though at IBC82 it was suggested that DBS sound radio transmitters on 26 MHz could be so powered in what seemed largely an engineering pipe-dream.
Now the idea has spread to optical fibres with their extremely low potential attenuation but with thin fibres limited in power-transmission capability. Japanese engineers have reported (Electronic Letters, September 27, 1984, pp811-2) transmitting 39 watts over 420 cm of $1000 \mu \mathrm{~m}-$ diameter fibre and believe that with this diameter fibre the limit would be about 80 watts. Using a $\mathrm{CO}_{2}$ laser with a wavelength of about $5.3 \mu \mathrm{~m}$ the transmissions loss is about $0.3 \mathrm{~dB} / \mathrm{m}$.
It is suggested that the ability to transmit 40 watts "enables us to use this fibre for some practical applications" but the writers do not expand on these.
Meanwhile, the international rivalry to transmit high-speed data over long lengths of optical fibre, using coherent systems, continues. At a recent Open Day at British Telecom Research, Martlesham, the claim was made that the adoption of superheterodyne principles for reception combined with the ability to obtain a narrow-band coherent output from semiconductor lasers has enabled BT to establish a world record by transmitting a $140 \mathrm{Mbit} / \mathrm{s}$ (the BT notes gave $140 \mathrm{kbit} / \mathrm{s}$ but I am assured this was in error) along 200 km of fibre without intermediate regeneration. A long external-cavity laser being studied at Martlesham has a linewidth of only 10 kHz and can be tuned over a $13,000 \mathrm{GHz}$ range, so opening the way for frequency multiplexing a series of independent transmissions over a single fibre.

However, on the "anything you can do I can do better" principle, I note that Japanese
engineers have reported (Electronic Letters, September 13, 1984) on optical link of 216 km with a total attenuation of 46.3 dB at $1.55 \mu \mathrm{~m}$, using 107 splices.

## Interscan or TRSB?

A distinctly aggrieved piece by Philip Watson in Electronics Australia (July 1984) complains that the microwave instrument landing system,, generally known as TRSB' (time reference scanning beamı, chosen in 1978 by the International Civil Aviation Organization and now in production, although of CSIRO origin, is seldom credited to Australia. As the Interscan system it was first field tested at Melbourne.
According to Philip Watson, the system should rank as one of Australia's most significant scientific achievenients, adopted as a world standard in preference to competing designs from the UK, continental Europe and the USA. Yet, he adds, "it is both surprising and disturbing to find that
Australia's role in this development has been completely ignored overseas."
Interscan TRSB operates on 5 GHz with a range of about 37 km and, among its advantages over still-current v.h.f., ILS can handle several aircraft at the same time. The system, as a world standard and including developmdents introduced in the USA, is not subject to basic patent restrictions but the Aussies are clearly sore that its origins and the early work at CSIRO are being overlooked

## Farewell 405

During the first week of January, 1985 405-1lne television (System A) will finally disappear into history - where for the vast majority of viewers it has been for the past decade. Announced as a British television standard (competing with Baird's 240 -line sequential system) during 1935, it has narrowly failed to make 50 years. The long-lasting Blumlein waveform, with positive modulation (excellent apart from susceptibility to ignition interference) and a.m.
sound might have been a 243 line system if the EMI team had not tried the effect of changing one divide-by-three multivibrator into a divide-byfive ( $243 \times 5 / 3=405$ ). The system which gave Britain a significant lead in world television will be remembered with affection by all who were not unduly worried by the often audible 10.125 Hz line-whistle. In association with 4.5 in image orthicon tube cameras it gave really excellent black-and-white pictures - and even the visible line-structure could have been alleviated if the public had taken kindly to spot-wobble techniques.

It was interesting to learn at IBC84 that there is a revival of interest in the USA in synchronized spot-wobble as an aid to high-definition television - a technique oyiginated by Alan Blumlein but which Leslie Jesty strove to introduce for so many years. It was fitting that he was in the audience at IBC84, though describing himself as "an unemployed engineer".

IBC84 provided further evidence of the very impressive results that are possible with the 1125-line ( 60 Hz ) NHK and the proposed 1249 -line ( 80 Hz ) BBC systems. The pictures on Sony's excellent 6 ft winde projection system could not be faulted, though both standards require over 20 MHz of video bandwidth and may never be possible in Europe for terrestrial or 12 GHz satellite broadcasting, although suitable for electronic cinematography and closed-circuit working.

Single-tube colour cameras are still finding it difficult to compete with three tubes. For display, the principle of the single-gun beam-indexing tube has been around for a very long time. On several occasions it has been hailed as the coming thing in brighter colour displays. One of the few such tubes actually in production is a 1 in tube made by Hitachi for camera viewfinders. Sony Corporation, however, has recently announced a new beam-index projection tube with a 5.25 inch faceplate and ethylene glycol-water cooling, claimed to have a peak brightness of about 1600 lumen, roughly seven times more than with good shadowmask tubes. The tube is being put into a home video
projection unit, with built-in v.c.r. and cable tv tuner, marketed in the USA at about $\$ 3000$.
Sony are also to provide a giant mosaic-type display ( 160 ft diagonal) for Expo 85 near Tokyo. visible at distances up to 1 km . This uses light sources having fluorescent-type cells with three electron flood sources, one for each primary colour, with a common cathode to provide a 40 m wide by 25 m high screen with a 400 by 378 array.

## Amateur Radio

## New IARU "locator"

From January 1985, a new IARU locator system will come into use with the UK and possibly throughout the world. It will enable v.h.f. and u.h.f. operators to pass, in a single six-character coded group, information on the location of their stations to within a couple of kilometres. It replaces a European-only "squares" locator system and was originally devised by John Morris, G4ANB as the "Maidenhead" system.
In the new system the world is divided first into 324 "fields" each covering $20^{\circ}$ of longitude and $10^{\circ}$ of latitude. Each field is labelled by two letters between " $A$ " and " $R$ ", the first letter indicating the longitude starting from $180^{\circ}$ West; the second, latitude starting from $90^{\circ}$ South. Thus most of the UK west of the Greenwhich meridian is in the field " 10 ", but the Channel Isles, Isles of Scilly and Lizard Point, Cornwall are in field "IN", most of the Shetlands in "IP", and those parts of England east of Greenwich in "JO"
Each field is then divided into 100 "squares" arranged as a 10 by 10 grid, each covering $1^{\circ}$ of latitude and $2^{\circ}$ of longitude, each designated by a digit " 0 " to " 9 ", the first indicating longitude, starting from the west, and the second the latitude starting from the south. Finally each square is divided into a 24 by 24 grid of subsquares, each covering 5
minutes of longitude and 2.5 minutes of latitude, labelled by two letters, each in the range A to X starting from the Southwest corner. Thus the location of Edinburgh Castle using the new system is 1085 JW . There is however no truth in the rumour that anyone working out their locator group correctly at the first attempt will be excused from taking the Radio Amateur's Examination.

## Costly h.f.?

Exchange rates and inflation continue to push up the cost of imported top-of-the-range amateur radio transceivers, leading to a revival of interest in "economy" designs and home construction, though a flexible, full-specification design is not a matter to be tackled lightly, on the kitchen table.

As an example of rising prices, one notes that the basic Yaesu FT-ONE h.f. transceiver is currently being offered at about $£ 1570$ compared with $£ 1450$ in October 1983 and $£ 1295$ in October 1982. The absence of UK production or assembly makes the imported equipment very vulnerable to changes in exchange rates. Japanese domination of the world market for such equipment is challenged only by a few American firms with, for example, the innovative Drake firm now increasingly concentrating on "professional" markets, and with some of the remaining American firms relying on assembly in the Far East.
Nevertheless, it still remains possible for newcomers, who do not aim too high initially, to assemble a complete h.f. transmitting station for less than $£ 100$ by using secondhand equipment and aerial based on wire suspended from trees or houses rather than aluminium tubing arrays mounted on towers.

At the other extreme one notes aerial tuning units, for which many of us still use just an odd coil and capacitor, priced at around $£ 350$ !

More use now appears to be made by both newcomers and long-established amateur h.f. stations of the lower h.f. bands, including $1.8,3.5$ and 7 MHz , reflecting the recent rapid decline in sun-spot activity, including a period in September
of almost a fortnight during which no spots were visible on the face of the Sun. We now face a period of two or three years of sunspot minimum, with the consequent lowering of the maximum usable frequencies, difficulty of working across the day/night barrier, and the virtual fade out of the 14 MHz and higher bands at night.

It will be interesting to see if the IARU Region 1
recommendation of no s.s.b. on 10.1 MHz remains unchallenged for long, with numbers of amateurs in Region 3 (Asia, Oceania) already operating phone in this narrow band. Currently in Europe, c.w. activity on 10.1 MHz is at only a moderate level.

## Back to batteries

It has long been a truism that for portable operation it is relatively simple to convert electric power into r.f.: the difficult part is finding an economic source of the basic electric power. With most modern amateur transceivers designed for operation from 1214 volts d.c. as mobile units, many amateurs are finding that the home construction of reliable regulated mains power supply units, fully protected against delivering an overvoltage output, and capable of supplying peak outputs for s.s.b. operation of over 30 A , is by no means as simple as it may appear at first sight. Lowcost, series-pass transistors, such as the 2 N 3155 , although nominally rated at 10 A , often prove to have "worst case" ratings of around 5 A , and their failure, invariably a shortcircuit, can typically put a damaging $18-20$ volts into an expensive transceiver.
"Crowbar" protection can similarly present unanticipated design problems, even though nominally fail-safe.

There is thus a growing belief that the simplest, most reliable and usually most economical solution for base operation of 12 V transceivers is the use of a float-charged car battery. The introduction of fully-sealed "maintenance-free" lead-acid batteries help to overcome the former fear of causing domestic damage to carpets etc. due to acid spillage. Standard car-battery chargers can be used with 4 A or 8 A rating.

The conventional lead-acid battery still tends to survive only a limited number of charge/discharge cycles without the plates buckling, shedding active material (which can short-circuit the cell) or suffering from sulphation. New Zealand amateurs, in particular, seem prepared to tackle the problem of regenerating cells by such techniques as washing out the shed material, or by using Glauber's salt solutions to counter sulphation, in a manner originally described in 1912 and also in Wireless World in 1941!

## Here and there

By October the sequence of G4Z Class A licences was almost exhausted and GO callsigns may begin to be issued late in 1984 or early 1985.

The Channel 4 play "CQ" took pains to provide an authentic portrayal of amateur radio operating. No so TV Times which must have upset some amateurs by saying that "peppered throughout the entertaining script is the freaky language of Citizens' Band radio"!

Pierre Lorain, F2WL's definitive book on the weapons, radios and techniques of SOE published in France in 1972 has been published in the USA and UK in an English-text adaptation by David Kahn as "Secret Warfare" (UK, Orbis Publishing Ltd., 185pp £7.00 hard covers). It includes clear drawings of about 15 different suitcase and pocket transmitterreceivers used for clandestine communications, 1941-45.

While the recent excellent BBC documentary series on SOE included sequences with John Brown, G3EUR showing the use of radio equipment, it seemed odd that a later episode gave the impression that Captain Hudson, the first SOE man with the Yugoslav Resistance, sent out his own radio messages. In face he was accompanied by H.W. King, who now lives near Oban, as radio operator, burdened down by a heavy Mark 3 transmitter supplied to SOE by Intelligence.
Australian amateurs will benefit - but possibly not for many years - from the decision of the Department of Communication to phase out some of the anomalies in Australian television frequency
allocations. Channel 5 A , adjacent to the 144 MHz band, will disappear, along with television usage of three channels in the v.h.f. boardcast band (Band 2) which 20 years ago was made available to Australian television but are now required for the expansion of v.h.f./f.m. radio. But it may be years before these decisions can be implementated.

PAT HAWKER, G3VA

## In brief

The September "Commentary" noted the Government's proposed transfer of the 22 high-power broadcast transmitters belonging to the Foreign \& Commonwealth Office's Communications and Broadcast Department to the BBC. Four months after this became known, no final decision has been made, with various committees examining the proposals which are bitterly opposed by many of the engineering staff. Though FCO still seems to hope to push these changes through, they are cagey about suggesting any possible timetable.
New battery technology is claimed as a key factor in the new Peugot Electric 205 car with a claimed operating range of between 100 and 140 miles at average speeds of 45 and 27 mph and to speed of 60 mph . The 12 nickel-iron 6 volt batteries have cells not completely separated, and each can be filled from a single opening; they provide about twice the energy of standard cells of similar size with 1500 recharge cycles possible. Battery recharging however takes 10 hours and the cost of the batteries is very high, reflecting the use of expensive materials.

Phone-in radio programmes can be dangerous work in the USA. Alan Berg, a controversial host on KOA Denver was shot dead last summer, after having been threatened several times - by no means the first American broadcaster to be gunned down apparently by angry listeners.

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[^2]
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CIRCLE 28 FOR FURTHER DETAILS ELECTRONICS \& WIRELESS WORLD DECEMBER 1984

# Mimicontroller 

## Given the i/o and assembly-language facilities of a BBC model B microcomputer, developing a controller is no great problem but to have the computer permanently running say a central heating system is a waste. The economical 6502 Mimicontroller takes over such applications.

Many secondary schools and an increasing number of primary schools are realising that the microcomputer is a powerful controller. They are also discovering that young people are strongly motivated by design tasks which challenge them to use electronics to control hardware ranging from model-maker's motors and N -gauge railways to mock-ups of c.n.c. drilling machines. Interface units allowing a microcomputer to drive motors, lamps, loudspeakers, etc., from an input/output port are now produced by a number of companies and various attempts have been made to produce control languages which make it easy for children of all abilities to, for example, switch a bit on or off.

At the other end of the spectrum there are microprocessor systems whose primary function

[^3]may be to teach machine-code programming but which are easily turned into controllers. Microprofessor is perhaps the bestknown currently available unit; Kim, Acorn System One and MK14 have all come and gone.

For the teacher, the choice is not an easy one. Microcomputers offer high-level language which may in fact be no easier than machine code for the complete novice - but cost much more than 'minimal' systems. Microprocessor controllers run only machine code but they come at a price that is much more likely to allow a whole class to have hands-on experience.

Since 1979 I have been exploring the possibility of using minimal dedicated machine-code controllers in the classroom and trials with a small three-chip design at Belper High School in Derbyshire have proved successful. Briefly, code for the controller was developed on either the Sinclair Radionics' MK14 or a purpose-built

2716 emulator* and programmed into an eprom for insertion into the three-chip controller. The other two i.cs in the controller are an INS8060 microprocessor and INS8154 ram/p.i.o. device. Cost of this board was about $£ 30$.
As the cost of the main i.cs on this original board has risen so the cost of 65 -family processors has fallen, and more schools have experience of 6502 machine code than hitherto. The need to redesign the controller was clear. At the same time I felt that being able to somehow make use of the BBC microcomputer assembler would be a significant advantage (this computer outnumbers others in schools). As well as the obvious advantage for the enthusiast who has outgrown handassembly of machine code, there is the possibility of tempting teachers to have a go at machine

[^4]


Mimicontroller is designed to emulate BBC microcomputer i/o in control applications. Software is developed and tested using the computer's assembly-language and $\mathrm{i} / \mathrm{o}$ facilities then transferred to a 2716 eprom for use in Mimicontroller. Ports A and B and control lines of the 6522 v.i.a., top left, act as those on the BBC microcomputer.
code control using a computer that they are already familiar with.

Using the assembler to help people write their first bytes of machine code bridges a conceptual gap which may otherwise be too large; thus Mimicontroller was developed.

## The circuit

Simplicity of the controller can be seen in the circuit diagram components, including zero-insertion-force socket for the eprom but excluding p.c.b., cost less than $\mathfrak{£ 2 0}$ which makes it attractive for school use. More relevant to most readers though is that Mimicontroller opens up the possibility of using the BBC microcomputer as a development tool for all sorts of applications from central-heating control upwards but without committing the computer to the actual job of controlling the application. Once the code has been developed, it is programmed into a 2716 eprom,
using one of the several eprom programmers available for the computer, and transferred to Mimicontroller.

The crystal, $\mathrm{IC}_{7}, \mathrm{R}_{4}$ and $\mathrm{C}_{4}$ and part of the 6502 produce inverted clock signal $\overline{\mathrm{CK}}$ and $\mathrm{IC}_{7 \mathrm{c}}$ a noninverted clock for the 6522 timers. The BBC microcomputer has a 2 MHz clock whose frequency is halved through a complex system when certain memory devices are accessed. Because of this it is virtually impossible to design a simple controller that will mimic the computer in respect of software timing. Memory i.cs rated at 350 ns are significantly cheaper than faster ones - a further reason for not attempting to clock the Mimicontroller above 1 MHz .

The 6522 versatile interface adapter, v.i.a., has elaborate timing facilities, however, and Mimicontroller's 1 MHz clock is the same as the microcomputer's 6522 clock. If software timing/ delay loops are avoided and the v.i.a. used instead on both com-
puter and controller, there should be no problems.
To prevent bus contention between the different memory devices, it is preferable to stobe memories with the CK clock. By feeding $\overline{\mathrm{CK}}$ into or gates $\mathrm{IC}_{8 \mathrm{za}, \mathrm{c}}$, the chip-select signals to $\mathrm{IC}_{5,6 \mathrm{c} 4}$ respectively are only allowed low when CK is high, provided of course that $\mathrm{IC}_{3 / 9 \mathrm{c}}$ have selected the device in question. In a similar way, $\mathrm{IC}_{8 b}$ strobes read/write line R/W using CK.
Gate $\mathrm{IC}_{7 \mathrm{a}}, \mathrm{C}_{1}$ and $\mathrm{K}_{1}$ form the power-on reset circuit; 74LS14 Schmitt inverters are used solely to give a short rise time to the reset signal. Time constant of $\mathrm{R}_{1}$, $\mathrm{C}_{1}$ should not be shortened.
The rest of the circuit produces the address map. It would be possible to assemble code for any 6502 system using the BBC microcomputer, but it's a significant advantage to be able to test the software by running it on the computer, modifying it if necessary, before committing it to eprom for use with Mimicon-
troller. Relevant addresses within the microcomputer are
$0070-008 \mathrm{~F}$ page-zero ram (reserved for user)
$0100-01$ FF 6502 stack
$0200-27 \mathrm{FF}$ area chosen for program

FE60-FE6F 6522 v.i.a.
One other point to bear in mind is that the processor loads its reset vector and interrupt vectors from FFFA-FFFF in memory. Since the programmer needs to be able to store these vectors in the top six bytes of eprom, address-bus states FFFA-F must be decoded into the eprom area $27 \mathrm{FA}-\mathrm{F}$.

Three-to-eight-line decoder $\mathrm{IC}_{8}$ and gate $\mathrm{IC}_{7 \mathrm{~d}}$ select the 6522 for exactly half of the possible 16 byte blocks between E00x and FFFx. In this way the 6522 is selected for FE60-F but inhibited for FFF0-F when the processor is loading a vector.

Output $\mathrm{Y}_{1}$ of $\mathrm{IC}_{3}$ is low for all addresses between 2000 and 3FFF thus selecting the eprom, $\mathrm{IC}_{4}$. In other words when assembling via the BBC computer, the eprom can be considered as residing in any of four 2Kbyte blocks, $2000-27 \mathrm{FF}, 2800-2 \mathrm{FFF}$, $3000-37 \mathrm{FF}$ or $3800-3 \mathrm{FFF}$, chosen by the user.
Gates $\mathrm{IC}_{9 \mathrm{a}, \mathrm{b}, \mathrm{d}}$ and $\mathrm{IC}_{7 \mathrm{f}}$ cause $\mathrm{IC}_{4}$ to be selected when the most significant byte of the address is F9, FB, FD or FF. Unless there is an error in the software, this can only occur when the processor is trying to load a vector. In this way FFFC, for example, on the address bus will cause the contents of 27 FC to be placed on the data bus. Vectors may therefore be located so
27FD reset high-byte
27FC reset low-byte
27FB NMI high-byte
27FA NMI low-byte
27FF IRQ high-byte
27FE IRQ low byte
Interrupt use on the BBC microcomputer is complex, but it is possible to use the normal facilities of the 6522, interruptig through IRQ, as long as the interrupt vector is loaded into the computer at 0206 (low byte) and 0207.

Mimcry is likely, but not certain, to be exact as the servicing of an interrupt - even from the v.i.a. in an assembly-language program - will bring the microcomputer's operating system into action, which may interface with the user's routine (see Lists). Using NMI (non-maskable interrupt) is substantially more difficult, not least because one does
not have access to the terminal, and is discouraged in the User Guide.
Mimicontoller's board (details later) carries 1 Kbyte of ram ( $\mathrm{IC}_{5,6}$ ) selected by the $\mathrm{Y}_{0}$ output of $\mathrm{IC}_{3}$, giving 256 bytes of page-zero ram. A control program can of course use all of this and the gen-eral-purpose ram area of 512 bytes between 0200 and 03 FF but the BBC microcomputer only guarantees not to tamper with 32 user bytes ( $0070-008 \mathrm{~F}$ ). By the time the control routine is called, the computer will have appropriated some of the stack for its own use. In other words, fewer than 256 bytes will be available in the computer but this is more than enough for most amateur control software.

## Testing

To test the board List 1 , a simple timing/clock program, can be run with a pair of seven-segment displays connected to port B (the user port) through two 74LS47 i.cs and current-limiting resistors. If this program is run on the BBC computer by a call to location 2000 (CALL\&2000) the seven segment display will count seconds (approximately). Load the reset vector into the computer by
$? \& 27 \mathrm{FC}=\& 00$
$? \& 27 \mathrm{FD}=\& 20$
and transfer the code to eprom. The software should give an identical count rate on Mimicontroller.

List 2, a variation of List 1, illustrates interrupt use. The program is put into an endless loop until an interrupt occurs, i.e. when timer one times out after 10 ms . The loop count in the index register ( X ) is decremented in the interrupt routine at location 2100 , and the display on the port changed if necessary. Lines 200 and 210 load the interrupt vector into the IRQ2V locations in the microcomputer and line 220 runs the program. To transfer the program to eprom, first set up the IRQ vector using

$$
\begin{aligned}
& ? \& 27 \mathrm{FF}=\& 21 \\
& ? \& 27 \mathrm{FE}=\& 00
\end{aligned}
$$

Again, identical operation should be seen on Mimicontroller.

When the software is run on the microcomputer, the sound generator comes into action after about 25 s - one illustration of the interaction of interrupts with the system. Secondly, the SED

List 1. Simple clock program counting seconds on two seven-segment displays may be used to test Mimicontroller.

## 10 DDRB=\&FE62:ACR=\&FE6B:PB=FE60 <br> 20 IER=\&FE6E:INFR=\&FE6D <br> $30 \mathrm{~T} 1 \mathrm{CL}=\& \mathrm{FE} 64: \mathrm{T} 1 \mathrm{CH}=\& \mathrm{FE} 65$ <br> 40 FOR PASS $=0$ to 3 STEP 3:P\%=\&2000 <br> 50 [OPT PASS <br> 60 SED:LDA\# \&FF:STA DDRB <br> 70 LDA\# \&00:STA ACR:STA IER <br> 80 .JOE:STA PB <br> 90 LDX\# \& 64 <br> 100 .BILL:LDA\# \& 10:STA T1CL <br> 110 LDA \# \& 27:STA T1CH <br> 120 .SAM:LDA INFR:BEQ SAM <br> 130 DEX:BNE BILL <br> 140 CLC:LDA PB:ADC \# \& 01:JMP JOE

## List 2. This variation of List 1 illustrates interrupt use.

## 10 DDRB=\&FE62:ACR=\&FE6B:PB=FE60 <br> 20 IER=\&FE6E:T1CL=\&FE64:T1CH=FE65 <br> 30 FOR PASS $=0$ TO 3 STEP 3: $P \%=\& 2000$ <br> 40 [OPT PASS <br> 50 LDA\#\&FF:STA DDRB <br> 60 LDA\#\&CO:STA IER <br> 70 LDA \# \&40:STA ACR <br> 80 LDA\# \& 10:STA T1CL <br> 90 LDA\# \&27:STA T1CH <br> 100 CLI:LDX\#\&64 <br> 110 .WATT:JMP WAIT:JMP WAIT <br> $120{ }^{130}{ }^{1} \%=2100$ <br> 140 IOPT PASS <br> 150 LDA T1CL:DEX:BNE SKIP <br> 160 LDA PB:SED:CLC:ADC \#\&01:STA PB <br> 170 LDX\#\&64 <br> 180 SKIP RTI <br> 190 jNEXT <br> 200 ? $\& 206=00$ <br> 210 ?\&207=\&21 <br> 220 CALL\&2000

instruction at line $160\left(2109_{16}\right.$ in assembled code) would be more economically placed in the first block of code at, say, 201C or line 45. In the microcomputer it is rendered ineffective by the operating system, which tampers with the processor status register when the interrupt is serviced.

In summary, use the assembler and board with abandon when no interrupts are involved. The computer will not only assemble the code but also run it so it can be tested. Be aware though of the extent to which the computer uses interrupt and do not be surprised if it will not run code which involves interrupts. Software using interrupts can of course be assembled and transferred to eprom for testing directly on Mimicontroller.

The controller has been made on a p.c.b. measuring 160 by 100 mm . Careful design allows a single-sided board with a manageable number of links. Current drawn by the circuit is about 350 mA so it can be driven by NiCd batteries and used on a 'buggy' for example. Use of $65 \mathrm{C} 02,65 \mathrm{Cl} 6$ and 2114 L devices reduces power consumption by a factor of four, but increases cost.

## Continued from Page 20

rently programs logical colour two to actual colour eight (flashing black/white). Removing it will leave logical colour two at its default actual colour, yellow, in any case leave the second half of this instruction intact. This sets logical colour one to black. Curious effects will occur without it.

Line 2690 checks input for a synchronization error which, if found, causes printing of a message and stops the program. One way of dealing with this is to add these lines.

## 245 CLS

$90^{*} \mathrm{KEY1}$ ? \&FE6C= \&D01M
?\&FE6C $=\& \mathrm{~F} 01 \mathrm{MGOTO} 2451 \mathrm{M}$
then if the message 'SYNC ERROR' appears, press function key one. This sends a pulse through $\mathrm{CB}_{2}$ to step $\mathrm{IC}_{10}$ so press the key as many times as is necessary to regain synchronization. Alternatively, a hardware reset can be instituted by including a push switch to connect pin two of $\mathrm{IC}_{10}$ to $\mathrm{V}_{\mathrm{cc}}$ through a $1 \mathrm{k} \Omega$ resistor.

Software synchronization could be made completely automatic by a subroutine that sent a pulse through $\mathrm{CB}_{2}$ whenever the first byte read was not FF. Some synchronization problems were experienced initially with the prototype due to switching transients. This was cured by inclu-
sion of a low-pass filter consisting of a $1 \mathrm{k} \Omega$ resistor and 68 pF capacitor in the output of one of the multiplexers.

Once the program has been run, the assembler section can be removed if the assembled version is saved as machine code using *SAVE. After running the program press the escape key then type *SAVE <filename> 5500 +300 . Lines $270,360-380$, $420-$ 440 and $510-2660$ can then be deleted from the program and the shorter version saved. Line 390 must be then changed to NEXT:NEXT:NEXT and the instruction 165 *LOAD <filename $>$ added. This speeds up
initialization of the program considerably since it no longer has to calculate frequencies of the equal-tempered scale.

No attempt has been made to make the program 'tube' compatible, the main reason being that the software is still of an experimental nature.

There would be no difficulty in making the program compatible with OSBYTE calls, though these appear to operate some 20 times slower than direct addressing of the devices. Whether this would be of any significance depends on the application.


## Twin-T filters

These band-pass and all-pass filters use twin-T networks.
Transfer function of the band-
pass circuit is
$\frac{V_{\text {sut }}}{V_{\text {in }}}=\frac{\left(1+4 s C R+s^{2} C^{2} R^{2}\right)}{\left(1+4 s k C R+s^{2} C^{2} R^{2}\right)}$
where $k=R_{1} /\left(R_{1}+R_{2}\right)$.

Attentuation of the filter depends on the choice of $k$. For example, at the resonant frequency $\omega_{f}$, attenuation is 26.02 dB for $\mathrm{k}=1 / 10,32.04 \mathrm{~dB}$

## Voltage controlled duty cycle

Forming part of an automatic voltage regulator for an alternator, this simple circuit gives a rectangular output waveform with a duty cycle between 0 and $100 \%$ depending on the control voltage. Frequency is determined by $\mathrm{R}_{1}$ and $C_{1}$ and is independent of the mark/space ratio.

The 555 is configured as an astable circuit, $\mathrm{Tr}_{1}$ providing a constant current charge for $\mathrm{C}_{1}$. This results in a linear ramp at the input of the voltage comparator which varies
between about one and twothirds of the supply voltage. Varying the control at the comparator's non-inverting input causes the output duty
cycle to vary between 0 and $100 \%$.
E.H. Rice

Exeter
Devon

for $\mathrm{k}=1 / 20,40 \mathrm{~dB}$ for $\mathrm{k}=1 / 50$ and 46 dB for $\mathrm{k}=1 / 100$. It is pointless to choose a value for $k$ smaller than about $1 / 50$.

Transfer function of the allpass filter is
$\mathrm{V}_{\text {out }}$ $\frac{\left(1-4 s C R+s^{2} C^{2} R^{2}\right)}{\left(1+s C R+s^{2} C^{2} R^{2}\right)}$

At the resonant frequency $\omega_{0}$, output voltage of the twin-T network is zero and output of the op-amp is $-\mathrm{V}_{\text {in }}$. The twin- T network with fixed capacitance values is suitable for direct implementation as an i.c. with many useful applications.
Kamil Kraus
Rokycany
Czechoslovakia

Further equations relating to this idea can be obtained by sending an s.a.e. marked TwinT filters to E\&WW's Editorial offices.


CIRCLE 26 FOR FURTHER DETAILS.

## What the competition hasn't been waiting for. <br> Latest version of Forth for the BBC <br> 16k Eprom type 27128

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extensive Manual ( 170 pages plus) and at $£ 45+$ VAT it is superb value
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## MULTI-FORTH 83 FOR THE BBC MICRO



CIRCLE 48 FOR FURTHER DETAILS



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# HDTV or EDTV? 

## Extended definition television - building up picture resolution gradually, flexibly and compatibly - is being considered as an alternative to all-out high-definition television on completely new standards. This report outlines some of the current investigations into ways of achieving higher definition pictures, as described in papers at IBC 84, the 10th international Broadcasting Convention at Brighton.

As long as the broadcasters continue to produce a succession of technical innovations, like rabbits out of a hat, to add to their systems, they will be relatively safe from the rival methods of distributing television programmes. Transmission standards, colour, additional channels, teletext, stereo sound, have all in their turn provided new opportunities for selling more consumer equipment and advertising time.

To judge from papers and demonstrations presented at the recent International Broadcasting Convention, the transmission standards, and other related standards, are now coming round for another rejuvenating pick-me-up. "High definition" is on everyone's lips again - as it was once before, in 1936, when used to distinguish the UK's then new 405 -line tv service from earlier $30-$, 150 - and 240 -line experimental systems.

The previous WWreport on the biennial IBC (December 1982 issue) focussed on the great significance to tv broadcasters of the then newly established CCIR digital coding standard for studio equipment. Many realised that this internationally agreed standard, based on luminance and chrominance components rather than on the composite colour signal, would open up all kinds of extra possibilities for improving picture quanlity.

Using the latest digital processing and storage technology, video information could be 'preprocessed' at the studio and 'post-processed' at the receiver without any constraint from the nature of the composite transmis-
sion standard. Picture-source and receiver-display standards need not be the same as the standard used for transmission over the air. Some of the proposals for 'enhancing' picture quality in this way were outlined.

Since 1982 several things have happened which have brought these proposed enhancement much closer to reality. In 1983, for example, a number of European broadcasters showed their involvement by putting on demonstrations of HDTV at the Montreux television symposium. Then in January 1984 the Japanese launched an operational direct broadcasting satellite (DBS) which was intended eventually to carry experimental transmission derived from NHK's provisional 1125-line HDTV standard. (In the meantime this satellite has had transponder faults - see September issue, p.14.)

And during this two-year period the European Broadcasting Union (EBU) has officially recommended a DBS transmission standard. Based on a proposal from the IBA and called C-MAC/packets, it is expressly designed to allow future compatible higher definition transmission.

All the work now being done by broadcasters and manufacturers seems to be based on certain assumptions. The first is that higher definition services will be provided through 12 GHz DBS transmissions in 27 MHz or 24 MHz f.m. channels. Another is that sufficient money will be available to meet the considerable costs.

It is also assumed there will be available large-screen displays of at least 1 metre diagonal, with a wider aspect ratio than that of existing tv receiver screens probably about $5: 3$. This wider display, according to K.H. Powers of RCA Laboratories, has proved to be more pleasing to viewers than $4: 3$, increasing their feeling of involvement with the programme material.
Beyond this common ground there are differences of engineering opinion on how higher definition services should be introduced. As pointed out by K.H. Powers, there are several options available for utilising the extra bandwidth offered by DBS channels - increasing the horizontal resolution and/or the vertical resolution and/or the aspect ratio, and the possibility of using time multiplexed analogue components to avoid the cross-colour, cross-luminance and noise impairments experienced with composite signals.
But the main difference of approach seems to be: 'straight' HDTV versus 'extended definition' television (EDTV). Is it bet-

| Lines per picture | Fields per second | Interlace factor | Line scan frequency kHz | Improvements or cures |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  |  | Large area flicker | Interline twitter | $\begin{aligned} & \text { Line } \\ & \text { crowl } \end{aligned}$ | Static raster |
| 625 | 50 | 2:1 | 15.625 | NO | NO | NO | NO |
| 625 | 50 | $1: 1$ | 31.25 | NO | YES** | YES | NO ${ }^{+}$ |
| 1250 | 50 | 21 | 31.25 | NO | YES* | YES | YES |
| 625 | 100 | $2+2.1$ | 31. 25 | YES | NO | NO | NO |
| 625 | 100 | $2 \cdot 1$ | 31:25 | YES | YES * | YES | NO |
| 1250 | 100 | $4: 1$ | 31.25 | YES | YES* | NO | NO |
| 625 | 100 | 11 | 62.5 | YES | YES | YES | NO ${ }^{+}$ |
| 1250 | 100 | 2.1 | 62.5 | YES | YES | YES | YES |

[^5]

Fig.1. IBA's compatible approach to higher definition television using extended
MAC in a C-MAC/packets
DBS channel. Other
researchers think there is further benefit to be obtained by starting with a high linenumber HDTV picture source and down-converting to 625
lines for transmission.
ter to establish a new high-resolution system in one go, or gradually to extend the resolution capabilities of existing 625 -line and 525 -line systems while preserving compatibility?

As an example of the 'straight' HDTV approach, E. Kimura and Y. Ninomiya of NHK Research Laboratories, Japan, described the NHK system intended for HDTV transmission through the Japanese DBS satellite. It has 1125 scanning lines, $2: 1$ interlaced, a field frequency of 60 Hz and a $5: 3$ aspect ratio.

In each line, the relevant luminance and chrominance signals are sent as time multiplexed components, with $\mathrm{R}-\mathrm{Y}$ and $\mathrm{B}-\mathrm{Y}$ chrominance information on sequential lines. This means that these two components have to be time compressed so that they can be contained within the line period. With the initial video bandwidths (e.g. 20 MHz luminance) this results in a baseband width too large for the normal DBS channel. So the signals are bandwidth-reduced by subNyquist sampling over a 4 -field sequence and thus transmitted in analogue form. Two companded digital audio signals are multiplexed in the field blanking
period. The baseband for transmission via the satellite is then 8 MHz wide.

In the receiver a 4 -field memory is necessary to store the subNyquist samples distributed over the 4 fields at the transmitting end. Pictures are reconstructed by interpolation from these to give a bandwidth of 25 MHz for stationary parts of the scene and 15 MHz for moving parts. Because the basic process introduces unacceptable blurring on slow, uniform movements in the scene, a motion compensation technique is added. This uses inter-frame comparisons at the transmitting end to produce a signal which is used at the receiver to shift the read-out address of the 4-field memory accordingly.

The NHK transmission standard is broadly comparable with the EBU-recommended DBS standard in that they both use time-multiplexed analogue components, time compression of these components and digital audio. But, as is well known, C-MAC/packets fits into the normal time frame of the existing 625 -line standard for compatibility purposes, and video information is time multiplexed with 751bit packets of digital data which can be used as required for digital audio channels or other data. The ' C ' in the name is simply a code letter for the particular method chosen for multiplexing the analogue video components and the digital audio and data.

But in another paper, M.D. Windram and R. Morcom of the IBA showed how C-MAC can become E-MAC - extended MAC - to carry higher definition pictures on the gradualist EDTV principle discussed above. This provides a wide aspect ratio, extended resolution, and MAC's
complete separation between luminance and chrominance components that overcomes the cross-chrominance, crossluminance and noise problems of composite transmission signals.

To provide the $25 \%$ extra picture information need for a $5: 3$ aspect ratio over a $4: 3$ ratio, some additional line time is obtained from the field blanking interval and some from the data period, still leaving enough audio data for two sound channels. But extra time compression is needed at the edges of the picture to achieve the full $5: 3$ ratio.

To increase the vertical resolution, the display at the receiver is up-converted to sequential 625line scanning (non-interlaced), thus providing an apparent $40 \%$ increase in this resolution. But moving parts of the picture need a motion-adaptive conversion technique to avoid blurring (described by T.J. Long and G.J. Tonge in another paper). Horizontal resolution is increased by using a greater than normal baseband bandwidth of about 11 MHz , which allows an uncompressed luminance bandwidth of over 7 MHz .

Figure. 1 shows how E-MAC would be used in compatible approach to high definition. The viewer can use either a conventional C-MAC/packets decoder and ordinary televison set or an E-MAC decoder with scan upconversion and a large, wideaspect display screen. The conventions C-MAC/packets receiving equipment simply ignores the extra information provided by the E-MAC system and decodes the signal as a normal 4:3 picture.

Philips Research Laboratories have also been considering various MAC type methods of coding for EDTV based on the 625-line time frame. These all assume a horizontal band width of 8.4 MHz . In one method, as described by S.L. Tan and R.W. Jackson, the luminance and chrominance information is interleaved vertically rather than horizontally. Chrominance signals are line sequential and time compressed by $4: 1$. This had the advantage of allowing the full 8.4 MHz channel bandwidth to be used for the luminance signal, now uncompressed.

In another method the $B-Y$ chrominance signal is inserted in the field blanking interval and the $\mathrm{K}-\mathrm{Y}$ signal is time multiplexed horizontally with the luminance information. Here, one advantage is that the $\mathrm{R}-\mathrm{Y}$ information
is no longer line-sequential, thus improving the vertical chrominance resolution.

The Philips researchers claim that, in conjunction with oversampling and non-interlaced display scanning, such coding systems allow transmission of signals with up to 390,000 picture elements in an 8.4 MHz channel. This compares with conventional PAL resolution of 147,000 pixels. A further system being investigated, using two DBS channels, offers an overall resolution of 780,000 pixels.
As part of an international research effort on world-wide standards for HDTV picture sources, the BBC has been studying the large-screen display at the receiver - a logical starting point for investigating HDTV signal parameters. I. Childs and A. Roberts of the BBC Research Department described how they have used a conventional picture tube to simulate one quarter of a large-screen display.

To test the belief that 625 -line pictures can be significantly improved by display processing alone, regardless of the transmission standard, they tried a number of scan up-conversions with different line numbers, scanning frequencies and interlacing factors (including $1: 1$, or sequential scan). The results obtained are shown in the table.

The authors conclude that display up-conversion can substantially remove the major limitations of 625 -line displays (righthand column headings in table). But there is a problem with scene movement when extra lines are generated. To get optimum results a suitable algorithm is needed to calculate the right information to go in these lines, and probably an adaptive technique will be needed.

Similar work on up-converted displays was described by D.W. Parker and L.J. van de Polder of Philips Research Laboratories. Their systems include: 625-line sequential displays at 50 Hz field frequency using one-line, onefield and and two-field stores for interpolating the extra information required; 1249-line displays with $2: 1$ interlace; and up-conversions with 100 Hz field frequency to overcome large-area flicker.

These researchers say that, if a free choice of display standard were available, they would plump for a system with about 800 lines sequentially scanned at a field rate of about 60 Hz .


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# Hand-held multimeters 

We found many more hand-held digital multimeters than we initially thought were on the market. The large number found over 160 - is partly due to the widespread practice of 'ownlabel' branding of products made in the Far East, chiefly in Korea and Japan but also in Taiwan, which results in many instruments having identical or very similar functions, ranges, errors and construction. We summarize their data in this month's table.

As with last month's tables for bench and systems multimeters, we have singled out the basic direct voltage range as being the most telling in terms of sensitivity and error; the function converters invariably degrade error specification. October's issue gave the errors that would arise in using non-sinusoidal waveforms on alternating ranges without the use of a 'true r.m.s.' converter, although if the crest factor of the waveshape being measured is known corrections can often be applied. (A rectangular wave with a pulse duty cycle of $40 \%$ can be measured without correction, provided adequate bandwidth is available, as the table on page 74 of that issue hints.) Those meters that measure the mean value but display a scaled r.m.s. value for sine waves will give the reading in column two of October's table for a one-volt peak input. So column one is what is sensed, col. two what is indicated, and col. three the heating equivalent value, with the error in col. six. Crest factor, usually taken as a measure of deviation from a sine shape, is peak value divided by r.m.s. value (form factor, less often used, is r.m.s. value divided by mean value). In the table the second row refers to both triangle and sawtooth waveforms, and the fourth row to both rectified and $90^{\circ}$-chopped sine waves

As well as Intel devices comprising analogue-to-digital con-

# Third and final article in our digital multimeter series brings together price and performance data for hand-held d.m.ms on the UK market. 

verter and display drive on one chip listed on page 76 of October's article, a number of others are available, some as original approaches to the application, some as exclusive customdesigned chips jealously guarded, and some simply on a second-source basis.

National Semiconductor's four c-mos circuits are of the pulse modulation kind requiring no external reference components, though a voltage reference is needed. Linearity is within $\pm 0.05 \%$ of full scale, typically $0.025 \%$. Two are microprocessor compatible, with bcd outputs for $3_{2}^{\frac{1}{2}}$ or $3_{\frac{3}{3}}^{3}$-digit operation, while two others offer $3_{2}^{\frac{1}{2}}$ or $3_{4}^{3}$-dagit multiplexed seven-segment outputs with a reading speed of either 5 or 2.5 per second, respectively.

Of Ferranti's interestingly wide range of converters that includes ramp, successive approximation, tracking, flash, charge balancing and dual-slope types, the $3_{2}^{1}$-digit dual-slope chips are most applicable to voltmeter design. ZNA116 and 216 both drive multiplexed sevensegment l.e.d. displays, consume about 50 mW but require additional analogue circuitry for the integrator and comparator sections.

The latest device ZN 451 is the most useful. Not only is it a single chip circuit requiring only passive external components (for a basic voltmeter), but because it uses a digital auto-zero mode it obviates the need for the capacitor to store error voltage. And additional signal, conditioning circuitry andded to the main circuit can be included within the auto-zero loop to guarantee zero reading for zero input. A voltmeter evaluation kit which includes p.c.b., display and conditioning i.cs, is available ( $£ 29.50$ ), though the chip is equally suitable for applications such as thermometry and transducer bridge interfacing.


Multimeters built into test probes are no longer new but perhaps a pen-style one that clips into the pocket will be. Middle photo: Japan's Soar 3200 (from HI); lower photo: another Japan-made probe sold under various names (Anders, Pantec, Beckman, Häwa, Hioki, Taylor); upper photo: not yet available.


Bench digital multimeters in addition to page $61 / 2$ table, November issue.

| Maker | Digits | Model | Basic price | Sens. <br> d.v. | $\begin{array}{ll} \hline \text { Basic } \\ \text { error(1) } & f_{\text {max }} \\ \text { a.v.(2) } \end{array}$ | Crest factor | Res. <br> (3) | $\underset{(4)}{\mu \mathrm{P}}$ | Other <br> (5) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| $\begin{aligned} & \text { AOIP } \\ & \text { (Danesbury) } \end{aligned}$ | $4{ }^{\frac{1}{2}}$ | $\begin{aligned} & 5120 \\ & 5121 \\ & 5122 \end{aligned}$ | $\begin{aligned} & 338 \\ & 474 \end{aligned}$ | $\begin{aligned} & 100 \mu \mathrm{~V} \\ & 10 \\ & 1 \end{aligned}$ | $\begin{aligned} & 0.05 \%+120 \mathrm{kHz} \\ & 0.03 \%+1400 \\ & 0.03 \%+1400 \end{aligned}$ | $\begin{aligned} & 4 \\ & 4 \end{aligned}$ | $\begin{aligned} & v \\ & 4 \end{aligned}$ |  | diode test G£261 dB, G£255 |
| Delnstor | $4 \frac{1}{2}$ | Exis 200 | 850 | $10 \mu \mathrm{~V}$ | $0.03 \%+120 \mathrm{kHz}$ | 3 | $\checkmark$ |  | Safety cert. |
| Hioki <br> (Doran Smith) | $\begin{aligned} & 3 \frac{1}{2} \\ & 4 \frac{1}{2} \end{aligned}$ | $\begin{aligned} & 3209 \\ & 3222 \end{aligned}$ | $\begin{aligned} & 342 \\ & 874 \end{aligned}$ | $\begin{aligned} & 100 \mu \mathrm{~V} \\ & 10 \end{aligned}$ | $\begin{aligned} & .2+1 \%+120 \mathrm{kHz} \\ & .04 \%+2200 \mathrm{k} \end{aligned}$ | $\bar{?}$ | $\checkmark$ | 2,4,7,12 | C, battery f, G. |
|  <br> Schwarz | $4{ }_{2}^{1}$ | UDL4 | 365 | $10 \mu \mathrm{~V}$ | $0.03 \%+220 \mathrm{kHz}$ | 5 | $\checkmark$ |  | charges |

[^6]

## Hand-held digital multimeters



Notes. 1: B button, $R$ rotary, $S$ slide. 2 Basic (d.v.) error is $\pm$ (reading $\%+1$ digit), or $\pm$ (rdg + f.s.) $\%$, unless shown otherwise. $3 f_{\text {max }}$ (a.v.) limits may differ. 4: Minimum range shown for d.v. upper range typically $100 \mathrm{~d} . \mathrm{v}$. and 750 a.v. Ranges for I, R are decades except for top I, normally limited to 10A. 5: Normally scaled to read forward voltage. R means maker suggests $2 \mathrm{k} \Omega$ range. 6: Audible signal for continuity, maximum resistance value given where known. 7: Crest factor (cf) given for true rms meters. G conductance range, C capacitance, T temperature. hi/lo R switch gives $>0.7 \mathrm{~V}$ or $<0.3 \mathrm{~V}$ for in-circuit resistance measurement. For cmos devices battery life is quoted as $500-2000 \mathrm{~h}$


For under $£ 30$ this evaluation kit provides a dvm with f.s. range of 2 mV .

Correction. Because of a last-minute breakdown in facsimile reception of proofs certain pages of the November issue went unchecked. Readers will have spotted the most embarrassing errors (e.g. zero 'ironings' instead of crossings in the caption to Datron's Autocal multimeter on page 65) but those in the tables are less obvious. Please correct Brown Boveri's 2110 basic error to $0.05 \%+1$, and Farnell's 141 error to $0.03 \%+2$. A zero slipped in the R \& S UDS5 error but its best figure over 12 months is $0.009 \%+1$, and its rated a.v. max is 200 kHz with a crest factor of 5.5 for true r.m.s. readings.
$4_{\frac{1}{2}}^{\frac{1}{2}}$ digit meters are usually set to display their maximum value for photography but this Fluke display could be misleading in suggesting full fivedigit capability.


## by Richard Lambley

## Multi-standard modem <br> Connections and modifications

Fig.1. Using the RS423 port of a BBC Micro. At the modem. it is cheaper to fit a six-pin DIN socket rather than the p.c.-mounting $D$ -
connector: connections
shown are those found on certain commercial modems.

Several readers have asked for fuller information about connecting the modem to the BBC Micro, which lacks a full RS232 interface. Figure 1 shows pin details of the computer's RS423 connector and the corresponding terminals on the modem. The polarizing

notch in the five-pin domino connector goes in on the side nearer the cassette port.

Notice that the RTS line from the computer is not used: this is to avoid the risk of line drop when the computer turns its attention to other matters, as it does during disc or tape filing operations. RTS is best tied high at the modem either by linking RTS to DSR, or (if IC7 has been omitted, as it may be when a full. RS232 interface is not required) by linking pins 13 and 14 on $\mathrm{IC}_{5}$.
Figure 2 shows a modification to take care of the opposite problem, a possible failure by the modem to disconnect the line when the user logs-off a viewdata system. This may occur if a noise on the line is interpreted by the modem as a burst of carrier.
The two inverters form a monostable which triggers when the line-seize relay drops out. Until


Fig. 2 . An additional monostable ensures that the line is dropped cleanly after a Viewdata call.


Fig.3. An extra mode for file exchange at 1200 baud.
the monostable times out (its period is a second or two) RL cannot close again. The purpose of the diode pump circuit is to trigger the monostable if a succession of rapid transitions occurs on $\mathrm{IC}_{8}$ 's carrier-detect output: this will make sure that the modem does not try to demodulate dialling tone.

Several spare inverters are to be found on the p.c.b. though

BBC Micro users who have added the interface for the autodialler may find it less untidy to use spare inverters in that.

However, there are times when you may want to hold on to the line regardless; for example, when exchanging files at 1200 baud with someone else. Reversing the direction of transmission can be made less hazardous by means of two diodes and the use of a spare contact on $S_{1}$. This provides an extra 1200/75 mode in which the modem will continue to hold the line even when there is no carrier (Fig.3).

To insure against possible instability in the auto-answer circuit, it is worth adding a high value resistor between pin 7 of $\mathrm{IC}_{11}$ and the 0 V rail. The value is not critical; anything between 1 and $10 \mathrm{M} \Omega$ seems to work successfully.

A further small addition to the board may be needed if you have been supplied with a 40147 for $\mathrm{IC}_{1}$. This device is not a direct replacement for the 74 HC 147 since it has a connection to pin 15. If you fit a 40147, be sure to tie pin 15 to the 0 V rail. Failure to do this may produce puzzling effects. One such modem I have seen appeared to be working all right except in the Bell 103 originate mode (for which all inputs to $\mathrm{IC}_{1}$ are pulled high): it turned out then that $\mathrm{IC}_{1}$ was driving the mode-select inputs of the Am7910 with 2 MHz square waves.

## Auto-dialler

An enhanced version of the autodialler (October issue) can be downloaded free of charge from the Microweb bulletin board in Stockport on 061-456 4157 (300 baud CCITT). The file is stored as Asciitext: when you have saved it you should reload it using *EXEC rather than CHAIN. The program includes one or two improvements, among them a facility for inserting pauses between digits to give time for a further dialling tone to appear.

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| /4) |
| :---: |
| $2 \mathrm{U}(31 / 2)$ height, 308 m depth |
| $3 \mathrm{U}\left(5^{1 / 4}\right)$ height, 230 m depth. |

## Width Behind Front Pane 437 m (All Tvpes <br> Alt

J. R She
J. D. R. Sheetmetal, 131 Grenfell Road, Maidenhead, Berks. SL6 1EX. Maidenhead 29450.

CIRCLE 41 FOR FURTHER DE'TAIIS.
ELECTRONICS \& WIRELESS WORLD DECEMBER 1984

# Intelligent eprom programmer 



> Having its own processor, this intelligent programmer can quickly copy eproms up to the 27512 and list eprom data on a serial printer - under computer control it programs one-chip microcomputers. Part two discusses computer control software and construction.

My control program for $\mathrm{SC} 84^{*}$ is written in assembly language but a program written in a high-level language could be used at the expense of speed. The SC84 program uses CP/M-compatible system calls so it should be possible to adapt it for other CP/M computers. The only 'non-standard' requirement is that the program needs to know the serial-port status, i.e., whether it is ready to be sent a character or whether there is a character in its receive buffer. Software for the eprom programmer is in an eprom provided with the kit. The SC84 control program is available separately on disc (details later).

Computer software for remotely controlling the eprom programmer needs to display eprom types and programming commands available, and to input and validate the user's selection If the operation involves a disc file, the program has to receive and validate the file identification. It must also react if a file for passing data to the programmer doesn't exist, or a file for receiving data from the programmer already exists.

When the eprom type has been established, the program must draw the length of the eprom from a table for use during file transfer. Communication between the computer and programmer is outlined in the flow diagram. The A6 ${ }_{16}$ byte sent before each operation is an arbitrary code signalling the start of the operation. It is used so that the programmer is not put off by any odd character
*SC84 was described in the May, June July, September and October issues of E\&WW
that the computer might have waiting in its transmit buffer when the two are connected.

At the end of the transfer, the program must examine eight bytes returned by the programmer. These bytes consist of a 24bit check sum of data read from the master socket, a 24 -bit check

source of information after a 'sum-check only' command is ended.

These are minimum require-
ment for the program. Other almost essential elements are some form of progress indicator and a routine that recognizes files

Fig.1. Displays presented by John Adams' SC84 software for controlling the eprom programmer. The first of these, (a), shows a sum check of two erased 2716 eproms. An attempt to read data from a 2732 in the slave socket into a disc file in which the file EPROM.COM already exists is shown at (b).

Screen (c) is a simulation of a bad-programming error produced by attempting to copy a 2732 to an empty slave socket and (d) shows a successful attempt reading an eprom's content into a disc file.

## SC 84 Eprom Programmer Control Program Version 1.00



Enter your EPROM choice, A to L, now...A
A - ABORT session
B - SUMCHECK only
D Read SLAVE to COMPUTER
F - PROGRAM SLLVE
from COMPUTER
Enter your COMMAND choice, $A$ to $F$, now... $B$
SUMCHECK of MASTER EPROM is 07F800
SUMCHECK of SLAVE EPROM is 07F800

| $\mathrm{A}-2716$ | $\mathrm{~B}-2732$ | $\mathrm{C}-2732 \mathrm{~A}$ | $\mathrm{D}-2764$ |
| :--- | :--- | :--- | :--- |
| $\mathrm{E}=2764 \mathrm{~A}$ | $\mathrm{~F}=27128$ | $\mathrm{G}=27128 \mathrm{~A}$ | $\mathrm{H}=27256$ |
| $\mathrm{I}-27512$ | $\mathrm{~J}-27513$ | $\mathrm{~K}-8741 / 8$ | $\mathrm{~L}-8749$ |

Enter your EPROM choice, A to L, now...B

A - ABORT session
C - Read MASTER to COMPUTER
E - COPY MASTER TO SLAVE

B - SUMCHECK only
D - Read SLAVE to COMPUTER
F - PROGRAM SLAVE
Enter your COMMAND choice, A to F, now...D
ENTER the name of the DATA FILE ... EPROM.COM
This FILE does ALREADY EXISTS. Do you want toA - INPUT a NEW FILE NAME, or,
B - OVER-WRITE the FILE
Enter your choice, A or B, now...

| A -2716 | B -2732 | C $-2732 A$ | D -2764 |
| :--- | :--- | :--- | :--- |
| E 2764 A | F $=27128$ | G -27128 A | H -27256 |
| $\mathrm{I}-27512$ | $\mathrm{~J}-27513$ | K $-8741 / 8$ | $\mathrm{~L}-8749$ |

Enter your EPROM choice, $A$ to $L$, now... $B$

## A - ABORT session <br> C - Read MASTER to COMPUTER <br> E - COPY MASTER TO SLAVE

B - SUMCHECK only
D - Read SLAVE to COMPUTER
F - PROGRAM SLIVE from COMPUTER

Enter your COMMAND choice, A to $F$, now...E
SUMCHECK of MASTER EPROM is 0000 E 9
SUMCHECK of SLAVE EPROM is 0000 FF
ERROR Pattern is 16
PROGRAMMING ADDRESS is 0000

| $A-2716$ | $B-2732$ | $C-2732 A$ | D -2764 |
| :--- | :--- | :--- | :--- |
| $E=2764 A$ | $F=27128$ | $G-27128 A$ | H -27256 |
| $I-27512$ | $J-27513$ | K $-8741 / 8$ | L -8749 |

Enter your EPROM choice, $A$ to $L$, now... $B$

A - ABORT session
A - ABORT SeSsion
C - Read MASTR To COMPUTER
E - COPY MASTER TO SLAVE

B - SUMCHECK only
D - Read SLAVE to COMPUTER
F - PROGRAM SLAVE
from COMPUTER

Enter your COMMAND choice, A to F, now...C
ENTER the name of the DATA FILE... B: F8086. OBJ
SUMCHECK of MASTER EPROM is 078200
SUMCHECK of DISC FILE is 078200
containing Intel hexadecimal code (a standard form for assembler or linker output files) and translates it into hexadicimal form before it is sent to the programmer. My program for the SC84 includes these features and more.

Typical screen displays are shown in Fig. 1(a-d) You are first prompted for the eprom type and then for the operation required. In each case, only a single letter is entered. If commands A or B are chosen, they execute immediately. Any other command results in a prompt for a file name to be used for the destination or source of data. Invalid file names of more than eight characters or invalid file types of more than three characters are rejected.

The file is searched for on the disc and extra prompting occurs, Fig. 1(b), if the destination file already exists (commands C to E) or cannot be found (command F). Once a correct file specification has been entered and initiated, the message 'programming now' appears on the screen and progressively tuns into reverse video as programming proceeds. Previously entered information about the eprom type is used by the software to work out how to scale the progress of this operation so that there are always the same number of changes. The rate of change varies faithfully in terms of bytes programmed as the programming algorithms skip or shorten programming time.

Programming ends when all locations are programmed or an error occurs. After successful programming, check sums of the eprom and disc file are displayed. One of these is derived by the computer, the other by the programmer, so they provide additional cross-checking of the programming. If an error occurs, both check sums are displayed together with an error pattern consisting of a hexadecimal byte in which logic ones represent faulty bit positions in the eprom location, and the address location of the error. Status leds on the programmer are set to reflect the result of the programming.

An extra option is available when using command $F$. If the file specification given as the data source is a .HEX type, the file is assumed to contain code in Intel hex. form and is compiled accordingly, with the starting address assumed to be the first location in the eprom. Data in the file is all ascii which makes the transfer of
data between computers much easier. Actual data in an Intel hex. file consists of lines or 'records' of ascii characters each consisting of a colon, the count of the number of data bytes in the line, a null byte, the data bytes, a check sum of the characters in the line followed by carriagereturn and line-feed characters. The last record in the file has a zero data count and holds the program start address as its starting address.

## Construction

These notes assume that you are using the kit and p.c.bs mentioned at the end of this article. The main board has components mounted on both sides. All of the components on side two of the board, i.e. all but the leds, push buttons and zero-insertion-force (zif) sockets, should be mounted and soldered first. Then mount the zif sockets by setting the lever to its lock position and working the socket carefully into the relatively tight-fitting holes on the p.c.b.

Push buttons and leds should be mounted next, but not yet soldered. Make sure that the leds are the right way round, with their anodes connected to the adjacent resistor. Fit the p.c.b. to the top panel of the case using the M3 nuts, screws and spacers. With the board centralized against the top panel and the nuts tightened, adjust the switches so that they are square within their cut-outs before soldering them in. Next, align the leds so that they stand vetically and square in their panel holes and solder them. The 6 MHz crystal stand upright and can be swathed in silicone compound to strengthen its mounting if required.
The cable linking the main board and power-supply p.c.b. should be between 16 and 17 cm of the ribbon cable stripped of the black wire. The 9 -way plug should be fitted to the main p.c.b. and a 9 -way connector fitted at one end of the cable with the gap in the connector between the violet and grey wires. Solder the cable at the power-supply p.c.b. with the wires in the same order as at the connector. To link the transformer and power-supply board, a short length of ribbon cable is soldered to the transformer at one end and fitted to a fiveway connector with pins two and four removed at the other.

A 20 cm length of 5 -way ribbon
cable with a 5 -way connector at one end and soldered to a 25 -way female D-type connector at the other forms the serial data connection, Fig 2. The D-type connector conforms to the standard 'output-to-peripheral' configuration for an RS232 port and can be connected to a printer using a direct pin-for-pin printer cable. For communications with a computer, a special cable will be needed to suit the computer's socket. For SC84, the serial port of which is wired exactly as the programmer's, this consists of a cable terminated with male connectors and with its data and hand-shake lines crossed over as shown in Fig. 3.
A 2732 eprom is supplied with the programmer control program; for this device LK1 (the long one) should be made. The other link is allows a 2716 to be used.
Both the 8035 and 8243 are available in cmos as the 80 C 35 and 82 C 43 and a version of the programmer has been constructed using them. There are no problems in doing so as the cmos versions have the same current sinking capabilities and interface levels as equivalent nmos parts. Using cmos devices saves power, but the three cmos i.cs cost two to three times more than three equivalent nmos devices - which together can cost less than just one of the 28pin zero-insertion-force sockets.

## Setting up

After carefully checking the construction, connect just the transformer to the power supply and test this part of the circuit, Fig. 4, before connecting the main
board. Your voltmeter must be accurate to 250 mV and cover the range 0 to 30 V . Check that the 5 V line is correct and that the +12 and -12 V rails are within 2 V of their nominal values. Check that $\mathrm{V}_{\mathrm{pp}}$ is zero then temporarily link PSU ON to +5 V and check that $\mathrm{V}_{\mathrm{pp}}$ rises to somewhere between +12 and 30 V and falls back to zero when the link is removed.
When the supply is operating as described switch off, connect the programmer board and switch back on again. The two leds indicating 2716 and the OK led should light. If not, check the supply rails, wiring of the leds (especially if one or two of those expected to light do light) and general construction.
Next press the UP button to test all of the eprom indicator leds. With the indicator again set to 2716, press and release the PROGRAM button. A relay should operate and the OK led should go out and the PROGRAM led light for around 1 to 2 s . If this happens press and release the UP button, checking that the indicator reads 2732. Press the PROGRAM button. One relay should release and another activate followed by about 4 s of attempted programming.
To set the progamming $\mathrm{V}_{\mathrm{cc}}$ supply, switch the programmer off then on again. Insert a $120 \Omega$ resistor in pins 14 and 28 of the slave socket, lock the socket, and measure the voltage on pen 28. It should be between 4.75 V and 5.25 V . Short of the base of $\mathrm{Tr}_{1}$ to its emitter and adjust $\mathrm{VR}_{1}$ on the main board until the reading is 6 V . Remove the short and the resistor.

To set the programming voltages, connect a $1 W, 680 \Omega$ resis-

tor between $V_{p p}$ and 0 V (e.g. in parallel with $\mathrm{C}_{7}$ ) and connect the voltmeter across it. Switch the programmer on, press the LIST button, and adjust $\mathrm{R}_{108}$ on the power supply board to set $V_{p p}$ to 25 V .
Switch the programmer off, pause, then switch it back on again. Select 2732A programming using the UP button then press LIST. Adjust $\mathrm{R}_{109}$ on the supply board to set $V_{p p}$ to 21 V . Switch off, pause then switch on again, select 27256 press list, and adjust $\mathrm{R}_{110}$ on the power supply to set $V_{p p}$ to +12.5 V . Switch off and remove the $680 \Omega$ resistor. Note that $R_{108}$ affects all voltages and so the programming voltages must be set up in this order.

To test the listing function connect a printer and, if it is set for one of the available data rates

Main circuit correction. The circuit around pin 1 of the master and slave sockets shown last month is revised as shown.

other than 9600 baud, use the UP or DOWN buttons to move the pointer to the desired rate and press the LIST button to select that rate. Move the pointer to 2716 and press the LIST button, whereupon the printer should

Fig. 2. The serial interface D connector on the programmer used for computer connection or printer driving, left.
Fig. 3. Cable connections for connecting the programmer
to SC84. These crossovers are required as the computer and programmer have the same pin numbering.
Normally, connection to a printer wil be pin for pin.


Fig.4. Multi-rail power supply for the intelligent eprom programmer. Programming voltage $V_{p p}$ is determined by the processor through input/output expander EXPO pins 2, 3 and 4.



Fig.5. 8741/8/9 microcomputer programming adaptor. Numbers in circles refer to pins on the eprom programmer slave socket into which this adaptor is plugged.
These extra components are needed to drive the processor clock and switch voltage levels.

A complete kit of parts for the eprom programming unit including transformer, zif sockets, unpunched case, connectors, hardware and programmed eprom but not p.c.bs and parts for the
microcomputer adaptor can be obtained by sending $£ 77.50$ including postage but exclusive of vat to John Adams at 5 The Close, Radlett, Hertfordshire, telephone Radlett 5723. Parts for
the adaptor excluding p.c.b. but including sockets are an extra £15.00. A disc with the SC84 control program is $£ 5.50$ excluding vat but including postage - please state disc size, etc. A set of p.c.bs including a plated-through and silk-screened
main circuit board are available
from Combe Martin Electronics, King Street, Combe Martin, North Devon EX34 OAD for $£ 17$ including UK/overseas postage.

The microcomputer
programming board is an extra £4 including postage.
start to list memory as a series of FFs. Note that as yet, no eproms have been inserted into either the master or slave sockets.
When using the programmer, do not remove or insert eproms from or into the slave socket unless the OK or ERROR leds are lit since the $V_{p p}$ programming voltage will be present on the slave socket. During listing, all three status leds stay off until the end of the operation. When programming, the end of the operation is indicated by the PROGRAM led going out and either the OK or ERROR leds lighting to indicate good or unsuccessful programming respectively.

At the end of each line printed during listing, the programmer checks the keyboard so that a listing may be interrupted by pressing and releasing the UP or DOWN key. If LIST is selected and a printer is not connected, the system will need to be switched off and then on again to regain control.

## Programming one-chip processors

Design of this programmer is flexible enough to be adapted to virtually any eprom device, due primarily to the general-purpose
nature of most of the signal connections to the programming socket. One example of an unusual programming task is to program microcomputers with built-in eprom.
Several such microcomputers - including the one used in this programmer - are available with built-in eprom. The idea of such a device with eprom is that it more closely emulates the final rom version of a one-chip microcomputer during development of a circuit. Using external memory as the program store means that signal lines have to act as data and address lines which severely restricts their use as general i/o lines. Further, as anything up to 16 pins are freed to act as i/o lines, use of the eprom version reduces the need for peripheral i.cs.

Rather as with eproms, manufacturers saw the eprom microcomputer as a development tool and thus a small-volume product. Just as with eproms, mánufacturers appear to have been caught out by their massive use in production as a means of maintaining product flexibility. Main suppliers of eprom microcomputers are Intel and NEC, types offered being the 8741A, 8748,8749 and the more advanced 8751 and 8744.

Being 40 -pin i.cs the programmer requires an adapter, which comes in the form of a small p.c.b. with a few components, a 40 -pin zif socket and a 28 -pin plug. One adapter is suitable for programming $8741 \mathrm{~A}, 8748$ and 8749 i.cs, Fig.5. Devices in the 8748 series are more difficult ot program, needing three controlled high voltages and multiplexed addresses and data. The programmer provides all of the control signals and even scrambles data to and from the 40 -pin socket in order to made p.c.b. layout simpler.
As the adaptor only has one socket, direct copying of one 40pin device to another is not possible. A bigger p.c.b. would be needed for this. Direct copying of these devices is not frequently required so I opted for the cheaper solution - one zif socket costs more than the rest of the adapter put together. The adapter always fits into the slave socket as it needs high voltages for both reading and writing. When the adaptor is used, the programmer should be controlled by computer, whence all operations except direct copying are possible. Copying can of course
be carried out by reading the master device into a disc file and then programming other devices using the file

The address for the 8748 programming operation is not generated by the programmer's CD4040 as it is for eproms, but from a count kept in one of the 8035 ram locations. This counter is incremented after each location programmed and the 4040 is clocked every time the eight-bit counter passes through zero. Thus low-order address lines on the 28 -pin socketlact as the higher-order address lines on the 40 -pin one. Four such lines have been laid to the socket so that the adapter has the potential to program devices with up 4Kbyte of eprom. These don't exist yet but if an 8750 is produced, the system is ready to program it.

As mentioned earlier, the data lines are scrambled. This illustrates one of advantages of programmable over discrete logic. There are many occasions when a little soft ware saves a lot of p.c.b. design effort. Perhaps too few electronic designers have to design their own p.c.bs as well! In this case, the top five lines are reversed, $\mathrm{D}_{7}$ going to $\mathrm{D}_{3}, \mathrm{D}_{3}$ to $\mathrm{D}_{7}, \mathrm{D}_{6}$ to D, and $\mathrm{D}_{4}$ to $\mathrm{D}_{6}$. As well as being the optimum solution to p.c.b. layout, this mirror transposition allows the same software routine to both scramble the data prior to programming and unscrable data read back for verification.

To set up the adapter fit it to the programmer, select 8749 and press the LIST button without a printer connected. Resistor $\mathrm{R}_{209}$ is adjusted to set the voltage on pin 7 of the 40 -pin socket to 18 V . Short of base of $\mathrm{Tr}_{202}$ to its emitter and set $\mathrm{R}_{2 \text { nfon }}$ to give 18 V on pin 25 of the 40 -pin socket. Finally, short the base of $\mathrm{Tr}_{201}$ to its emmitter and set $\mathrm{R}_{203}$ to give 21V on pin 26 of the 40 -pin socket. After completing this procedure, switch the programmer off to clear the list instruction. Don't forget to remove the shorts.

Working of the programmer and the software controlling it are subjects of the next article

## The 8048

There are five types in this family of one-chip microcomputer, namely the 8035,8039 , 8049 and 8748 , each with varying memory type/capacity, see table. All can address up to 4 Kbyte of external program rom, 256 byte ram and have 32 bytes of dedicated ram.

## 1/0 facilities

Port zero is an eight-bit port which may be used for parallel i/o. When accessing external memory, this port multiplexes the lower eight bits of the memory address and data read from or written to memory. Separate WR, RD and $\overline{\text { PSEN }}$ signals are available to strobe data to and from external memory and from program memory respectively.

On eight-bit port one, the lines may be used as individual inputs or outputs. The output structure of these lines is interesting. When set high, only a weak pull-up resistance of about $50 \mathrm{k} \Omega$ holds the line high but during the actual transition from low to high, a strong pull up is temporarily applied to ensure speedy action. In its high state, as well as being a high output, the line may also be used as an input.

On port two, also eight bits, the upper four lines act in the same way as those of port one. The lower four lines are available for i/o but also output the higher-order memory address lines during external program memory access, and act as a four-bit command and data bus between the microcomputer and any expanders fitted to the system.
Test inputs $\mathrm{T}_{0}$ and $\mathrm{T}_{1}$ can be used as subjects of conditional jumps in the program. A special instruction sets $T_{0}$ and $T_{1}$ can be used as subjects of conditional jumps in the program. A special instruction sets $\mathrm{T}_{0}$ as a clock output running at one third of the crystal frequency. Test line $\mathrm{T}_{1}$ may also be used as the input to the event counter. An active low maskable interrupt input, $\overline{\text { INT, }}$, may also act as a test input for conditional jumps.
Other pins are a strobe for

## 8048 family characteristics, values in bytes

| Type |  |  |
| :--- | :--- | :--- |
|  | Internal rom Other ram |  |
| 8035 | None | 32 |
| 8039 | None | 96 |
| 8048 | 1K | 32 |
| 8049 | 2K | 96 |
| 8748 | 1K (eprom) | $\mathbf{3 2}$ |
|  |  |  |

the expanders, $\overline{\text { PROG, an }}$ address-catching latch, ALE, a single-stepping input, SS , and an input forcing the processor to access external memory, whatever its type.
The microprocessor accesses external memory automatically when a pro-gram-counter address exceeds the limit of the internal rom. The incrementing section of the 8048 program counter is actually only an 11bit register. To access 4 Kbyte , an extra address line is required so the contents of the memory bank flag (MBF) form the twelfth bit of the address. This bit is set or cleared by the instructions SEL MB0 and SEL MB1 and transferred into the program counter on the next jump or call operation.

Programmer control lines

| Line | $\mathbf{8 0 3 5}$ |
| :--- | :--- |
| $P_{10-17}$ | Data bus to eproms |
| INT $^{\prime}$ | Serial data input |
| $\mathbf{T}_{0}$ | Serial hand-shake input |
| $P_{24}$ | Select EXPO |
| $\mathbf{P}_{25}$ | Select EXP1 |
| $P_{26}$ | Serial handshake output |
| $P_{27}$ | Serial data output |

## Internal facilities

An eight-bit counter/timer capable of interrupting the system is provided. When used as a timer it is clocked at $1 / 480$ of the crystal frequency; it can set a flag and interrupt the system when the count passes through zero. The processor's built-in clock generator requires only a crystal and two capacitors externally. Only one capacitor is required by the power-on reset circuit.
Crystal frequencies for the 8048 may be as high 11 MHz , this being divided by 15 to give the basic instruction rate. A typical crystal frequency is 6 MHz giving a cycle time of $2.5 \mu \mathrm{~s}$ and a basic timer unit of $80 \mu \mathrm{~s}$. Some $60 \%$ of the instructions execute within one cycle. The remainder, mostly immediate instructions where a second byte of data has to be fetched as part of the instruction, take two cycles.
Two identical banks of eight 8 -bit registers which may be alternated between are used (similar to the Z80's). Two registers in each bank, $\mathrm{R}_{0}$ and $\mathrm{R}_{1}$, may be used as pointers to ram for indirect operation, the others are solely general purpose. Lastly, the 8048 has two settable and testable flags, one of which is automatically preserved during an interrupt.

## Instructions

Data movement between registers, the accumulator and internal/extemal memory

| Line | IC, (EXPO) |
| :---: | :---: |
| $\mathrm{P}_{40}$ | PSU on |
| $\mathrm{P}_{41}$ | PSU select 12V |
| ${ }_{P 43}$ | PSU select 21 V |
| ${ }_{\text {P }}$ | S22 through RL ${ }_{2}$ |
| ${ }_{P} \mathrm{P}_{51}$ | Counter clock, M22 |
| $\mathrm{P}_{53}$ | Counter reset |
| $\mathrm{P}_{60}$ | RL ${ }_{\text {, }}$ S23 |
| $\mathrm{Pb}_{61}$ | $\mathrm{RL}_{2}$, $\mathrm{S}^{22}$ |
| $\mathrm{P}_{62}$ | $\mathrm{RL}_{3}$, S1, M1 |
| $\mathrm{P}_{63}$ | RL ${ }_{4}$ S26, M26 |
| ${ }^{\mathbf{P}_{71}}$ | $A_{12}, ~ M 2, ~$ $A_{13}, ~ M 26, ~ S 26 ~ t h r o ~$ |
| $\mathrm{P}_{72}$ | $\mathrm{A}_{14}{ }^{13}, \mathrm{M} 27, \mathrm{~S} 27$ |
| $\mathrm{P}_{73}$ | $\mathrm{A}_{15}, \mathrm{M1}^{\text {through }} \mathrm{RL}_{3}$ |

## IC $\mathrm{B}_{\mathrm{a}}$ (EXP1)

LED row 0
LED row 1
LED row 2
LED row 3
LED column 0
LED column 1
LED column 2
LED column 3
Sounder
OK led
ERROR led
PROG led
S1 UP key
S2 DOWN key
S3 PROG key
S4 LIST key
is fully catered for. In particular there are indexed instructions of the 'get the byte pointed to by the A register from a particular page of rom and put it in the A register' form. This allows a particular page to act as a very efficient area for look-up tables.

There are basic arithmetic and logical commands capable of acting directly on the A register and input/output ports, allowing direct mainipulation of from 1 to 8 bits a port. There are long jumps as well as short ones (i.e. within a page) which can be conditional on zero in the accumulator, the carry bit, any of the accumulator bits, a timer overflow, internal flags, the test lines or the interrupt line.
Z80-style decrement and jump-on-not-zero instructions (DJNZ) are available for all 16 R registers. Of particular interest is a jump of the form - go to the location pointed to by the A register in the current page, get the byte there and put it into the lower byte of the program counter. This allows very efficient transfer of execution to one of many routines depending on the contents of the A register.
Standard call and return mechanisms are used for subroutines although there are no PUSH and POP instructions for storing information on the stack. The stack is within an area of memory devoted to ram and is eight return addresses deep. When a subroutine is called or an interrupt accepted, the 12 -bit program counter and four status flags (carry, half-carry, internal zero and register bank) are pushed on to the stack. There are two types of return instruction. One just reinstates the program counter, the other, designed to make the call operation more transparent, reinstates flag bits too. The latter type is usually used for interrupts.

[^7]
# Semiconductor suppliers 

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High Wycombe, Bucks HP13 6EE. Telephone: 0494441129 C.W.O. Speciality: Computer peripherals.

## ADD-ON DEVICES,

11 Shield Road,
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Ashford, Middlesex TW15 1AU.
Telephone: 0784247141.
c.w.o. Minimum order £25.

Speciality: Surface-mounted components.

AEROMEL INTERNATIONAL, 17 Harland Way,
Southborough,
Tunbridge Wells, Kent TN4 OTQ.
Telephone: 089237977
Trade. Speciality: Milspec. devices.
ALTEK MICROCOMPONENTS, 22 Market Place,
Wokingham, Berks RG11 1 AP.
Telephone: 0734791579 c.w.o. Speciality: Microprocessors and memories.

AMBAR COMPONENTS, Gatehouse Road,
Aylesbury, Bucks HP19 3ED
Telephone: 029634141 c.w.o. Speciality: Microprocessors etc.

## ANGLIA COMPONENTS

Burdett Road,
Wisbech, Cambs PE 132 PS.
Telephone: 094563281
c.w.o. Exclusive distribution. Promax

ARIES ELECTRONICS,
159 Boyn Valley Road,
Maidenhead, Berks SL6 6DT.
Telephone: 062837431
c.w.o.

ARROW ELECTRONICS,
Leader House,
Coptfold Road,
Brentwood, Essex CM14 4BN.
Telephone: 0277219435
Trade.

At EWW we receive a regular stream of enquiries for the sources of components. In order to answer it is often quite a task to wade through suppliers' catalogues to find the information. We have constructed this list in order to provide some clues as to the possible whereabouts of semiconductors. It is not comprehensive but we have tried to identify those companies who do supply the end user rather than representative or agency companies. We hope to publish a list of manufacturers and their UK agents - as a supplement to this survey at a later date.

We have had direct contact with all the companies listed either through their postal answers to our questionnaire or by telephone. One question which we considered of importance was whether the distributors were willing to sell "small quantities to private buyers." We were pleased and a little surprised that a large proportion of them were and we have indicated this by C.W.O. (cheque with order), after their addresses. Others were marked 'trade'. We also asked if any of their lines were exclusive to the individual company and if they specialized in any particular field. Their answer are recorded in each entry. All of them claim to be able to respond to an order on the same day or within 24 hours. Most of them are willing to identify substitutes and provide them as alternatives if the desired components are not available, but only after consulting the customer.

All say that they can provide data sheets for the products they distribute and do not charge if the product is purchased at the same time. Most companies also said that they were abot to offer technical advice on the the applications of components.

Considering the current interest in microcomputers and communications, we thought it would be interesting to see if any of the companies offered data bases and computerized ordering. None of them did, but a few were thinking of it or were installing equipment to do so.

AVIQUIPO OF BRITAIN,
St. Peters Road,
Maidenhead, Berks.
Telephone: 062834555 c.w.o. "We can supply any semiconductor."

## BARLEC-RICHFIELD

Foundry Lane,
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Telephone: 040351881
Trade.

BARRIE ELECTRONICS, Unit 221, Stratford Workshops, Burford Road, London E15 2SP Telephone: 015550228
Retail. Speciality: Transformers.

BETA DEVICES,
6 Sun Street,
Waltham Abbey, Essex.
Telephone: 019716529
c.w.o. Postage "at cost."

BI-PAK SEMICONDUCTORS,
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Ware, Herts SG12 9AG.
Telephone: 09203182
c.w.o.

BOSLEDGE,
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Manachester, M4 1 PE.
Telephone: 0618347339
Trade.
BOSLEDGE,
27 Church Street,
Manchester, M4 1PE.
Telephone: 0618347339
Trade.
CAMPBELL COLLINS,
162 High Street,
Stevenage, Herts.
Telephone: 043869466
CIRKIT,
Park Lane,
Broxbourne, Herts.

Telephone: 0992444111 c.w.o. Mail-order catalogue.

CONSORT ELECTRONICS,
Rosebank Parade,
Reading Road,
Yateley,
Camberley, Surrey GU17 7RN.
Telephone: 0252871717
c.w.o. Exclusive distribution:

Solitron, Microsemi.
CURZON ELECTRONICS,
17c London Street,
Basingstoke, Hants RS21 1NT
Telephone: 025651841
Trade.
DTV GROUP,
2 Ernest Avenue,
West Norwood, London SE27 ODJ.
Telephone: 016706166
c.w.o. Retail counter.
D.W. ELECTRONICS
(CAMBRIDGE),
Tudor House, High Street,
Fenstanton, Huntingdom PE 18
9JZ.
Telephone: 048067666
c.w.o.

DAGE EUROSEM,
Rabans Lane,
Aylesbury, Bucks
HP19 3RG
Telephone: 029632881
c.w.o. Speciality: Digiral i.cs, Power fets.

DIALOGUE DISTRIBUTION,
Watchmoor Road,
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Telephone: 0276682001
c.w.o. Speciality: Digital i.cs, Power fets.

DISTRIBUTED TECHNOLOGY,
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Oxted, Surrey RH8 9PA
Telephone: 088336161
c.w.o. Exclusive distribution:

Trelec, TIC. Speciality: Power
Semicond, r.f. devices.
ELECTRONIC RESOURCES,
Henlow Trading Estate.
Henlow, Beds SG16 6DS.
Telephone: 0462815555
c.w.o.

ELECTROVALUE,
28 St. Judes Road
Englefield Green,
Egham, Surrey TW20 OHB,
Telephone: 078433603
c.w.o. Retail shop.

FARNELL ELECTRONIC COMPONENTS
Canal Road,
Leeds, Yorks LS 12 2TU.
Telephone: 0532636311
Trade.
G.E. Electronics (London) Ltd.

Earley. House,
182 Campden Hill Road,
London W8 7AS.
Telephone: 017270711
c.w.o.

GAIN ELECTRONICS,
63 High Street,
Prices Risborough,
Aylesbury, Bucks.
Telephone: 084447116
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GOTHIC CRELLON,
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Telephone: 062864300
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Telephone: 0703772501
Retail shop.
HARMSWORTH, TOWNLEY \& CO.
Harehill,
Todmorton, Lancs OL14 5JY. Telephone: 0706814931 c.w.o.

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Sunbury-on-Thames, Middlesex.
Telephone: 019797799
c.w.o.

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Birmingham B33 0TQ.
Telephone: 0217842485
HB ELECTRONICS,
Lever Street,
Bolton, Lancs BL3 6BJ.
Telephone: 0204386631
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Telephone: 0525405015
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Telephone: 016671531
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COMPONENTS,
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11 Shield Road,
Ashford, Middlesex TW15 1AU.

Telephone: 0784246273
Trade.

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Telephone: 088346433
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Telephone: 072442636
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L.S.W. Components Service,

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Telephone: 0234826824

LANGREX SUPPLIES,
Climax House,
Fallsbrook Road
London SW16 6ED.
Telephone: 016772424
c.w.o. Speciality: Obsolete
components.
MCP Electronics,
38 Rosemount Road,
Alperton, Middlesex HAO 4PE
Telephone: 019026941
c.w.o. ( $£ 25$ minimum order) Speciality: r.f. and digital signal processing.

MDL COMPONENTS,
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Telephone: 0202681331
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## MACRO-MARKETING,

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Trade.

## MEMEC,

Thame Park Road,
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MERLIN ELECTRONICS,
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Maidstone, Kent ME15 6JP.
Telephone: 0622678888
c.w.o.

MICROLOG,
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Duke Street, Woking, Surrey GU21 5BA.
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c.W.O.

MS COMPONENTS
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W. Norwood, London SE27 9LH.

Telephone: 016706166
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MCLELLAND ELECTRONICS,
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Kitson Road, Leeds LS10 3YY.
Telephone: 0532460572
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Ditton Walk,
Cambridge CB5 8QD.
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Spencers Wood,
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Cresto House,
Water Lane,
Wilmsow, Cheshire.
Telephone: 0625529523
Trade.
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Ilford, Essex IG2 6LE.
Telephone: 015546222
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QUARNDON ELECTRONICS
(SEMICONDUCTORS),
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Derby DE3 3ED.
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## RS COMPONENTS,

PO Box 427 ,
Epworth Street,
London EC2P 2HA.
Telephone: 012531222
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brand' and therefore they do not list their manufacturers. Most semiconductors in the RS catalogue are listed as 'similar to' recognised components. Trade only

## RASTRA ELECTRONICS,

275 King Street,
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Telephone: 017483143.
c.w.o. or credit card. (£10
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Reading, Berks RG1 4 LS.
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2E Saxby Road Industrial Estate,
Melton Mowbray, Leics LE13 1BS.
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Trade.
SEMELAB,
Bank Street,
Lutterworth, Leics.
Telephone: 045554711
Trade.
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Vine House,
104 Ashley Road,
Walton-on-Thames,
Surrey KT12 1HP.
Telephone: 0932241866
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Continued on page 81 Table on pages $60 \& 61$



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## B. BAMBER ELECTRONICS



## D.C. SUPPLIES

I got lost in Dr Smith's generally excellent article on "D.c supplies from a.c. sources", October issue, when I got to the Appendix, after the first equation on page 68. That equation indeed applies for a square wave of voltage balanced about zero volts.
"If you drive the winding with a unidirectional pulse train..." starts the next paragraph; ... Of voltage one assumes pro tem. The core will then certainly "be magnetized in one direction only" in the sense that each application of unidirectional voltage will drive the flux in the same direction, in accordance with Faraday's Law cited on the previous page. But this immediately followed with a reference to Fig. A3 and a peak value of flux which seems to be followed by a reversal of flux change. What voltage accounts for this? (There can in practice be several answers but they all seem to introduce factors which have not been included within the stated problem with its unidirectional pulse (voltage) train.)

If the unidirectional pulses are of current there is no perceivable relevance of flux considerations at all unless by some very indirect route involving permeability.
In my experience a circuit diagram always helps, even in the simplest cases. In Fig. A3 there are two graphs for i , the lower of them labelled
"decrease in magnetizing current". Is this shown separately because it is supposed to be in a different winding?
Below A3 a paragraph starts "The usual procedure is to take the ratio of the mean values of flux...". Looking at Fig.A4 and wondering what two or more values of mean flux there might be, I see $\eta$ used as if it were the ratio between the peak (supposedly equal) positive and negative excursions of flux about the (single unique) mean, at last in the botton part of Fig. A4. But the range of flux change (whether in time T/2 or not depending on unstated assumptions) is then $2 \eta \Phi$ where $\Phi$ is the mean flux. But by this time the meaning of the symbols is starting to become obscure. Certainly it is a matter requiring proof, to my mind,
that

$$
\hat{\nabla}=\frac{4 \eta}{1+\eta} \mathrm{NBAf}
$$

which I would be interested to see established.

Finally. I deplore your lapse from the excellent practice, so long maintained after other once-fine journals departed from it, of maintaining a sensible separation between advertising and the rest. I have for years pursued the advertisements thoroughly in $W W$ when it has suited me: I am now wondering for how long I will have the patience to avoid the d...d things when I want to read the rest.
R.S. Taunton

Crowborough
East Surrey

## BAIRD

I was very surprised to read in the June issue of Electronics \& Wireless World on page 15 and 16 under the heading 'Communications Commentary' - sub-title 'Baird in history' that ."Baird's low definition television was barely ... true television ... since, even in its final broadcast form, it contained no real sync. signals". It does of course depend upon what meaning one attaches to those last four words.

For two years I have been compiling a chronological history of early mechanical television taken from the pages of technical journals and books of the period 1870 to 1937, including our own Wireless World, Television (\& Short Wave World), The Electrician and many others.

To avoid making this account too long I will confine myself mainly to statements found in text books of the early 1930's when the service was actually in operation and which show that the importance of the synchronizing signals actually transmitted at 375 Hz were well appreciated and were well within the capabilities of all main BBC m.w. broadcast transmitters as explained in the extract from the 1st book.

1. Book of Practical Television edited by G.V. Dowding Publisher: Amalgamated Press, Farringdon St., E.C.1. 1935 Chapter 9, 'Synchronizing a mechanical viewer', p. 106:
"In mechanical systems using one rotating member, the positioning of the lines is a fixed factor using one rotating member, the positioning of the lines is a fixed factor in relation to the rotating disc or mirror drum that produces the number of pictures per second. So long as the device rotates at the right speed the number and positions of the lines is bound to be correct. Centering the picture or avoiding it being split is bound to be correct.
Centering the picture or avoiding it being split vertically is all that has to be adjusted as there are $12 \frac{1}{2}$ pictures per second, each with 30 lines (with the black spacer at the top), the frequency of the synchronizing pulses will be 375 per second - the 'phonic wheel' synchronizer will have 30 teeth with the distance between each tooth being approx twice the width of each tooth".
2. Television for the Amateur Constructor by H.J. Barton Chapple Publisher: Sir Isaac Pitman \& Sons, London. 2nd Edtn 1934

Chapter IX 'Making Vision apparatus', page 194, "- using the 'tuned' synchronising transformer to resonate at 375 $\mathrm{c} / \mathrm{s}$ to give a strong sync. -" Author's note: This would also enable the sync. signals to be connected in the correct phase, see later explanation.
Television Engineering by J.C. Wilson Publisher: Sir Issac Pitman \& Sons, London. 1937.

Chapter IX 'Synchronizing', pages $340 / 1$, Figs $206,7 \& 8$
'Toothed wheel mechanical synchronizing' - moreover the impulses must be positive in the coils of the synchronizer, they must give rise to an increase in the current passing through the coils and not a decrease. Circuit 207 shows how this can be accomplished for low definition transmissions (a transformer coupled circuit) - the usual resistance of the two coils in series is between 700 to 1000 ohms with an inductance of some 3 to 4 H (page 342).

This book is notable also for the completeness of the various chapter references and for Appendix VII 'Table of early t.v. disclosures' which I have found of great assistance.

In the case of the earlier more 'popular' books, it does not seem to have been appreciated that the
synchronizing signal of the Baird-BBC 30 -line service was derived from a black line at the top of the picture, scanning being carried out from the bottom right hand corner of the vertical upright picture, the spot moving vertically upwards, covering the picture area from right to left, finishing in the top left hand corner. The lines at the centre of the picture were also more narrow than those at the outside edge to improve centre picture definition.

Various accounts emphasise the need for a strong pulse into the sync. coils for a steady picture. However with the Baird disc scanner receiver where most circuits show the sync. coils connected in series with the neon picture lamp, this would result in a negative picture as well as requiring a high h.t. voltage of over 200 volts, usually derived from batteries. To get a positive picture with strong sync. pulses, the circuit on page 106 of the 'Book of Practical Television' would have to be used with the sync. signals via the transformer phased for positive-going with a positive picture from the negative signal, the black sync. line at the top of the picture giving maximum transmitter output.

An example, however, of the good sync. results that could be obtained from a disc-scanning receiver is given on page 386 of the September 1934 issue of 'Television' in which the 'Disc Kit' being supplied by British Television Supplies Ltd., of Bush House is being reviewed. The last paragraph states 'the synchronizing gear was effective in holding the picture steady for long periods. We have every confidence in recommending this kit receiver'. In this version the phonic wheel assembly was mounted on the scanning disc spindle, while the driving motor was fixed to the wooden baseboard at one side connected to the spindle by pulleys and a circular form rubber belt to reduce 'hunting'.

Much correspondence and technical notes appeared in the various technical magazines under the heading 'Reversing Negative Pictures' but regretfully almost no attention was given to the importance of strong positive-going pulses in the phonic wheel synchronizer coils by correct phasing from a postive picture signal. A good
example of a note under the above heading is at the bottom of page 144 of the April 1934 issue of 'Television'.
The apparent general failure to appreciate the above probably accounted for the poor overall reputation of the Baird disc-scanner receiver. The later mirror drum versions were much superior and I can personally remember the whole 30 or so minute transmission (including the ' 8 Step Sisters') without any loss of synchronization and this is confirmed by a Wireless World review of the Bush radio, Baird mirror drum receiver in the Sept. 15th 1933 issue, pages $237 / 8$, where favourable comment was made to the steadiness of the picture. "The synchronizing is really good and during a half hour transmission the apparatus often carries through without going out of sync. even once - . - the lapses from sync. are apparently due more to the transmitter scanner than the receiver, for they invariably occur at the end of an item when the curtain falls to the studio. Apparently this interrupts the sync. impulses". F. Poperwell

Totteridge
London

## RELATIVITY

It was delightful to read that Professor McCausland has finally come down in favour of the anti-relativists. His polite, 'bending-over-backwards' approach to see the relativists point of view must have been quite frustrating. I congratulate him for his succinct conclusion that Special Relativity is now too suspect to be regarded as a tenable theory.
It is also delightful to read Dr Murray's views and comments on one of Einstein's mistakes concerning Relativity theory.
Can I now enter the field with a suggestion? Since Wireless World has aggregated an elite band of contributors and letter writers who are dissatisfied with contemporary physics, why not hamess such dissident energy? Such a team could reshape the physics of the world!
I suggest this harnessing could be done by a scheme (produced under the auspices of Wireless World) as follows

- Wireless World to publish a series of articles on the new physics.
- Wireless World to issue printed self-adhesive labels for affixing to the above folders.
- Each article to be classified by the Decimal Bibliographical System.
- Introductory pages to be available giving a list of physicists in sympathy with the London School whether contributors or not.
- Every so often Wireless World should describe an experiment that should be made in order to test theories put forward by contributors.
A.H. Winterflood

Muswell Hill
London N10

I must have written my letter (July) badly, because I was concerned with what kind of factors would lead us to approve or disapprove of a theory, and not with any application to specific points in relativity. Presumably everyone agrees that special relativity works, in the sense that it gives a good quantitative description of phenomena studied in mechanics. (The first cyclotrons only worked with low energy particles because they assumed Newtonian mechanics to be accurate; only after they were re-designed with relativistic mechanics would they work with higher energies.) So if the theory appears to be nonsensical in some particular application, what are we to make of it all?

One guiding principle is "don't throw the baby out with the bath water"; anyone who does that is not a fit person to bath the baby. Do you remember the bell ringers and the clock maker? Look at all relativity's successes as well as its difficulties and then decide if there is something worthwhile and important in it. Einstein won just fame because he wrote the first worlds on relativity, not the last words; as most of its applications are at the frontiers of science the theory has developed since his day. The helpful thing to do is to show how its flaws can be overcome, not merely to snigger and do nothing constructive. Rightly or wrongly, scientists are impressed by a theory which (in its growing stages) does a lot
which is right; they like to work on its rough spots and improve it rather than throwing out the baby.
One thing that I will not do is to give away my own views on the correctness of relativity, so don't try to read between the lines! What I do want people to do is to think about what kind of evidence or arguments are appropriate for use with a theory whose applications are in the frontier districts of science. It has been failure to do this rather than anything to do with relativity which has kept your columns busy.
J.G.D. Pratt

West Horsley
Leatherhead
Surrey

In his article "The Roots of Relativity' in the May issue Dr Scott Murray claims to reveal a crucial mistake in Einstein's early (1905) account of a 'thought experiment' on the failure of simultaneity in special relativity. He states (p.71, col.3) that "at the instant when the flashes occur the position of $\mathrm{M}^{\prime}$ coincides with that of $\mathrm{M}^{\prime \prime}$. With this phrase and in what follows he assumes that if $M$, the observer on the embankment, interprets the lightning strikes at $A$ and $B$ as occurring at the same instant, i.e. as being simultaneous, then so will the observer on the train, $\mathrm{M}^{\prime}$. By so doing he invalidates the whole of his subsequent discussion. In fact if M interprets the two strikes as being simultaneous $\mathrm{M}^{\prime}$ will interpret the strike at B, toward which point he is travelling, as occurring before that at $A$. It is a safe bet that hardly any of the 'scientifically educated people (who) harbour niggling doubts about it (special relativity) have really grasped what the failure of simultaneity entails. The mutually contradictory nature of the various criticisms of special relativity, which Dr Murray himself remarks upon, reflects the different points in the criticisms at which the assumption of universal simultaneity is slipped in, a process of which Dr Murray's analysis of Einstein's crucial mistake' provides a typical example.

Some hypnotist must have been at work on him to account for the way he fails to register the key provisos 'as judged
from' and 'considered with reference to-' 'the railway embankment' which occur in Einstein's exposition. Einstein asserted, not that $\mathrm{M}^{\prime}$ was hastening away from or toward a beam of light in any absolute sense, but only that he was doing so relative to the observers stationary with respect to the embankment. Consequently light from the strike at B would certainly reach him before it reached $M$, a commonsense conclusion which for once special relativity allows to be true. Dr Murray's 'routine reminder that the velocity of light from a moving source - a relatively moving source - has never been measured' has now been overtaken by events. The Open University program 'Maths; Space-Time Geometry' broadcast on BBC2 on June 2nd described a measurement carried out in Paris of the velocity of light emitted by electrons travelling round a storage ring at a speed relative to the lab. of some $95 \%$ of the speed of light. The ring consists of alternate straight and curved segments, the electrons being bent to follow the latter by magnetic fields. In the curved segments, because of their enforced centripetal acceleration, the electrons generate electromagnetic radiation. By sending bunches of electrons round the the storage ring one can cause this radiation to be generated in pulses, and the velocity of the pulses can be measured in the laboratory, in any chosen direction relative to the motion of the electrons, using suitable collimation to ensure that the radiation observed comes from the right segment. Within experimental error that velocity was found to be equal to the velocity of light. Even natural philosophers are not entitled to ignore the evidence of experiment.
C.F. Coleman Grove, Nr Wantage Oxfordshire

## FUNDAMENTALS OF ENERGY TRANSFER

It is always refreshing to see a contribution from Ivor Catt even if I do not wholly concur with his conclusions. I am well aware
of Heaviside's views that electromagnetic energy leaves a source and enters a load sideways, and he gives an example of a source in London connected by a telegraph wire to a receiver in Edinburgh. Some of the energy from London travels far out into space before converging on the receiver, , but unfortunately I have not been able to find in Heaviside's Electromagnetic Papers any account of how the energy in distant space knows that it is time to start the descent for Edinburgh. However, this is a difficult matter and it is not surprising that the story is incomplete. What is much more difficult to accept is why we should in our theory stumble on the relatively simple matter of a parallel-wire transmission line.
The National Physical Laboratory defines the SI unit of electric current as "The ampere is that constant current which, if maintained in two straight parallel conductors of infinite length, of negligible cross section, and placed 1 metre apart in vacuum, would produce between these conductors a force equal to $2 \times 10^{-7}$ newtons per metre of length."

Many text books on electricity, elementary and advanced, use similar wording to define the ampere and indeed the definition is not new. If we substitute centimetre for metre and dyne for newton we have the c.g.s. electrodynamic definition of the ampere that has been used since the beginning of this century, and during this time this piece of scientific nonsense has been for the most part uncritically accepted.

Consider the following relatively short transmission lines:
In diagram (a) the line is open circuit at the distant end hence the conductors experience a mutual electrostatic attraction. In (b) the conductors carry a current and hence mutually repel each other. In (c) we have a combination of electrostatic attraction and electromagnetic repulsion which for some value of R must neutralise each other.

Not very surprisingly the value of R to produce zero resultant force is the characteristic impedance of the line, and that can be achieved by extending the line in diagram (a) infinitely to the right. It seems to have been forgotten

that if a line has $Z_{0}=100 \Omega$
then to establish a current of 1 A in the line we need a supply of 100 V and the electrostatic attraction because of that cannot be ignored.

Forces on the conductors of a transmission line arise from reflections, and the principal characteristic of an infinite line is that it is free of reflections. Now this absence of force on an infinite line follows almost intuitively from the principal of virtual work so it is all the more surprising that the error should have gone undetected for so long. In an infinite line there is equal sharing of the stored energy between the electric and magnetic fields associated with the line. If we increase the separation of the line conductors by a small amount we need a force to overcome the electrostatic attraction, and that can be calculated from $f=d w /$ $d x$ where $d w$ is the increase in stored energy and dx the displacement. Likewise the force associated with increasing the magnetic field energy is $f=$ $d w / d x$ and because of energy sharing equally, dw is the same in each case, as is $d x$, and the forces oppose each other, so there is a net resultant of zero. Mr Catt rightly says that nothing travels sideways across a transmission line in the TEM mode and that includes force. Lateral forces on the conductors of a transmission line always arise because of reflections that upset the balance of electric and magnetic energy storage in the line. That is only possible with a line of finite length.

Of course it may be objected
that the definition does not specify that the two conductors should be the go and return of a single circuit. They could perhaps be the two go conductors of a circuit with a distant common return. Apart from the added complexity due to the third conductor the problem is the same whether the currents in the specified conductors flow in the same or opposite senses. The twinbeam c.r.o. is an example of two parallel conductors carrying current in the same direction. It can be easily shown for this case that for relatively low anode voltages the beams repel each other electrostatically. As the anode voltage is increased the magnetic attraction of the beams becomes greater and would exactly neutralise the repulsion if the electrons in the beams could accelerate to the speed of light and there, as in the case of inifite parallel conductors, the nett force would be zero. Examination questions on this part of c.r.t. science are not uncommon and take the form of "Show that, no matter how the beam electrons are accelerated, the force between beams can never become attractive". The short answer to that is that we can never get the electron velocity up to the energy propagation velocity of the parallel conductors.
It is quite understandable how the definition of the ampere comes to be as it is. A line of finite length has end effects that we do not know how to take into account, so what could be simpler than to remove them to infinity? Unfortuntely this ploy leaves us with a useless line as far as the measurement of force is concerned.

In practice, the SI definition makes no difference for no one pays any attention to it. The ampere is standardised using an Ayrton-Jones current balance in which the conductors are arranged as circular coils and not as straight lines. In the Ayrton-Jones balance we are dealing with equivalent lines finite length short-circuited at the far end so that all the energy is stored in the magnetic field and none in the electric field, so we have no problems.

However, the definition does make a difference of those of us like Mr Catt and me who have some responsiblity for educating the young in fundamentals of our science. How can Mr Catt
persuade his students that nothing traverses a transmission line in the TEM mode sideways, when in all quarters, they see "authoritative" statements to the contrary?
Chris Parton
Bell College of Technology Hamilton
Scotland

Mr Catt's article (September 1984) treads some very shaky ground: I consider many of his statements to be rather questionable but I think I can lay the rest the so called 'Catt Anomaly'.

If I understand Mr Catt correctly he is unwilling to accept that a charge pulse can travel down a transmission line at a speed greater than the speed of light local of the copper of the conductors.
In fact, the speed of light in the conductors (or, for that matter, the electron drift speed of some millimetres a minute) has no bearing at all on the speed of an EM pulse travelling down the transmission line. In 1 nanosecond charge does not have to travel 1 foot down the wire: all that is required is for a drift of charges to occur at the leading edge of the pulse, as it moves, so as to leave a net charge on the wire, in the wake of the pulse.

If this does not seem clear, consider the case of a low amplitude sound pressure pulse, travelling down a pipe: the air in the pipe, behind the wavefront travels very slowly indeed, while the pressure front travels forwards at the speed of sound.

I should like to add that I consider the issue of whether EM fields in a waveguide cause currents in the conductors, or vice versa, to be a meaningless, unanswerable question: field and currents are related by the physics of the situation, one does not preceed the other in time, and no wave or pulse travels without both.

There are a great many statements in Mr Catt's article to which I take exception, but I lack the enthusiasm to describe all the fallacies, or research his reference list to isolate their origins.
N:C. Hawkes
Abingdon
Oxfordshire

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ELECTRONICS \& WIRELESS WORLD DECEMBER 1984

# Mobile radio system and techniques 

## Topics discussed at the IEE conference recently held in York.

Efficient use of the radio spectrum was the theme that ran through many of the papers presented at the IEE's September 1984 conference in York on "Mobile Radio Systems and Techniques". The radio spectrum is a natural resource of finite quantity, much of which is subject to an ever increasing demand from spectrum users, including the land mobile radio services.
Mobile radio in the UK today is going through a period of changes. The two new cellular networks at 900 MHz are now being installed by Racal and TSCR in a dramatic race to be first on the air and to be operational by early 1985. At lower frequencies, the UK's Bands I and III tv transmitters are to be turned off for ever at the end of 1984, leaving over 70 MHz of prime spectrum available for other services. Band III $(174-225 \mathrm{MHz})$ has already been ear-marked for land mobile radio use.
Internationally too, mobile radio is seeing many changes with new cellular radio network being planned or installed in several countries. The choice of system to be used (AMPS, TACS, NMT, S-900, MATS-E etc) is still being heatedly discussed in many countries. Consequently, there was no shortage of topics for the 341 delegates from 22 countries to discuss during the four days of conference!

## Spectrum efficiency

In his opening address, Mr W.H. Bellchambers, one of the five members of the Geneva-based International Frequency Registration Board, made pleas for a greater awareness of both spectrum engineering and spectrum efficiency. This should include the establishment of research posts into "frequency management" at universities and more standardization on the use of frequencies internationally.

Mr Bellchambers went on to question the wisdom of having two incompatible services operating in the same frequency band in neighbouring countries, as will soon be the case in Band III. The UK will be using these frequencies for new land mobile radio services while the UK's continental neighbours will continue to use these frequencies for tv transmissions.
The planning of the international radio spectrum is currently done through a programme of ITU conferences, starting with the administration conferences, such as the WARC held in 1979, which are followed by a series of detail Planning conferences, spread over a number of years. The time taken for the implementation of new frequency allocations has been up to 20 years. Mr Bellchambers wondered if there was not a better way of managing the spectrum internationally, that could take less time and be more responsive to changes in technology. Mr Bellchambers urged the delegates to start planning now for the next WRAC in five years time, and to
bring to that conference ideas for streamlining the international frequency spectrum management process.

In conclusion, Mr Bellchambers wondered whether the technology of the future would enable today's method of "frequency allocation" which defines specific frequencies, to be replaced by a spectrally more efficient system where a given service would be allocated a "band" of frequencies. The equipment itself would be continuously frequency agile and would allocate itself a frequency within the given band to give optimum performance under the given circumstances. This was a direct challenge to engineers to design more spectrally efficient systems.

## Cellular radio

The present phase of cellular radio being planned or installed by European administrations will not be compatible on a truly panEuropean basis. Cellular radio users will have to wait until the next generation of cellular mobile radio equipments are available in

Fig.1. Proposed Skylink America mobile satellite service for coverage of North America from a single geostationary satellite using up and down links in the upper u.h.f. bands. Mobile users would access the satellite directly.

the early 1990 's before they can drive from one country to another and still use their car radio-telephone.
Cellular radio installations in Ireland, Hong Kong and Finland were described at the conference by representatives of the respective administrations.
The Irish PTT earlier this year scrapped plans to use a 450 MHz network and has now opted for a TACS network on 900 MHz that would be compatible with the network currently being installed in the UK.
Delegates heard with interest about the experience gained with the Hong Kong 450 MHz cellular network that operates throughout the colony's tightly packed highrise building areas.
satellite over the North American continent.
A paper describing a proposal for a UK land mobile communications satellite experiment which would use four satellites (3 live and 1 standby) in a Molniya orbit to give 24 hours coverage was presented by a team from the University of Bradford. An extended feasibility study has been undertaken by a number of UK universities and the RutherfordAppleton laboratory. Apart from the super-synchronous, pear shaped Molniya orbit proposed, the other major feature of the study project is the proposed use of on-board microprocessor facilities for signal and message handling to maximize the efficiency of resource usage.


Fig.2. UK land mobile services sub-bands in Band III, planned to avoid vision carriers of the new French fourth channel programme 'Canal Plus'.

Cellular radio users in the four Scandinavian countries using the NMT system on 450 MHz can move across borders within Scandinavia and stil have access to the radio-telephone network. Future development of the NMT system will include the installation of a 900 MHz network. Even though the frequencies used may be the same as other European cellular networks (eg: the UK TACS systems will also be on 900 MHz ), the different signalling procedures used make the systems incompatible.

## Satellite versus cellular

Papers describing possible future land mobile services in North America described how coverage would be available to mobile radio users in rural and remote areas via satellite. Areas that would never likely be covered by cellular radio networks because of the low user density could be served directly by the propposed satellite land mobile network.
Skylink Corporation of America described their proposal for a mobile, portable and limited fixed services network operating in two 4 MHz sectors ( $821-825 \mathrm{MHz}$ receive, $866-870 \mathrm{MHz}$ transmit) from a single geo-stationary

## Band III in the UK

Mr K.P. Fisher of the Radio Regulatory Department of the DTI described in detail some of the technical background to the Band III frequency allocation proposals for land mobile services in the UK Government's May 1984 Green Paper on the future uses of Bands I and III after the closedown of 405 -line tv transmissions on at the end of 1984.

The DTI was well aware that any new land mobile service on the band III frequencies would have to be very carefully planned to avoid interference to the services from the transmissions from neighbouring countries that will continue to use band III for the foreseeable future.
In planning the future use of band III for mobile services in the $\mathrm{UK}, \mathrm{Mr}$ Fisher stated that it is the level of interference caused to continental tv viewers that is the limiting system design contraint rather than the interference caused to the mobile services by the overseas tv signals. Mr Fisher informed the delegates that an agreement had been drawn up with the French administration setting out the highest
permissible signal strengths from UK land mobile services in France.

A large response had been received from industry and interested parties to the Government's Green Paper on the future uses of these frequencies. Mr Fisher informed the conference that the RRD would be making an announcement concerning its preliminary findings at the IEE colloquium to be held on 8th November.

## "Relugees from Band II"

The mobile radio networks run by the UK's utilities (Electricity, Gas and Coal) are currently operating on frequencies just above the present f.m. Band II (104-108 MHz ). As the f.m. broadcast band limit is to be increased to 108 MHz on an exclusive basis by 1995 at the latest, the radio networks operated by the utilities will have to be relocated. The utilities because of the nature of the services provided, need to have nationwide communications facilities. Units operating in one part of the country might have to be transfered to another to meet a particular local requirement and the communications facility needs to go with them.

Mr C.E. Dadson of the Electricity Council speaking on behalf of the mobile radio services of the nationalized power industries said that they need wide area mobile radio coverage in order to be able to provide communications for essential services. The present proposal is that the utilities should relocate from their Band II frequencies to the new mobile services allocations in Band III.
Mr Dadson expressed fears that, in practice, land mobile services operating in Band III in competition with continental stations might be put out of service for days on end because of interference caused through troposheric propagation and ducting conditions. Coastal areas, where operating restrictions in Band III would be the greatest, were often just those areas where the nationalized power industries needed to have the best communications. In conclusion he asked for special consideration of the national coverage requirements of the "refugees from Band II"!

## H.f. radio

Although the majority of the papers related to v.h.f. u.h.f.
systems, there were two papers on h.f. techniques. One by MrN . Gerdes of the School of Signals described a low-cost h.f. propagation prediction method which could be run on a BBC computer. Comparisons between actual and predicted propagation on different h.f. paths are the subject of continued investigation, but so far there appears to be close correlation. The method is intended as a low-cost alternative to the prediction programs run on main-frame computers by bureaux.

The second papers on h.f. described an experiment being carried out at the University of York into improved design for h.f. communication networks involving mobiles. The study centred around a network of sereral interconnected h.f. base stations and a mobile station. The objective was to increase the reliability of communication to and from the mobile station by the interchange of data between the base stations concerning the reception of the mobile. This was described as "geographic diversity". As part of the study a low-power h.f. link is to be set up between York and Canterbury.

## Pagers

Paging systems are also users of the radio spectrum and as they develop forward from the basic "bleeper" to carriers of messages, both numeric and alphanumeric, they bring together radio engineering and data transmission technologies. Papers described recent developments in the integration of pagerr receivers. One such paging receiver, described by a team from Philips Research Laboratories had only 12 "off-chip" components (but 13, including the battery!).

The POCSAG paging code originally devised by the UK Post Office has bow become standardized by the CCIR for international use.

## Amateur radio experiment

As with any meeting of international mobile radio specialists ther was a good number of amateur radio operators among the delegates. One of the presented papers described an amateur radio experiment which involved using a number of mobiles fitted
with 144 MHz transceivers to build up a propagation map of an area around Birmingham in order to determine the relative performance of different sites for use in a possible emergency communications operation as part of Raynet. Unlike professional propagation analyses which rely on expensive test-gear, this amateur experiment relied more on the enthusiasm of the participants that it did on test gear. (Although presumably the computer required to solve the 300 odd complex simultaneous equations after the event in order to produce the results could have been classified as "expensive test gear"!).

The IEE's Conference on Mobile Radio Systems and Techniques came at a time of unprecedented change and developments in the area of mobile radio in the UK. By the time that the next such conference is held, both cellular radio and the new Bank III mobile services will have been put into operation and it can be expected that there will then be more papers based on actual experience and less on predictive techniques that was the case this year.

References

1. IEE Conference Publication 238 "Mobile Radio Systems and Techniques" 2. HMSO Cmnd 9241, "Bands I and III" Consultative Document

## Pulse-width demodulator

Designed as part of an opticalfibre interface between a computer and e.h.t. generator, this pulse-width demodulator uses a 723 voltage regulator and fet as a low-drift constantcurrent sink.
The 723 and fet provide a 4.77 mA reference for a 10 kHz pulse-width demodulator. When the t.t.1. input is low, all of the reference current is supplied from the 5 V rail, but when the input is high, current has to be drawn through the diode from the inverting output stage. The diode and switching transistor must turn off quickly, but their on resistances do not greatly affect performance. Current-tovoltage conversion and two lowpass filter stages are provided by the output circuit. I have followed this arrangement by a 10 kHz notch filter (paralle! T) to remove modulation frequency break through.

When the 723 is driven from
a stabilized supply, the only drift problems seem to be those due to temperature. After gluing a lump of aluminium to
the device, drift was $+0.02 \%$ in the first 15 minutes, $+0.02 \%$ over the next hour and $\pm 0.02 \%$ thereafter.
Jim Dawson
University of New South Wales Sydney


DON'T WASTE GOOD IDEAS We prefer circuit ideas with neat drawings and widely-spaced typescripts, but we would rather have scribbles on 'the back of an envelope' than let good ideas be wasted. Submissions are judged on originality or usefulness not excluding imaginative modifications to existing circuits so these points should be brought to the fore, preferably in the first sentence. Minimum payment of $£ 30$ is made for published circuits, normally early in the month following publication.


## Electronic ignition key

Your car can be made burglar proof if it can only be started by entering a code on a keyboard. With this circuit, the code has to be entered within a time limit and the code cannot be corrected without setting off the
alarm, thwarting even the shrewdest car thief.* When the ignition is first switched on, latches $\mathrm{IC}_{\text {la,b }}$ and monostable circuits $\mathrm{IC}_{2 \mathrm{a}, \mathrm{b}}$ are cleared by $\mathrm{RST}_{1}$ and reset signal $\mathrm{RST}_{2}$ initiates an 11s period,
during which the alarm is disabled. If any of the systemreset keys is pressed ( 1,3 or 8 ) during this period, another 11 s cycle is initiated by $\mathrm{IC}_{3 \text {. }}$. This inhibits further code entry and the alarm is activated at the end of the cycle. When the conrect code is entered during the initial 1ls period, reset keys three
and four are pressed together to release monostables IC $2_{\text {a,b }}$ and latch $\mathrm{IC}_{\mathrm{ta}}$.

Pressing key seven, the first digit of the code, triggers the first monostable device, $\mathrm{IC}_{2 \mathrm{z}}$. The Q output of $\mathrm{IC}_{3 \mathrm{a}}$ will be high, provided that it has not been triggered. As the $\bar{Q}$ output of $\mathrm{IC}_{2 \mathrm{a}}$ goes low the rising edge of $\mathrm{IC}_{2 \mathrm{a}}$ 's B input (switch bounce is accounted for), through pressing and releasing key five as the second digit of the code, triggers the second monostable, $\mathrm{IC}_{2 \mathrm{~b}}$. Output Q of this device feeds the D input of latch $\mathrm{IC}_{12}$ and pressing and releasing keys two and six together (third and fourth digits of the code) latches $\mathrm{IC}_{1 \mathrm{l}} \mathrm{Q}$ output high. Output $\bar{Q}$ of $\mathrm{IC}_{\mathrm{la}}$, being low, inhibits firing of the s.c.r., even after 11s, and the critical phase is over.
The spare second half of $\mathrm{IC}_{1}$ serves as an output latch, the Q output of which energises a t.t.I. relay on whose normallyopen contacts switch a further higher power 9 V relay with normally open and normally closed contacts. The normallyclosed contact releases the

s iort across the ignition contact breaker and the normally-open contact offers a holding path for the 9 V relay, even during reset.

If the correct code is not entered during the 11 s period the ear horn is switched on. Turning off the ignition key does not stop the aiarm but pressing the horn button will Eomentarily inhibit conduction of the s.c.r.

Use of the secondary 9 V relay ensures reliability, even if the tattery is not fully charged and curing the voltage dip caused by starting the engine. Supply cecoupling is not critical as the eircuit only needs to operate properly while the car is stationary. Malfunctions eccurring while the engine is funning are of no consequence ance the 9 V relay only acts Fhen the ignition key is switched off.
G. Varkey Cochin Naval Base
Kerala Endia

- Or a driver uncer the influence of Eleohol - Ed.



## Easy to read hexadecimal display

One seven-segment display made to display hexadecimal values is awkward to read. Devices for displaying binaryencoded decimal values don't respond to binary itputs above 1001 but this circui: display a hexadecimal value in decimal form and its most-sgnificant decoder is capable of reading between zero and five. Hr De Smet Willy (onlug Zomergem
Belgiura


## One led shows six modes

In my application, this multivibrator driving a 'threecolour' led is used to indicate one of six modes of a cassette recorder but it can be applied to any situation where d.c. switching is used.

Supply to the multivibrator is split so that oscillation at about 1 Hz only occurs when the pause switch is on. The six modes indicated are

- led off, deck disengaged - green, running (play or wind) auto-stop effective
- red, record mode - yellow, running (record) auto-stop effective red/yellow, pause (record) - red/green, pause (play) if winding auto-stop ineffective Comections shown are for the Hart Electronics VFL910 cassette deck. In other applications, green and yellow could be made to flash alternately. J.E. Wilson Leicester


## Differential line driver replaces transformer

There are many applications where balanced line driving is required, particularly in audio applications where noise is a problem. Transfonners are expensive and suffer from limited bandwidth and stray magnetic fields so this circuit uses opamps
Output maintains constant amplitude even if one balanced line is earthed, thus simulating
true transformer attion. Devices chosen have low noise figures and can drive 6008 lines directly. The circut has a 22 dB overload margin wien driving 0 dBm into $600 \Omega$, and greater than 60 kHz full power bandwidth can be achieved by sareful choice of resistos. S. Whitt Ipswich Suffolk


## by P.N.C. Hill

*The interactive control program can be obtained from this office. Please enclose a large, stamped and addressed envelope and mark your letter 'XY Plotter'.

Fig.1. Simplified 400 step sequencer for controlling stepper motors.

Fig.2. 200 step sequencer for controlling stepper motors with reduced resolution.

## More on the XYplotter

## Further constructional information and some software

Since the publication of the previous article on the construction of an $\mathrm{X} / \mathrm{Y}$ plotter in the January edition of WW, I have received a number of enquiries requesting further information. Although the first article was intended only to introduce readers to the idea of constructing such a precision instrument, I hope that this will further encourage any would-be constructors.
The software required to drive the plotter has also been expanded and re-written in Pascal on a CP/M-based system similar to the SC84 computer


Output to motor driver stages


Output sequence $A B, B C, C D, D A$
described in this journal. An interactive control program has been developed to allow diagrams to be drawn from a stock of predefined shapes and then annotated with a wide selection of type sizes and founts*. A graph/histogram package also links a digital filter design program to the plotter so that frequency response and impulse plots can be drawn and labelled.

## Driver circuits

The driver circuit described originally might seem excessively complicated to those who wish only to experiment with stepper motors. As mentioned previously the aim was to achieve 400 step-per-revolution accuracy from the motors. To achieve this from the four-pole motors, the sequence of excitation must be as follows:

## $\mathrm{A}, \mathrm{AB}, \mathrm{B}, \mathrm{BC}, \mathrm{C}, \mathrm{CD}, \mathrm{D}, \mathrm{DA}, \mathrm{A} \ldots$

The rom sequencer described previously performs this operation for both motors therefore keeping chip count and interconnections to a minimum. However, those constructors unable to gain access to a prom programmer may prefer a more traditional approach in generating the necessary control signals for either 200 or 400 steps.
400 step sequencer. The circuit in Fig. 1 may be used to drive a single stepper motor. The 74LS193 counter maintains the current position in the 8 stage sequence: only the top 3 bits are used for this, the fourth bit being left disconnected. The counter's up/down input serves as a motor direction control, so that pulsing the counter steps the motor forwards or backwards. The binary outputs from the counter are decoded by the Nand gates. The four final outputs marked $\mathrm{A}, \mathrm{B}, \mathrm{C}, \mathrm{D}$ are connected to the power driver circuits, which in turn provide current to the stepper motor windings.

200 step sequencer circuit. A much simpler circuit can be used to generate the sequence: $\mathrm{AB}, \mathrm{BC}, \mathrm{CD}, \mathrm{DA}, \mathrm{AB} . .$. Although this halves the number of steps per revolution, useful results can still be obtained: the circuit is shown in Fig. 2. Only two D-type flip-flops and two ex-Or gates are required for each motor - the motor direction is controlled by the state of the external input to the ex-Or gates. This simply inverts the feedback around the counter formed from the two flipflops. The step pulse to increment the motor position is applied simultaneously to both clock inputs on the 74LS74 flipflop.

Either of the above methods can be used to generate the logic sequence used to drive the current in the motor windings. The power driving section simply hooks onto the output of the sequencer. The exact design of the driver naturally depends on the type of stepper motors used and also on the loading conditions of the motors.
Driver stages. Various factors must be considered when designing the driver stages. A typical scheme which might be used is as follows. The selected winding(s) is driven hard on as the sequencer output changes to the next state. The rotor accelerates towards its next position, drive current being then reduced to maintain the new rotor position and prevent overshoot. If the motor is driving a predominantly inertial load, then this maintenance current may be only slightly (if at all) lower than the main pulse current. However, if the load is frictional, then the holding current need be only very small; indeed, it may not be required at all, so reducing the power dissipation considerably. In the case of the plotter the motors drive a load which is part frictional and part inertial: a compromise is thus sought. A maximum of 200 mA per phase for the pulse current with a holding cur-

rent of approx 40 mA is found suitable for the $12 / 24 \mathrm{~V}$ motors obtained from Stewarts of Reading. These figures are not critical and optimum choice will depend on the mechanical construction of the plotter.
Motor controller circuit. The common-collector driver circuit originally proposed is ideal for providing the two levels of current in a controlled manner. The fact that the initial circuit was designed for use with several types of motor and transducer contributes to its relative complexity. An alternative (effective) method of driving the motor is shown in Fig. 3. If the discrete transistors are too bulky then transistor arrays can be used, provided the maximum current ratings are observed. Little or no heat sinking is required for the driver transistors if they are used only in the fully on or off states. However, the main regulating transistor will require a heat sink if an appreciable hold current is used.

It is also important to note that the actual measured current in the windings will not be exactly as first predicted due to the very high self inductance and impedance of the wire used in the motors. Large back e.m.fs will
result during the current switching process, most of which the diodes ( $\mathrm{D}_{1}-\mathrm{D}_{4}$ in Fig. 3) dissipate harmlessly. Failure to include the diodes will almost certainly lead to blown output transistors.
The on/off sequence is generated by a circuit similar to that in Fig. 2. In addition, logic for providing manual control from two push-buttons is included to allow movement of the pen independently of the computer. This is particularly useful when removing and re-loading paper. The movement rate is also increased after about one second of manual control to speed up the movement of the pen over longer distances whilst still allowing accurate positioning at a lower speed.

The circuit is self-contained (with the exception of the power supply) and so needs to be built once for each motor: Fig. 4 shows a photograph of the completed circuit. The component values shown are suitable for the SLOSYN motors mentioned in the previous article. Resistor $\mathrm{R}_{5}$ sets the maintenance or hold current, $\mathrm{R}_{1}$ sets current-pulse length, supply voltage sets pulse current. Best results may be obtained with a supply voltage typically higher than the motor's rated voltage. Heating effects are not a problem

as the current remains at the higher level only when the motor is stepped.

The circuit operation is basically straightforward with the possible exception of the modulation of the clock frequency by the output of the 74LS123 monostable via $R_{4}$. When either button is pressed the mono-stable $\left(\mathrm{M}_{1}\right)$ is triggered, so slowing the clock. After a short-period ( $0.3 \times \mathrm{R}_{2} \times \mathrm{C}_{2}$ seconds) the out put returns high,

Fig.3. 200 step, stepper motor controller and driver.

Fig.4. Photograph of completed stepper motor control module. The manual control push buttons can be seen on the front of the board next to the led indicators. The leds turn off when the corresponding coil is being driven to avoid drawing excessive current from the logic.


Fig.5. Serial interface for graphics plotter.

Fig.6. Photographs showing the completed plotter in more detail. Both stepper motors can be seen attached to the pen carriage and the paper carriage.
so increasing the clock frequency by an amount dependent on $\mathrm{R}_{4}$. The clock base frequency is governed by $\mathrm{R}_{3}$ and $\mathrm{C}_{3}$ which, together with the Schmitt Nand gate, form a gated oscillator.
The 74LS74 flip-flop and the ex-Or gates provide the $\mathrm{AB}, \mathrm{BC}, \mathrm{CD}, \mathrm{DA}$ sequence
required for coil excitation, the direction being controlled by the state of the DR line.
The 74LS157 is used to route the control signals from either the computer or the logic associated with the manual over-ride buttons to the sequence controller. Pressing either button over-rides
the computer - necessary during software development!

## Interfacing to the computer

Any home computer is capable of driving the plotter. Sadly, the interfacing facilities of most of the numerous hobby machines are far from being standard. If parallel outputs are available, then interfacing the plotter is a simple matter. Two wires are required for each motor: direction control and step pulse. One additional wire is required for each pen for the lift control, giving a total of six wires for a two-pen plotter, plus the common earth line (of which several are recommended for safety's sake). One standard eight-bit output port is thus sufficient. Additional input lines can be added for feedback for finding the paper origin etc.

This form of parallel interface requires a multi-core cable and cumbersome connectors, and offers no protection to the microprocessor from external nasties such as 25 V lines etc. An alternative approach is to use a serial interface driven from the RS232/ IEEE 425/426 output of the computer. This is portable and flexible and only requires two wires! Many uart chips are available which perform the basic shifting and timing operations necessary for an asynchronous receiver. The AY 3-1015-D is one such device particularly useful in this application as it can operate without a host microprocessor. It requires a stable clock at a frequency of 16 times the baud rate. This is generated from a crystalcontrolled oscillator and divider circuit. Baud rates of above 4800 are advisable to allow a reasonable plotting speed to be achieved. Figure 5 shows a suitable interface circuit.



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The incoming serial data is conditioned and buffered and fed into the RRD input of the device, the parallel output appearing on pins $26-33$. The DR signal on pin 19 indicates a received byte of data and is reset by pulsing low the DRR input on pin 18.
Although the circuit has not been used with the plotter it has been successfully used with other projects. The gates connected to the outputs of the AY 3-1015-D simply route the received data strobe signal to either (or both) of the sequencer circuits used. If the corresponding output bit is set to one then the sequencer is stepped once. If the bit is left low then the sequencer remains in its previous state. eg: Sending 01 H steps the X motor once in one direction whilst 03 H steps it in the opposite direction.

## Improvements to the mechanics

Some simple changes have been made to the mechanics which have improved the performance of the plotter. It was initially suggested that button-hole thread was suitable for the traction wires along which the motors run. This does work extremely well; however, plastic coated stranded wire produces better results as it can be tensioned to a greater extent. A source of suitable wire is a fishing tackle shop as it is sold for use in attaching to large hooks to prevent even larger fish from biting through the line. Various thicknesses are available but 40 lb seems the most suitable.
A second improvement is to the pulleys that run along the wire. One pulley is fitted to each of the two motors which are in turn fitted to the pen carriage and to the plotter bed (or paper carriage). These should ideally be made of Brass rather than aluminium alloy, since the latter seems to promote excessive wear on the wire. The pulley diameter should be as small as possible to give the best resolution and smoothest performance of the plotter.

Finally and most importantly, the width of the flat area of the pulley on which the wire runs (see Fig. 13 of original article) must be sufficiently great just to prevent the wire from winding to the ends. This is best done by calculating the number of pulley revolutions that must be made for the appropriate carriage to move from one end of its travel to the other. The pulley width must
therefore be greater than this number times the thickness of the wire. In the case of the 401 b wire the outside diameter is approximately 0.8 mm . A pulley of, say, 7.0 mm diameter requires 14 revolutions to move the 21.2 cm of a sheet of A4 paper, which means that a pulley width of 11.2 mm is required. If the width is smaller than this then the plotter functions satisfactorily except for some long-term slippage.
The completed plotter is shown in the photographs in Fig. 6. Both stepper motors are clearly visible, as are the taut wires along which they run.

## Controlling soltware

Once the plotter has been constructed and the electronics built and then interfaced to the computer, the software development can begin. This may, at first glance, appear to be a simple task, but there are a number of problems which can result if a few precautions are not taken.
The first rule is not to use float-ing-point arithmetic for the main line and curve drawing routines, since the inevitable truncation errors lead to inaccuracies which accumulate, as all pen positioning is naturally done relative to the previous pen position. If a positioning error occurs at any point the error remains from then on. These slight error may not be noticable on small diagrams but on larger, more complex drawings the end may not join up with the start.
For simplicity in the description of the software, the pen is assumed to move in both the X and $Y$ direction over the paper. In practice this is not the case as the paper actually moves in the $Y$ direction.
Straight lines. This is the most elementary routine (apart from pen up and pen down). We may specify the endpoint of the line on a cartesian coordinate system, the origin of which is previously defined at a suitable point on the paper. The X and Y coordinates of the endpoint are passed to the routine which then moves the pen from its current position (wherever that is) directly to the endpoint. If the pen is up then no line is actually drawn. If the pen is down then a straight line is drawn on the paper.
It is tempting to perform this function by calculating the gradient of the proposed line. A deci-

mal number is obtained. For each step the pen is moved in the X direction it must be moved by this amount in the $Y$ direction. Obviously the pen can only move in whole steps, so at each point one must calculate the exact position using floating point arithmetic and then move the pen to the nearest actual location. When the end of the line is reached any small error present must be corrected or else it will propagate. The problems mentioned are only the beginning, since we also have to cater for vertical line without a division by zero error in the calculation of the gradient. Doubtless it would be possible to devise routines to perform all these checks, but if they were written in anything such as Basic it would probably take serveral minutes to draw each line. Luckily, an alternative exists.

A glance at Fig. 7 highlights the problem. The central line shows the ideal (unobtainable) solution: The staircase route for the pen is the best approximation. At every point there are two possible directions in which the pen can next be moved: one step in the X direction or one step in the $Y$ direction (the movement is either positive or negative on each axis but this is constant for any given line and so need not be determined at each step). We simply wish to determine which of the two options leads to the pen being positoned closest to the ideal line. If we calculate the distance that each of the two solutions would place the pen from the ideal line we can then choose the best of two.

## Fig.7. Drawing straight lines.

## Fig.8. Calculation of

 perpendicular distance of point from the required line.The calculation of the deviation from the ideal line is extremely simple, as Mr Griffiths pointed out in his letter published in the February edition of $E \& W W$, and which is illustrated in Fig. 8. Thinking in terms of programming we may call coordinates of the endpoint of the line (X DEST, Y DEST) and the current pen position (XT,YT), both relative to the starting point of the line. The coordinates at which the pen would be positioned after the next step has been issued are calculated for each of the two possible next moves. These would be (XT $+1, \mathrm{YT}$ ) and (XT,YT+1), the ' 1 ' referring to the one step moved in either of the two directions. We then compare deviations from the ideal line obtained by both solutions, adopting the solution with the smallest deviation. The implementation of this is further simplified as the value of ' 1 ' (the line length) is constant and so need not be included in the comparison. The test may be implemented as follows:

## IF ABSIXT*Y DEST

$-(\mathrm{YT}+1) * \mathrm{X}$ DEST]
$<\mathrm{ABS}(\mathrm{XT}+1)^{*} \mathrm{Y}$ DEST]

- YT*X_DEST]

THEN MOVE IN Y DIRECTION ELSE MOVE IN X DIRECTION
ABS indicates absolute value.
*University College, London


Fig.1. Elemental CR circuit produces derivative action, used to obtain rate information

# Digital derivative feedback at l.f. 

## In a proportional-plus-derivative servo system in a model ship, a hybrid digital-analogue circuit gave better results in extracting the rate signal that the more usual lead/lag RC network.

It is well known that a resistance capacitor network such as that shown in Fig. 1 will produce derivative action on any signal injected into it. This effect can be used to produce a 'rate' signal in servo mechanisms, but it has two serious defects. The first of these is that the degree of phase shift depends very much on the frequency being differentiated. The signal emerging from the network, will be a combination of a proportional signal and a derivative signal at $+90^{\circ}$ to it. The ratio of these two determines the phase advance, which unfortunately decreases with decreasing frequency. The second drawback is the size of capacity needed at low frequencies, say 1 Hz , to produce moderate derivative action. The phase angle of the emerging signal depends upon the ratio of the impedances of the resistance
and the capacity. The impedance of the resistance can be taken as the resistance; the impedance of the capacitance $X_{c}$, is

$$
\mathrm{X}_{\mathrm{c}}=1 / 2 \pi \mathrm{fC} \text { ohms }
$$

for a frequency in hertz and capacitance in farads. The phase angle is then
$\tan ^{-1} \mathrm{X}_{\mathrm{K}} / \mathrm{X}_{\mathrm{c}}$ for $\mathrm{X}_{\mathrm{R}}$ and $\mathrm{X}_{\mathrm{c}}$ inohms.
The difficulty with the system is the size and quality of the capacitance needed to produce a reasonable phase shift at low frequencies. In Fig. l the capacitance shown is assumed perfect, with no leakage resistance or self inductance, and this can only be approached in practice with very careful layout and high quality, expensive and bulky capacitors. Suppose, as in this case, we are
working with a model ship, and wish to consider frequencies in the response region of yaw motion, say 0.5 Hz . With careful shielding and f.e.t. amplifiers, $\mathrm{X}_{\mathrm{r}}$ could be made as high as $10 \mathrm{M} \Omega$, so that to produce a lead angle of $85^{\circ}$, the capacitance need, assuming no loss, is $0.36 \mu \mathrm{~F}$. This is obviously undesirable and a more reasonable value might be $0.01 \mu \mathrm{~F}$, which gives a lead angle of only $17^{\circ}$ and has an impedance of $32 \mathrm{M} \Omega$ at this frequency.

## Direct rate measurement

It was felt that this problem could be more easily overcome by measuring the rate of change of the incoming gyroscope signal directly. This can be done with the gyro signal charging a capacitance which is discharged at a fixed


Fig.2. Circuit diagram of digital proportional-plusderivative servo.
rate by resistance, but this is really the same as the phase lead network. The solution was to sample the incoming gyroscope signal at fixed small intervals and take the output derivative signal as the consecutive difference between two consecutive samples. This gives a derivative signal which easily works at low frequencies and within noise limits, improves with decreasing frequency.

The incoming gyro signal can be sampled with a 'sample and hold' circuit, which can be obtained as a single i.c., and one would need a minimum of two samples to produce a rate signal for a given time interval. The rate signal is obtained by making the two samples the input to a high gain differential amplifier.

In practice, sample and hold circuits suffer two distinct lags, hold settling time, and acquisition time. The first of these is the time required for the output to settle after a 'hold' has been given. When measuring small differences between consecutive samples this hold error could introduce magnified errors in the output, but for the sample and hold circuits used the hold settling time is of the order of 1 microsecond, and for the frequencies being considered the error can be ignored. The acquisition time is much longer, of the order of 20 microseconds for the capacitor needed at the sample holding times used in this instance, and
prohibits the use of only two sample circuits. The electronics with two circuits is also less straightforward, since each sampling circuit must hold for one clock cycle, sample for sufficient time to acquire the new signal level, and then switch back to hold. During the sample period no rate output is available and one also has to switch both sample circuit outputs between the differential amplifier inputs every other clock cycle.

A better solution is to use three sample and hold circuits, switched so that whilst two are producing the required output the third is acquiring the new signal level prior to switching to the hold mode. For three holding circuits AB and C the required sequence of differences for a continuous positive rate output is:

$$
\begin{aligned}
& \mathrm{B}-\mathrm{A} \\
& \mathrm{C}-\mathrm{B} \\
& \mathrm{~A}-\mathrm{C} \\
& \mathrm{~B}-\mathrm{A}
\end{aligned}
$$

etc.

With samples produced at the correct time from a clock signal, the above may be produced from a dual-channel, analogue demultiplexer driven at the same clock frequency. This would feed the two samples required from the hold circuits to the differential amplifier, and thus produce the rate signal. From the sequence above it can further be seen that each held sample is required for

two thirds of the time, so that the sample and hold needs to be driven by a clock signal which is assymmetric. Furthermore, the clock signals to the three sample circuits should be $120^{\circ}$ out of phase with each other.

The outcome of these requirements is the circuit diagram in Fig. 2. This was built from medium and low specification components with a clock rate of 75 Hz , and will produce a derivative signal from a 2 V source from 0.01 Hz to 10 Hz . The phase angle at 0.01 Hz is approximately $89.4^{\circ}$, and the largest capacitors are $1 \mu \mathrm{~F}$, these being low voltage types in the smoothing/lead circuit.

The clock is a conventional 555 timer in a stable mode which produces the 75 Hz square wave. This is then fed to the 74107, which is an assymmetric divide by three, producing two of the driving clocks needed directly. The third clocking signal is produced by 'anding' the comple-

Fig.3. System used to provide velocity feedback.



Fig. 4. Effect of differentiation network at higher input frequencies. At (a), input is one-twentieth of clock frequency, giving little distortion. Higher input frequency at (b) increases distortion.
ments of the two flip-flop outputs in a 7400 Nand gate. The three other gates on the quad 7400 produce inversions of the clock signals, used to help produce the a.c. zeroing of the sample and hold circuits. The latter are LF398Ns, which have a comparatively low specification in terms of a.c. noise, d.c. offset, hold setting time, droop rate, and other important parameters of sample circuits. The signal to be sampled is buffered by an inverting 741, feeding a common signal line. The sampled outputs are fed to three pairs of gates of a DG529, which is a dual quad analogue multiplexer. This is a latching version, but this facility was not needed. The addressing of the 529 which goes $00,10,01,00$ etc. is taken directly from the clock signals driving the second and third sample and hold circuits, and the output feeds the two inputs of a 725 N differential amplifier with a gain of 5.5 . The output at this stage from a pure sinusoidal input will be a stepped sine wave leading in phase by approximately $90^{\circ}$. To reduce instability problems and provide a smoothed output to the autopilot summing function, the signal then passes through a high frequency lag network (with gain) which produces a $90^{\circ}$ phase lag at 16 Hz , thus cancelling the derivative feedback above this frequency. The only other item is the unity gain buffer amplifier and the
autopilot summing function. The former prevents direct feedback around the differential circuit, and the high frequency oscillation that this would cause.

## Circuit performance

The differential amplifier was tested in isolation and produced excellent results. At this stage the lag network of the final circuit was not used, and a four-pole Butterworth low-pass filter with a breakpoint of 16 Hz was used to attenuate high frequencies and provide a smooth output.

Although not strictly necessary, the differential circuit was then used to provide velocity feedback in the closed-loop rudder circuit, as in Fig. 3. It was then found that at even moderate derivative gains the rudder circuit became unstable, and the rudder, which was driven by a low-inertia motor had a tendency to oscillate at about 16 Hz . Various modifications were tried, including lowering the frequency first to 10 Hz and then to 2 Hz , and the use of Bessel rather than Butterworth filters, but none of these proved to be a solution to the problem. The reason for this is that although these active filters give a rapid attenuation of higher frequencies, they also produce large changes in phase in the region of the filter breakpoint, so that along with the differentiator an overall phase lead of $180^{\circ}$ could easily be produced, adding to instability. At this point the active filters were discarded and the phase-lag network used in the final autopilot was introduced.

This network very largely eliminated the instability problem, since it had no inductive action, and produced only a phase lag. The derivative gain could be increased by a factor of about 8 before the onset of instability, compared to the active filter circuits. However, it was found impossible to prevent oscillation of the rudder servo at high derivative gains, despite adjustment of the phase lag network. The answer to this problem lies in the behaviour of the differentiating network as the frequency going through the circuit approaches the clock frequency of the circuit itself. The differentiating effect is shown in Fig. 4(a) and 4(b) for low and high frequencies. In 4(a) the input frequency is $1 / 20$ of the clock frequency, and it can be seen that the resulting signal is a sine wave with very little distortion which leads by a phase angle
of $90^{\circ}$ minus half the switching time of the clock. The higher input frequency in 4(b) at about $1 / 5$ of the clock frequency, shows considerable distortion and harmonic content, and the phase lead, although on average being $90^{\circ}$ minus half the switching time, varies along the wave train.

From this it is apparent that the differentiality circuit will always produce some harmonic distortion. The constantly changing phase at higher frequencies made it impossible to prevent the oscillation with the simple RC lag described above, and the harmonics produced would maintain oscillation in the absence of sufficient attenuation.

Having examined these waveforms it was then found that the problem could be solved by increasing the clock frequency, thus decreasing the phase distortion and harmonic content of the rudder servo signals to an extent sufficient to prevent oscillation. The limit to which the clock frequency can be increased depends on the noise in the system. With increasing clock frequency, the differential amplifier is amplifying smaller differences in the incoming signals, and will reach a point where the noise in the circuit becomes comparable with the difference in voltage between two consecutive samples. The most important sources of noise in the circuit are d.c. voltage offset in the sample and hold circuits, a.c. feedthrough of the switching signal in the sample and hold circuits and in the multiplexing switch, and noise in the multiplexing switch differential amplifier. The upper limit on input frequency for a given clock frequency will be governed by the degree of frequency and phase distortion that can be accepted. The circuit is actually measuring input voltage slew rate so it must be remembered that for a real circuit there will be a maximum permissable sinusoidal input voltage, at a given frequency beyond which the circuit limits and 'chops' the output.

In practical terms, the results gained from a prototype were very good. This was built using low-cost components with relatively poor electrical characteristics compared with what can be obtained. The prototype was built without a specific printed circuit board and made as compact as possible, and in fact is contained on a single board 10 cm by 10 cm . It will, however, deliver a usable
rate output for input frequencies from 0.01 Hz to 10 Hz , i.e. over a frequency range of $1000: 1$; which is far better than would be achieved with an RC phase lead network. For nonsinusoidal input signals there are further advantages because the circuit does not contain any fixed time constants that are comparable to the frequency that is being differentiated. This is particularly so for a square wave input for which the convention RC circuit gives very poor performance.

If the square wave shown in Fig. 5(a) is put through an RC lead network the output will have the form shown in 5(b), in which the initial spike decays exponentially. The rate of decay depends on the time constant of the circuit and can be increased by making the product of R and C in the lead network smaller. Unfortunately the impedance of the capacitance increases as capacitance decreases, so that this also leads to greater attenuation of the output and a higher proportion of noise in the amplified output. Decreasing the value of $R$ has the same effect. Figure 5(c) shows the output that is obtained taken before the high frequency attenuator in this case, consist of short alternating pulses of 13 milliseconds duration with no tail off. Because there is no synchronization it will sometimes occur that the circuit samples the input during a transition, in which case the pulse will be slightly longer.

A further benefit of this principle of operation is that there is no lower frequency limit, although a change in technique would be called for for frequencies in and below the cycles per hour range. This is because the sample and
hold circuits suffer from voltage droop. The sampled voltage in the sample and hold circuit is held as charge in the holding capacity, and the voltage on it monitored by a buffer amplifier to produce the 'hold' output. The buffer amplifier has a high, but still finite, input impedance, so that the charge in the holdiing capacity will gradually drain away, resulting in a decay or 'droop' of the buffer output. The decay rate obviously depends upon the buffer input impedance and the capacitance size, so that the latter, which is external to the i.c., is chosen according to the sample frequency and hold time expected. The present prototype uses $0.01 \mu \mathrm{~F}$ polycarbonate capacities rated at 250 volt and has a hold time of 26 milliseconds, so there is scope with this analogue system for extending the frequency range downwards by increasing the size of the holding capacitances.

However, a point would be reached where this became impractical, and the solution then is to change over to a latching digital holding circuit. The incoming signal is still sampled with a sample and hold circuit, but the output of this is then fed to an a-to-d convertor. The hold time is thus reduced to the conversion time of the a-to-d which can be very short. Each output line of the converter then feeds a binary latch, thus holding the digital signal indefinitely. The latch is sampled by d-to-a convertors to reproduce the original analogue voltage, and the remainder of the circuit would be unchanged. System clocking frequencies may also be extended downwards by frequency divi-

sion, and there is thus no lower limit to the frequency that may be differentiated.

In the this low-frequency range the system may be compared to differentiation using microprocessors. The results should be very similar in terms of quality of the output signal, and in fact the digital form of this circuit described above is actually a microprocessor with a single programme 'hardwired' into it. A conventional microprocessor, Z80 for example, would be more easily adapted to changing requirements, but would be considerably more expensive. For the frequency range in which the prototype operates, similar considerations apply, with the added provison that the analogue solution will inevitably give a more accurate signal with less noise, then a microprocessor equivalent. The added complexity of the microprocessor and the act that the input and output circuits operate with discrete voltage levels can only degrade the quality of the final output signal.

Fig.5. Ouput of circuit (c) compared with simple CR network.

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## CARETAKER FOR THE BBC

The Caretaker rom for the BBC Micro is a collection of utilities for Basic programmers. Among these are a search-and-replace facility for editing programs, commands for merging programs and for loading or saving individual sections, a 'bad program' recovery routine and a very comprehensive compactor for saving memory space.
One novel feature offered by the package is single-key is not a strictly accurate description of the process since you have to press the tab key at the same time. A set of stickers is supplied to indicate which key does what. This facility (which
can be switched off) may not save much typing time, but should certainly prove an attraction to refugees from the ZX Spectrum.
Several routines in Caretaker parallel those of Beebugsoft's excellent Toolkit rom: some of them are less easy to use, though they allow rather more flexibility. A useful 'help' page is available to remind the user of the commands and their syntax.

Caretaker comes in an 8 K eprom and costs $£ 133 \cdot 35$ Computer Concepts, Gaddesden Place, Hemel Hempstead, Hertfordshire HP2 6EX, tel 0442-63933.

EWW 205

## MICRO TRACKER BALL

Developed from their tracker balls used in military and airtraffic control applications, Marconi have produced a tracker ball control for use with microcomputers. There are advantages of such a device compared with joysticks or mice'; they offer more accurate positioning of the cursor than joysticks and do not require the table-top space needed for mice.

The two-inch diameter ball is in non-slip contact with two shafts accurately positioned at $90^{\circ}$ to each other. Each shaft is fitted with a slotted disc; light source and photodetectors measure the rotation of the disc and electronic circuitry translates these into X and Y coordinates. The resin ball rotates freely in any direction and its ease of use makes it suitable for long sessions. With the appropriate software the RB2, as it is called, is capable of moving a cursor one pixel at a time on a computer graphics screen while also being able to

move and change direction very rapidly.

The first issue of the tracker ball has been aimed at users of the BBC micro and is supplied with utilities software. This allows it to replace the cursor keys or be used instead of a joystick. The software allows the user to define the function of the three keys built into the tracker ball housing. Further software in preparation include c.a.d. and graphics programs. Computers with an RS232 input can also use the RB2 by the addition of an interface card fitted inside the control's housing at extra cost. Prices vary according to the distributor but are about $£ 50$. MEDL Power Division, Carholme Road, Lincoln LN1 1SG. EWW 206

## FALSE COLOUR SATELLITE WEATHER MAPS

A complete system for receiving Meteosat pictures, which includes everything from the antenna to a colour monitor, is produced by Microwave Modules. The processed image as displayed by the MMS 1690 system is received in segments; 24 in the case of visible pictures and 9 for infra-red. Each image is provided with a caption which gives date, time (GMT), type of image and segment code. Meteosat transmissions include
outlines of the countries, which may be concealed by clouds, and coordinate points at 10 degree spacings. Although Meteorat is stationed above the equator and can monitor about one third of the Earth's surface, as a European satellite it gives performance to the images covering Europe which are updated at least twice an hour.

The receiver is equally suitable for other geosynchronous weather satellites spaced around the

World. With the addition of a suitable antenna, the NOAA and Meteor satellite signals at 137 MHz may be received. However the images may only be captured during their brief fly-past periods unlike Meteosat, which provides continuous data.

The system consists of an easy-to-assemble antenna kit, suitable for mounting on a vertical mast; a 1691 MHz GaAs fet preamplifier which is weather-tight and may be

mounted close to the antenna ensuring the optimum signal strength and picture quality; an s.h.f. down-converter to 137.5 MHz ; the main sixchannel receiver which incorporates the power supply; and a digital scan converter which translates the signals into pictures and can also convert the monochrome images into colour by assigning colour values to different bands of the 64 grey levels obtained. Sequences of weather images can be retained inexpensively by recording the signal onto a good quality cassette recorder. A library of images can be built and replayed through the scan converter to simulate a timelapsed sequence. The system is completed by a colour monitor. Component parts may be purchased separately. The complete system costs $£ 1738$, excluding tax, claimed to be $50 \%$ cheaper than any similar system. Microwave Modules Ltd, Brookfield Drive, Aintree, Liverpool L9 7AN. EWW 207

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use of front panel controls Specific areas of a program may be explored by the four-level triggering system and the timing delays which can count up to 50 thousand clock cycles to pinpoint a specific action. The instrument is operated through menu selection on the screen and multi-function keys. For example it is even possible to recall the colour coding of the probe leads and thus save a lot of time referring to the instrument's manual. State displays may be chosen to give the information in binary decimal, octal, hexadecimal or ASCII.

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which, according to Philips, is well below the cost of rival instruments. It is also available for hire from Livingstone Hire and from Instrumental Rentals. Pye Unicam Ltd, York Street, Cambridge CB1 2PX.

EWW 208

## MONOLITHIC PREAMPLIFIER

An ultra-low-noise monolithic preamplifier circuit is particularly suited to microphone signal processing, says CMS who are marketing the SSM 2015 from Solid State Micro Technology. The very low voltage noise performance is enhanced by a programmable input stage which allows overall noise to be optimized for source impedances up to $4 \mathrm{k} \Omega$. The circuit has a bandwidth of 700 kHz at a gain of 100 with symmetical slew rates of $6 \mathrm{~V} / \mathrm{us}$ and distortion of $0.007 \%$. True differential inputs and a high common-mode rejection of 100 dB provide easy interfacing to balanced micropphone outputs, tape heads and singleended devices. The SSM2015 is specified for commercial temperature ranges only and has a one-off price of $£ 9.48$. CMS (Distribution) Ltd, 26 Pamber Heath Road, Pamber Heath, Basingstoke, Hants RG26 6TG.
EWW 209

## COLOUR GRAPHICS

A high resolution graphics controller combines many of the features usually found on much larger systems into a single Eurocard. It has been specifically designed for industrial environments where the engineer needs reliability, speed, and easy software implementation. The board is based around the Thompson EF9366 grraphics processor i.c. and is especially suitable for process control where the ability to display text mixed from a variety of sizes and colours, combined with graphic symbols makes it easy to represent a diverse range of
industrial processes. The board is compatible with the Acorm bus and includes full address decoding so that it occupies only 32 bytes of the host computer's memory. It includes 64 K of onboard graphic memory. Eight colours with inverse and flashing are available on a screen image 512 by 256 elements with a resolution of 5 to 8 dots. In black and white, 16 levels of grey are available. An internal rom includees a full set of 96

ASCII characters and the highdensity text mode enables the use of 32 rows of 85 characters. Character size and style, including italics are all fully user programmable and characters may be scaled independantly in X and Y directions by up to 16 times. Price? £299. Cambridge Microprocessor Systems Ltd, 44a Hobson Street, Cambridge, CB1 1NL..
EWW 210

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Rapid curing is claimed for the structrual adhesives developed by Loctite for the encapsulation of electronic components. The one-part adhesives are cured by exposure to medium or high intensity u.v. light in a matter of seconds. Loctite estimate that in such operations as potting relay switches there is a saying of $26 \%$ when compared with epoxy compounds. In addition the u.v. adhesives use no solvents and have an indefinite pot life, making them highly suitable for automation.

A secondary system for curing ensures that even the parts that the u.v. light does not reach are cured by the anaerobic action of the adhesive. Different formulations of the adhesive are suitable for different applications: for example UV365 is recommended for smaller component potting, sealing and encapsulation. It has a medium viscosity and when cured can withstand temperatures from -55 to $175^{\circ} \mathrm{C}$, and is resistant to most kinds of chemical enviroments, retaining full strength under the most stringent of tests. Other adhesives in the series vary
chiefly in their viscosities, making them more suitable for different applications. The lowviscosity UV371 has good penetration properties and is therefore good for coating and shallow potting. Loctite UK, Watchmead, Welwyn Garden City, Herts AL7 1JB. EWW 211


## MULTI-TASKING MICRO

The addition of one rom to a
As it stands, the Multi-Forth
BBC microcomputer can change it into quite a different computer; one that runs Forth and is capable of executing several different Forth applications at the same time. Multi-Forth 83 is a 16 K eprom which may be plugged into a spare 'sideways' rom socket within the computer. Depending on its position, it can be given a higher priority than the Basic rom and thus be controlled from Forth with Basic as an addition. The latest version of Forth, Forth 83, is used. This is an improved version of Forth 79 with many additions to the resident dictionary of command words*. In addition, Multi-Forth has been tailored to suit the $B B C$ and has therefore translated many of the Basic commands, for example the graphics and sound commands, into Forth. Many of the operating system commands have been incorporated and all the 'star' commands may be used from within Forth.

Especially suited for control and monitoring applications, multi-tasking enables the user to run a number of programs simultaneously, transparent to each other. Each task is placed in a queue and the total length of the queue can be up to 28 programs. A task has its own 32 -bit clock and can be scheduled to execute at a pre-determined time, controlled directly by the user, or controlled from within other tasks, forming an interactive interdependant suite of programs for such applications as robotics. At the simple level, printer output can be defined as a task which, when run leaves the computer free for normal use. Disc and tape can be run at the same time on different files; very useful in data logging. Up to four files may be open to access at a time and block files greater than 32 K can be maintained on disc.

The system is vectored and features may be redefined or the whole system may be re-configured by redirecting the vectors. In addition, there is a standard 6502 assembler for machine-code definitions and a standard Forth screen editor which is enhanced to allow the use of the cursor keys for full-screen editing.
rom, along with full fitting instructions and an impressive 170 -page manual costs $£ 40$. Despite using the 16 K rom as economically as possible, David Husband, who developed the system, had to leave some of the refinements out.
A $£ 40$ disc added to the rom program provides a de-luxe version which contains many more source-code definitions and a large number of colour and graphics commands. The de-luxe version improves on the already impressive tasking facilities, although the full system is not yet finalized David Husband keeps adding to it. The disc includes a demonstration program which divides the screen in five windows; two are scrolling text independantly, one displays a real-time clock, another an oscilloscope-type display and these all leave the larger window free to enter and run programs exactly as if there were not four other programs running at the same time.

Versions of Forth on rom with multi-tasking facilities are also available for the Electron, The ZX Spectrum, The ZX81 and other computers. Skywave Software, 73 Curzon Street, Boscombe, Bournemouth, BH1 4 PW .
EWW 213

* a full description of the differences between Forth-79 and Forth-83 is in the August edition of Byte.


## MOTOR SPEED CONTROLLER

Designed for the control of electric drills, the ZN411E from Ferranti is equally suitable for the control of a number of a.c. motors including other power tools, and domestic equipment. The controller offers 'soft start' capability, precise control and can work in reverse complete with a built-in shunt regulator, the i.c. comes in an 18 -lead dual in-line package and will operate from the a.c. mains supply, though it can be operated on d.c. power as well. Stocked by Celdis Ltd, 37 Loverock Road, Reading, Berks RG3 1ED.
EWW 214

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## DATURR

In the advertisement published in the November issue the Small Cases described as light grey are now being supplied in blue. The 19in Cases described as having 'prodruding edges' are also available without these, other than those advertised at reduced prices.

## (ii) DATURR 릉ㅇN

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CIRCLE 38 FOR FURTHER DETAILS.

## BBC BASIC EXTENSION

Other utility and language roms for the BBC Micro provide their special facilities through 'star' commands. But Vine Micros' Addcomm rom is different. The new commands it offers are in the form of additional Basic keywords.

Unlike standard Basic words these are not converted into single-byte tokens by the computer, so some care must be taken in using them. But they give many new facilities, notably in the graphics department: there are commands for drawing circles and ellipses, for filling areas with colour and for scaling the screen co-ordinates. In addition there is a selection of Logo
graphics comınands.
Addcomm also incorporates some Basic editing aids, including a 'bad program' rescuer, a compacter, a character definer and a search-and-replace utility. Other commands give the user a string sorting routine, easier handling of text and graphics windows and (though not for purists) the freedom to jump out of For-Next loops without the usual consequences.

The Addcom rom is competitively priced at $£ 28$ including v.a.t. and carriage; it is also available for the Acorn Electron. Vince Micros,
Marshborough, Sandwich, Kent CT13 0PG, tel. 0304812276. EWW 215

## RS232 TO CURRENT-LOOP CONVERTER

A converter to enable noiseimmune communication over long distances with full protocol is available to connect to RS232 parts. It can convert in either direction and so can be used at either or both ends of a communications link. It may be used in half of full duplex and may be operated in passive or active mode; when 'active' the converter supplies the loop
current. The Remark model 65 provides for 20 or 60 mA current loops and can transmit or receive at uup to $9600 \mathrm{bit} / \mathrm{s}$. Supplied with a reference manual the unit costs $£ 100$ or less for bulk buyers. Advanced Technology Maintenance Ltd, 2 Norwich Road, Metropolitan Centre, Greenford, Middlesex UB6 8UB. EWW 216



## SWITCHER I.C.S AND LAYOUTS

Switching power supplies operating at high frequencies can be easily adapted to provide power at specific current and voltage values. Siemens produce four control i.cs for such supplies and have now made it even easier to design them by the provision of card layouts with the position of all components marked. The four circuits, TDA4700/14/16/18,
provide power line-hum suppression, dynamic currentlimiting, over and under-voltage protection, soft start for the supply itself, double-pulse suppression, reference overload protection and the facility for external synchronization. Siemens Ltd, Windmill Road, Sunbury-on-Thames, Middlesex TW16 7HS. EWW 217

## IC LEG STRAIGHTENER

Simply push the integrated circuit through this device and the rollers will position the legs so that they fit i.c. holders or p.c.b. hole patterns. The machine is adjustable and can cater for different leg thicknesses. This particular model can cope with 0.3 and 0.6 in pitch dual in-line devices but others are availableifor 0.4 and 0.9 in pitches. Low cost and handy for small batches or for those who do not have automatic insertion machinery. Other methods of bending the

pins on i.cs are fraught with dangers and difficulties. Hionics Ltd, Lower Quemerford Mill, Calne, Wilts SN11 8JS. EWW 218


## XY PLOTTER

A flat-bed xy plotter has a number of add-on options to make it a versatile instrument. The Parfitt plotter is basically a three-pen plotter that can be instructed by a computer to draw anything from from complex geometric shapes and patterns to graphs and electronic circuit diagrams. It can use any of the three pens during its drawing program to produce colour drawings. The pens can be replaced by a scriber and the plotter can then be used to make scraper board pictures on scribe copper for etching. This in turn can be replaced by a light sensor and the instrument can scan a picture to be displayed on a tv screen. A picture scanned in
this way can be reproduced by the plotter. The light sensor can also be used to negotiate mazes. On top of all this a drill or router can be fitted and used to machine soft materials and drill (e.g.) p.c.bs. These addons come as extras on top of the basic price of $£ 270$, the full kit of plotter, power supply, drill/router, optosensor, scriber and software on disc is $£ 490$. Software on cassette is provided in the lbasicl package. Aimed at schools and colleges, it plugs directly into the user port of a BBC micro and an optional conversion makes it suitagle for the other widely used educational computers, RML 380Zs. Parfitt Electronics Ld, 6 View Road, London N6 4DA. EWW 219

## CRYSTAL CLOCKS

A quartz oscillator is combined with a programmable on-chip c. mos divider chain to produce up to 57 different output frequencies up to the crystal's own frequency. A specific frequency is selected by applying logic levels to the six control pins of the 16 -pin d.i.l. package. A selection of oscillators with crystal frequencies in the range 32.768 kHz to 1 MHz are available as standard and a version without the crystal can
be supplied. For applications requiring a data-rate generator, two versions can be provided with base frequencies of 768 and 96 kHz which can produce rates between 50 and 48000 baud. Low power consumption and small size combine with the elimination of much external circuitry make these oscillator/dividers suitable for many applications. Euroquartz Ltd, 5 Church Street, Crewkerne, Somerset TA18 7HR.

EWW 220


## GAS-FIRED SOLDERING IRON

A Christmas present for the engineer who has everything is this butane-powered portable soldering iron. The Oryx Portasol works on a different principle from other gaspowered irons. There is no flame during operation, the chemical energy in the gas being converted directly into heat by means of a patented catalytic converter in the iron's tip. The rate of conversion is adjustable up to the maximum setting
catch 10 ns pulses and automatically stretch them to 50 ms . It sets its threshold level automatically as a function of the voltage found at the tip and operates from the supply voltage of the circuit being tested, from 4 to 15 V . Optionally an additional adaptor allows the probe to be used from a supply up to 25 V . OK Industries UK Ltd, Dutton Lane, Eastleigh, Hants SO5 4AA.
EWW 221


A pen-sized, digital logic probe that may be used on d.t.l, t.t.l. and mos circuitry comes from OK Industries. The instrument detects high or low logic levels and can indicate if the level is correct or incorrect. It can also detect open circuit. Such a probe can often be used for testing when the only alternative would be an oscilloscope and/or other bulky test equipment. The probe has a frequency range from d.c. to 50 MHz . The PRB- 1 can also

## PSEUDOSTATIC RAM

Low-cost dynamic ram chips can emulate static devices by incorporating refresh counters and nultiplexers within the package. The NEC uPD4168, a 64 K ram arranged as an 8 Kbyte memory, uses the standard 28 . pin package and is pincompatible with static rams,
while internally it refreshes memory at a rate of 128 cycles/ 2 ms . Access time is 200 ns with a cycle time of 330 ns .
Operating from a single supply rail of 5 V , it consumes 330 nW while active and 28 mW on standby. The price is claimed to be $40 \%$ cheaper than the equivalent static ram. Available through Impulse Electronics Ltd, Hammond House, Caterham, Surrey CR3 6XG. EWW 222
which gives the equivalent power to a 60 W electric soldering iron. Tip temperature is adjusted to between 250 and $450^{\circ} \mathrm{C}$. The gas resevoir is filled in much the same way as a gas cigarette lighter, and the pensized tool has an igniter (i.e. flint) incorporated in its cap. Apart from its portability the iron offers the elimination of electrical damage to sensitive devices. Costs $£ 17.90$ inclusive Replacement tips are readily available. Greenwood Electronics, Portman Road, Reading, Berks RG3 1NE. EWW 223

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CARR $\mathbf{E 6 . 0 0}$ Spare paper rolls $\mathbf{£ 4 . 5 0}$ each

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A massive purchase of these desk top these quality 30 cDs printers at a SUPER LOW PMICE against their original cost over £ 1000 Unit comprises of full OWERT electronic keyboard and printer mech with print face similar to correspondence quality ypewriter. Variable forms tractor unit
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 I/O THRMmitatsrromergs + CaR + var I terminal Many features including ASCI keyboard and printer for data 1/O auto data defeci circuinty. RS232 serial interiace. 110 baud. 8 bit paper tape punch and reader for cheap and reliable dat a storage. Supplied in good condition and in working order Options Floor stand $\boldsymbol{E} 12.50+V A T$ KSR33 with 20 ma loop interface $\mathbf{E} 125.00$ Sound proof enclosure $\mathbf{E 2 5 . 0 0}+$ VA

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Im $160 \times 120 \times 350 \mathrm{~mm}$. All outputs are fully regulated and shot circuit proot Supplied in NEW or little used condition. Complete with circuit. $\mathbf{£ 1 5 . 5 0 + £ 2 . 5 0} \mathrm{PP}$ FARNELL 5 Volt 40 amps . Type number G6-40A. This miniature switching PS measures only 160 mid mps" Fuly regulated and smoothed with over voltage protection Etc. 120 or 240 current list price ONIY E/30.00 + E3.00 CARR \& IMS.
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PERIPHERAL SYSTEM SUPPLY. Runs almost any system. Fully cased unit supplied n a brand new or little used condition. Outputs give $5 v @ 11$ amps " $+24 v @ 4$ amps. All outputs are crowbar protected and the 5 volt output is fully regulated. Fan cooled. Supplied tested with circuit $555.00+$ E8.50 CARR.
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[^8]

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* RUNS MS/PC-DOS \& CP/M 86
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## Appointments



## SERVICE INDUSTRIES LIMITED of

Lahore, Pakistan requires for the proposed engineering complex for manufacture of mobile, portable and fixed station communications equipment the services of a Pakistani national post-graduate electronic engineer with at least five years experience. Age less than 35 years. He is expected to advise on the planning, designing and manufacturing of communications equipment. Annual salary will be above Rs. 100,000 along with benefits.
Please send your latest photograph with resume by 15th December 1984 .

SERVICE INDUSTRIES LIMITED<br>80-E/1, Gulberg-3 Lahore-11 (Pakistan)

# TECHNICIANS <br> Advanced Telecommunications Technology 

Technicians at the Foreign and Commonwealth Office have wide-ranging responsibilities and can make full use of comprehensive and unique facilities. There are also good promotion prospects and the opportunity, in some cases of service overseas
We have vacancies at Hanslope Park, (Near Milton Keynes) for technicians to work on the installation, maintenance and other work associated with HF communication equipment, VHF, UHF and microwave links and associated test equipment; teleprinters, telephone subscribers' apparatus,, PMBXs, PAXs, PABXs, and ancillary equipment including that using analogue and digital techniques and voice frequency telegraph.
Applications should normally have 4 years' relevant experience ( 7 years for more senior posts).
To be considered you will need to possess ONC in Engineering, Mechanical Engineering, Electrical Engineering or Electronic Engineering, or an equivalent City and Guilds Certificate, or another equipvalent qualification such as an appropriate TEC or SCOTEC certificate.
Ex-service personnel who have had suitable training and at least 3 years' appropriate service (as Staff Sergeant or equivalent) will also be considered
Salary will rise from a minimum of $£ 6512$ pa to a maximum of $£ 9009$ pa for the Junior Grade and from a minimum $£ 8757$ pa to a maximum of $£ 9998$ pa for the Senior Grade. Starting salary in the Junior Grade may be above minimum for those with additional relevant experience.
Annual leave on entry is 4 weeks 2 days
Relocation assistance may be available
Hours: 42 hours per week
The Foreign and Commonwealth Office is an Equal Opportunities Employer Please apply for an application form, quoting reference TT/06/84, and stating the grade you wish to be considered for.
The closing date is 6 January 1985.
Recruitment Section, Foreign and Commonwealth Office,
Hanslope Park, Hanslope, Milton Keynes MK19 7BH Foreign and Commonwealth Office.

## AUDIO ENGINEERS

Join a small dynamic team where your contribution will be recognised! Audix design and manufacture professional audio communication systems and equipment for radio/T.V. broadcast studio applications. We are involved in a major and successful development programme which is leading to an increasing demand for our products. We are therefore expanding our Project Engineering, Test and Development sections and have a numbeer of challenging vacancies for young self motivated engineers:

## Project Engineering Manager

To manage a small team responsible for ensuring that all major projects are engineered and supplied in accordance with tender specifications. The position involves liaising with customers and production departments, special engineering and occasional supervision of installation and commissioning. Previous experience of professional audio engineering is essential.

## Project Engineer

To be responsible for all engineering aspects of major projects from liaison with customers to equipment commissioning. Visits to installation sites both within the U.K. and overseas will occasionally be necessary. Previous experience of professional audio engineering is essential and it would be advantageous to have some knowledge and experience of microprocessors.

## Senior Test Engineer

Capable of system test of complex microprocessor based mixing consoles and communication systems. Occasional visits to client's studios and commissioning of systems may be necessary.

## Development Engineers

We require both senior and junior engineers who have some digital experience, together with a good appreciation of analogue techniques. Experience with $\mathbf{Z 8 0}$ microprocessors would be an advantage.

## Software Engineer/Trainee

This post would ideally suit a young electronic engineer who has a special interest in software design. Experience with $\mathrm{Z80}$ microprocessor would be an advantage however specialist training courses can be provided

## Development Technician

Required to assist in the design, assembly and test of breadboards and prototype PCBs. The post would suit a young engineer who has previously worked typically as a test or service engineer. From this position there are opportunities for rapid promotion into other departments within the company. For further information on these appointments please telephone Mrs J. Lawson on 079940888 or write enclosing your C.V. to:-

Audix Limited Station Rd., Wendan Saffron Walden Essex

## Appointments

# DODolby 

Dolby Laboratories Inc. manufacture and market Audio Noise Reduction equipment which is used by major recording companies, recording studios, the film industry and broadcasting authorities throughout the world.
Due to increased sales and the introduction of new products we have the following vacancies:-

## Electronic Test Technicans ( $£ 135$ pw NEG) We need people educated to HNC level (or equivalent) with the potential to develope test and fault finding skills (to component level) in a semi-automated test environment.

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For further information contact: Sarah Kennedy, Dolby Labcratories Inc. 346 Clapham Road, London SW9 9AP. 01-720 1111

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INTERESTING WORK
REPAIRING, MANUFACTURING AND DEVELOPING ELECTRONIC FLASH EQUIPMENT FOR A EADING PHOTOGRAPHIC DISTRIBUTOR IN CENTRAL LONDON Applicants should have a knowledge of basic electronics to City \& Guilds standard but experience in this field is not essential. A driving licence would be an advantage., Salary negotiable

Telephone Nigel Fielden on 01.8334737 for an interview. (2768)

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ELECTRONICS TECHNICIAN-
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The post is available with the Particle Physics Research Group, and is funded by an SERC rolling grant for
an initial period of 3 years. The group an initlal period of 3 years. The group international collaborations at CERN, Geneva, Switzerland.
The person appointed should posses at least an HNC and have had previous practical experience in Electronics. He will be based at Queen Mary College, but will be expected to spend time both at CERN and at the Rutherford Appleton be closely associated with the development of new fast timedigitising and charge sensitive analogue techniques for particle detectors. The data acquisition system is computer controiled. Novel techniques using UV lasers and fibre optics will also be implemented. The successful candidate will be encouraged to pursue original ideas range $£ 6581$ to $£ 9871$ per annum depending on age and experience plus $£ 1250$ London Weighting. Letters of application giving age. experience and qualifications and the names of two referees should be sent as soon as possible to Mr D.R. Stone. Physics Department, Queen Mary College, 4NS.

Closing date: 7 December 1984
(2777)

## TOMMMTRNIGUTIOTNSTOTVHITTON Test Engineers microwave communications systems Wells, Somerset

If you have a thorough knowledge of functional testing of microwave electronics sub-assemblies, units and systems, then we can offer attractive career opportunities at Wells in Somerset.
We are the Communications Division of Thorn EMI Electronics Limited and have played a leading role in developing the technologies on which modern communications systems depend.
Working in a development and production environment, you will be involved with microwave systems for civil applications, currently covering frequencies up to 20 GHz .
These opportunities would suit electronics technicians with experience in microwave testing, although college leavers who have received training in microwave systems as part of their course could also be considered for some of the more junior posts.
As a leading high technology company, career development opportunities are excellent with the potential of further developing your knowledge and experience.
Attractive salaries and benefits include an active sports and sociai club and free life assurance.
Please 'phone or write for an application form quoting Ref. EWW/385 to Mr. F. M. Taylor, Assistant Personnel Manager, Thorn EMI Electronics Limited, Communications Division, Wookey Hole Road, Wells, Somerset BA5 1AA. Tel. Wells (0749) 72081. Ext. 227.


THORN EMI Electronics (2776)

## Appointments

## I.L.E.A.

Learning Resources Branch Television and Publishing Centre Thackeray Rd, London SW8

## Television Engineer (MG 10)

Salary range £7470- 59432 plus $\{1347$ London Weighting Allowance, and an irregular hours allowance of $\mathbf{2} 228$. The Mobile Video recording Section, which is equipped to broadcast standard, makes observational classroom recordings for teacher education. A television Engineer is required to join a technical team of 8 for operational and maintenance work involving rigging and driving duties.

## Film Camera Assistant (ST1/2) <br> Slary Range $55,568-$ - 8,451 plus \& 1347 London Weighting Allowance.

The Centre's Broadcast quality colour programmes use 16 mm sound film and video insert provided by the film camera section in which this vacancy has arisen.
Applicants should have relevant training and experience in servicing the requirements of film and video cameras together with the associated location lighting equipment, in television or documentary film environment.

## PRODUCTION DIVISION Television Maintenance Engineer

(ST1/2) [Re-advertised9
Salary Range $\mathbf{~} 7470-\mathbf{8 4 5 1}+\mathbf{~} 1347$ London Weighting Allowance. Plus $£ 192$ shift allowance A maintenance engineer is required to work at the Television and Production Centre which is equipped to professional colour TV broadcasting standards. The engineer will work in a section of four which is responsible for maintaining a high level of performance of a wide range of sound and vision equipmen
Full job description and application forms for all the above posts are available from EO. Estab 1B, Room 366. The County Hall, London SE1 7PB. Please S.A.E. The closing date for completed application forms is the 2nd January 1985 These post are suitable for Job-share.

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South Herts High performance mini computers to $£ 11,000$ pa
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## AUDIO DEVELOPMENT ENGINEER

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Required in the electronics workshop. The workshop staff are responsible for the maintenance of electronic instruments and for the development and construction of electronic equipment for both teaching and research. Applicants should have an appropriate qualification (minimum ONC or equivalent) and considerable experience in electronic engineering, preferably including computers.

Salary will be grade $5 \mathbf{£ 6 , 5 8 1 - \mathbf { E 7 } , 6 8 4}$ p.a. Applications to Mr. W.G. Black,
Department of Electrical \& Electronic Engineering, The University of Leeds, Leeds LS2 9JT. (2779)

Philips Drake Electronics is an expanding company specialising specialising in the design and manufacture of equipment for the Broadcasting Industry. We now require engineers for the following positions:

## PROJECT ENGINEER

A vacancy exists in our Projects department for an enthusiastic and self motivated engineer. The department deals primarily with the system design of broadcast communications equipment to customers' requirements and is responsible for the preparation of production and handbook documentation in addition to providing technical support for our sales manufacturing and test department.
A suitable engineering qualification together with some experience in broadcast or the professional audio industry is essential.

## ANALOGUE DESIGN ENGINEER

We are looking for an experienced engineer to join our development team. The successful candidate will be involved in all aspects of design from concept to production. He/She will most likely have a relevant degree and must be capable of producing innovative but practical designs with minimum supervision. Experience of the professional audio industry would be an advantage

## TEST ENGINEER

We require a test engineer with experience in testing analogue (preferably audio) circuits and fault finding to component level. He /She will become involved in varied testing from small batch produced units to complete studio communications systems and will be required to adapt to digital technology as this is

## SOFTWARE ENGINEER

A new position of microprocessor software engineer has been created and we seek a suitable candidate to design software in PASCAL and ASSEMBLER for the MC6800 family. The ability to work on your own initiative and communicate your ideas clearly is essential.
Attractive salaries will be offered to the right people.
It any of the above positions appeal to you please apply in writing including your current CV or phone Jill Humphreys on Welwyn Garden City (07073) 33866 for an application form. Philip Orake Electronics Ltd., 37 Broadwater Road, Welwyn Garden City,

## Electronic EngineersWhat you want, where you want!

TJB Electrotechnical Personnel Services is a specialised appointments service for electrical and electronic engineers. We have clients throughout the UK who urgently need technical staff at all levels from Junior Technician to Senior Management. Vacancies exist in all branches of electronics and allied disciplines - right through from design to marketing - at salary levels from around $£ 5000-£ 15000$.
If you wish to make the most of your qualifications and experience and move another rung or two up the ladder we will be pleased to help you. All applications are treated in strict confidence and there is no danger of your present employer (or other companies you specify) being made aware of your application.

## TJB ELECTROTECHNICAL

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Please send me a TJB Appointments Registration form
Name
Address

Qualifications - ONC, HNC HND final City and Guilds or equivalent, or 2 ' $A$ ' levels plus either 2 years as student or junior technician or 3 years relevant experience. Further details are available from Martin Welch (01) 7940500 ext 3209 Job description and application form available from the Personne Department Royal Free Hospital Pond Street, Hampstead, London NW3 2QG Please quote reference no: 1499

## VDU ANALOGUE DESIGN

To £16k Thames Valley

Our client, an international name at the forefront of computer peripheral technology, is seeking an experienced Analogue Electronics Engineer to join its expanding VDU manufacturing division.
This represents an ideal opportunity to become part of a team pioneering the development of VDU's and in telligent terminals. Candidates will therefore need to be able to demonstrate strong innovative flair backed by solid experience in the design of VDU's or TV drive circuits and PSU's. An appropriate qualification, preferably a degree, is essential.
The rewards are high, including a full range of big company benefits, assistance with relocation and very real prospects for career development within a young and extremely successful company.
Please write, enclosing a full C.V. and quoting reference number WW/824 to The Chief Executive, DCM Appointments, 66 Frith Street, London Wl.

DCM APPOINTMENTS

## TECHNICAL PROJECTS LIMITED

is a young and rapidly growing company specialising in the development manufacture and marketing of audio products for the professional entertainments industry worldwide. Our customers include the BBC, independent television companies, local radio, hire and production houses, manufacturers, education and MOD etc. We are noted for product quality and customer service. Due to expanding business opportunities the following two vacancies have arisen:

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(Audio/Acoustic Measuring Equipment)

1. Wales, Midlands \& East Anglia
2. Northern England and Scotland

Applications are invited from sales engineers ideally in the field of profes sional audio/acoustic measurement and its related test products, possibly working in a large company and wishing to make an impact in a smaller one using their expertise and experience in field sales to promote the company's growth
A combined five figure salary and commission of circa $£ 19.000$ is envisaged in the first year with every opportunity for increase and advance ment into management. Company car provided plus normal benefits. Please write or telephone:
The Sales Manager, TECHNICAL PROJECTS LIMITED,, Unit 2
Samuel Whites Ind. Estate, Medina Road, Cowes, Isle of Wight, PO31 7LP Telephone: (0983) 291553
(2755)

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## Appointments

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to join JET, an exciting multi-national scientific and technical project. JET is the largest single project in the European fusion research programme, investigating the feasibility of nuclear fusion as a new energy source.
The project is sited in the attractive countryside of the Thames Valley at Culham Laboratory near Oxford. The modern, purpose built laboratory complex is equipped with large and advanced research facilities supplied by European industry, and offers excellent opportunities to acquire valuable experience in many challenging areas of technological research.
We now seek a Radio Frequency/Electronic Technician to join our team of over 500 staff currently working on the project. He or she will play an important part in the expanding scientific programme which will be carried out during the project operation phase up to the 1990's.
The successful applicant will be involved in the procurement, installation, operation, maintenance and modification of high power radio frequency amplifiers, transmission line equipment and associated control circuitry.
A degree in electrical/electronic engineering or an equivalent qualification is required and several years' relevant experience of RF equipment in broadcast, industrial and scientific use, would be an advantage.
The successful candidate will be offered an appointment within the UKAEA and witl then be assigned to JET.
Salary will be dependent on qualifications, age and experience, in the range £7,990-£13,665.
Excellent working conditions, including restaurant and social facilities, are provided and other benefits include:

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For an application form and further details please write to Mr. M. Taylor, The Personnel Department, UKAEA, Culham Laboratory, Abingdon, Oxon OX14 3DB, quoting reference J 3225 . Closing date for applications is two weeks from the publication of the advertisement.

HIGHLAND HEALTH BOARD Department of Medical Phvsics and Bio-Engineering, Raigmore Hospital, Inverness

## SENIOR TECHNICIAN/ TECHNICLAN -

## Electronics Section

A vacancy exists for a Senior Technician/Technician with ONC or high qualifications. The work involves design, construction, repair and maintenance, as well as clinical involvement.
Salary is - Grade III $£ 6408$ - $£ 8283$ Grade IV £5404-£7104 Job description and application form from Area Personnel Officer,
Highland Health Board, 17 Old Edinburgh Road, Inverness. (Tel. 0463239851 )
For additional information contact Mr A R Bowley, Deputy Director.
(Tel. 0463234151 ext 276 or 277)
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[^1]:    Telephone 01445 2713/0749

[^2]:    CIRCLE 59 FOR FURTHER DETAILS

[^3]:    Peter Nicholls is East Midlands coordinator for Electronics and Control Technology with the Microelectronics Technology with the Microelectronics Education Programme

[^4]:    *The author's eprom emulator was
    described in the September, October and described in the September, October and
    November 1982 issues of E\&W pages November 1982 issues of $E$
    83,73 and 82 respectively.

[^5]:    * Subject to interpolation algorithm
    $\dagger$ There is, however. some subjective improvement due to sequential display

[^6]:    1. Figures are direct voltage $\pm$ (reading $\%+$ digits) or $\pm$ (reading + f.s. $) \%$ over 12 months unless otherwise noted.
    2. Level limit may differ e.g.-1dB $, 1 \%, 3 \mathrm{~dB}$
    3. Level limit may differ e.g. $-1 \mathrm{~dB}, 1 \%, 3 \mathrm{~dB}$
    4. 4: four-wire measurement, hi/lo: high and low test voltages
    5. Microprocessor functions, see November table ( 9 : variance, standard deviation, and r.m.s.)
    6. Microprocessor functions, see November table (9: variance, standard de
    7. G:GPIB interface, B : bod/parallel interface. C: capacitance, dB:decibels
[^7]:    Note, abbreviation $M$ refers to pins on the master zif socket, $S$ to pins on the slave socket (last month's circuit diagram).

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    Further details - Henry Butcher \& Co. 79/83 Colmore Row, Birmingham B3 2AP

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