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Scanning electron microscope photo of amorphous selenium as used for charge carrier layer in photocopying machines. Photo by Manfred P. Kage, made available by SEL. Stuttgart.

IN OUR NEXT ISSUE Digital storage for oscilloscopes, allows waveforms to be stored for later examination. Pre-trigger and step elimination are provided in an add-on unit.
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## wireless world

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Book 6 Central processing unit (CPU), memory organisation character representation. program storage, address modes. input output systems, program interrupts, interrupt priorities, programming. assemblers computers, executive programs operating systems and time sharing


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IT IS UNLIKELY that many engineers have hitherto seen themselves as technological Renoirs or
Gainsboroughs, or even prophets. But painters and practitioners in engineering suffer from the same barriers to full expression - they are often dependent on patronage and, in common with prophets, though most of them are not without honour, it is often not recognized in their own country.

We have recently seen examples of engineers who have originated quite remarkable inventions, but who, not having enough capital to put their ideas into practice, have hawked their wares round all the more obvious sources of finance - government establishments, manufacturers, financiers - with no success at all. No one, it appears, is in the risk business - at least, in the UK. As a result of this frustration, people are beginning to look overseas for their backing, which is fine for the inventor and his backer - not so good for this country.

Several British organizations exist for this very purpose, although if our correspondence pages are anything to go by, many bright ideas go unrecognized. A common complaint arnong engineers who do manage to sell their ideas and are assisted by, for example, the NRDC, is that the amount of money advanced is insufficient for an efficient operation. It may be said that half the amount needed is better than nothing, but if a cramped financial position leads to excessive caution and inhibits the broad view, it could well be worse than nothing.

Reasons for the directors of companies not wishing to risk venture capital on inventions with which they
are unfamiliar hinge to a large extent on the very fact of their unfamiliarity . Company directors, as a class, are not noted for their engineering knowledge, being recruited in the main from accountants, economists and arts graduates. Their field of interest is in marketing, finance and sales; the products of the companies over which they preside need not have much influence on their work at all, except insofar as they determine the people they rub shoulders with in business.

While they are reasonably adept in their own sphere of activity it seems unlikely that a financier is best able to judge the worth of even a simple piece of engineering, and if the project put forward for evaluation is even
' moderately recondite, then a degree of relevant knowledge is essential. And yet only around $30 \%$ of UK directors have any such knowledge. In contrast with this, Germany has about $70 \%$ of knowledgeable directors and the US $85 \%$. Taken in conjunction with our fairly dismal performance in recent years, these figures are significant, although it is impossible to say that this is the real reason.

It does appear, though, that there is a need for more university and college students to be given a chance to read their chosen engineering subjects, with a background support of 'business' training. Admittedly, this does fly directly in the face of the university tradition which insists that universities are not there to train people, but to educate them. Times are hard, however, and it should be recognized that some students, at least, are going to have to soil their hands and engage in vulgar commerce.

# Stereo f.m. tuner - Mk II 

Improved design uses new i.cs - 1

by L. Nelson-Jones, F.I.E.R.E.



This tuner is based on the alithor's highly successful design first published in the April, 1971 issue. The circuit has been modified to use two recently introduced and improved i.cs, together with a pre-aligned f.e.t. front-end module designed by the author. The circuit features an improved a.f.c. system which operates directly on the varicap tuning and allows simultaneous tuning of all the r.f. stages. Circuit options include the use of either a six-pole LC filter or ceramic resonator i.f. units.

WITH THE LARGE SIGNAL LEVELS that can occur from high-gain aerial arrays it is possible for an f.e.t. front-end to malfunction. This is due to oscillatorpulling by the signal at $g_{1}$ of the mixer which has capacitive coupling to $g_{2}$ and therefore the oscillator. In extreme cases the oscillator may be pulled completely into lock with the signal, which results in a zero-frequency i.f. To overcome this a diode limiter has been used to damp the second tuned circuit so that the oscillator cannot be pulled-in. The diode limiter does, however, lower the image-frequency rejection performance but this only occurs at very high signal levels. With this modification, and by applying around 20 dB of a.g.c. to the r.f. stage at high signal levels, the front end can handle signals of around 600 mV .

This front-end circuit, shown in Fig. 1, also differs from the MkI design because it does not have a separate a.t.c.
system. The tuning supply voltage is now modulated by the a.f.c. voltage, and controls all three tuned circuits together. Even with a high level of a.f.c., there will be no loss or gain within the holding range.

Choke coupling is used in the r.f. stage so that all tuned circuits are at d.c. ground potential. Because Mk II design is for varicap tuning only, a more compact layout has been possible. Decoupling has been improved by shorter lead lengths and a reduction in value of the decoupling capacitors to 470 pF . This ensures that the capacitors do not come near to resonance. The complete frontend is now housed in a screened case to reduce oscillator radiation and pick-up from sources such as the i.f. strip, stereo decoder, and the demodulator.

An isolated coupling loop on the oscillator coil is brought out via two terminals on the main i.f. board. The loop provides a level of around 50 mV and is designed to feed a digital counter at $50 \Omega$ impedance. This level will not cause excessive oscillator radiation, and can be interfaced to a counter via a buffer stage. The second gate of the r.f. stage is brought out through a decoupling RC network for a.g.c. A suitable biasing network is provided if a.g.c. is not required (a.g.c. ref).

## I.f. amplifier

The first i.f. amplifier stage is in the front-end already described, and pro-
vides a broadly tuned output at $330 \Omega$ impedance. Fig. 2 shows the main i.f. ciricuit. The block filter can be the Toko six-pole LC filter, as shown in Fig. 3(a). In this case the additional series resistor $\dot{R}_{2}$ has to be used to raise the source impedance to $1 \mathrm{k} \Omega$, and $\mathrm{C}_{2}$ is placed at the filter input to provide the design source capacitance. Correct loading is provided by the input biasing resistor $\mathrm{R}_{8}$.

A second option, shown in Fig. 3(b), is to use a pair of Toko i.f. ceramic resonators, type CFSE-10.7, which have a design source impedance of $330 \Omega$. In this case $\mathrm{R}_{2}$ and $\mathrm{C}_{2}$ are not needed. As there is no d.c. path through these filters, $\mathrm{C}_{4}$ is also redundant and is replaced with a link. Because the gain with these resonators is too high, a 10 dB attenuator is placed between the two filter sections (this does not impair the noise performance of the tuner).

A third choice is to use two Vernitron FM4 i.f. ceramic resonators as in the Mk I design. These filters cannot normally be directly cascaded, but the 10 dB attenuator section between them provides a satisfactory performance. Whichever type of ceramic resonator is used, it is essential that both have the same colour coding.

The main i.f. gain is supplied by the multi-stage limiting amplifier contained within $\mathrm{IC}_{1}$. The circuit has been designed around the recently introduced CA3189E, which is an improved and


Fig.1. Front-end module. Damping the second tuned circuit with a diode improves signal handling capacity.


Fig.2. Main i.f. circuit can use either the CA3089E or the newer CA3189E.


Fig.3. (a) Six-pole LC filter. (b) Two ceramic resonators. (c) Muting circuit for CA3089 and (d) CA3189.

Fig.4. Stereo decoder. Emitter follower on pin 11 of the i.c. improves the signal-to-noise ratio at low signal levels by reducing the separation.

somewhat altered version of the CA3089E. The printed circuit board can be used with either of these i.cs by making suitable component changes. However, in my experience the performance of the CA3089E is rather inferior to the CA3189E. The audio output of the CA3189E is adjustable and the values used give a level of 490 mV for each of the options.

A further option is available for the CA3089E and CA3189E because both can be used with single or double-tuned quadrature coils, $\mathrm{L}_{2}$ and $\mathrm{L}_{3}$. The doubletuned arrangement can give very low demodulation distortion if correctly
adjusted, but this requires a low distortion f.m. signal generator, and distortion measuring equipment. With a single-tuned circuit both i.cs will stilr give low distortion compared to earlier integrated circuits such as the TAA661B. Both devices provide an a.g.c. feed for the front-end module, but because the level is not the same for the two devices the a.g.c. voltage is fed through potentiometer $\mathrm{R}_{6}, \mathrm{R}_{7}$. The a.g.c. threshold is adjustable with the CA3189E. I have found this particularly useful in setting up the signal strength output, from pin 13 of $\mathrm{IC}_{1}$, to give a steady and progressive increase of level with signal input voltage. The potentiometer concerned is usually set around midway.
The external muting circuit components for the two i.c.s differ and are shown in Fig. 3(c) and 3(d). The newer CA3189E operates on deviation as well as signal level, and the value of $\mathrm{R}_{18}$ sets the deviation at which muting begins. This may be varied if required from about $2.7 \mathrm{k} \Omega$ for a large deviation before muting, to $22 \mathrm{k} \Omega$ for a very small-deviation before muting. In both circuits the setting of $R V_{2}$ determines the signal level at which muting takes place. With $R V_{2}$ set to 0 V , the muting action is stopped.

## Tuning voltage supply and a.f.c.

The a.f.c. output from $\mathrm{IC}_{1}$ is not applied directly to the front-end, but is used together with the a.f.c. reference output from $\mathrm{IC}_{1}$ to derive a 12 V tuning supply which is modulated by the a.f.c. output

of $\mathrm{IC}_{1}$. It should be noted that the tuning range of the new front-end is 1.5 to 11.5 V for 87.5 to 108 MHz . The a.f.c. reference of the CA3089E and CA3189E is a 5.6 V zener-stabilized supply which can be used for the reference of a conventional stabilized supply. If a suitable amount of a.f.c. voltage from pin 7 of $\mathrm{IC}_{1}$ is added to the reference supply, then the tuning voltage will change in a way that will correct the error which caused the a.f.c. output to differ from the a.f.c. reference voltage level.
The a.f.c. reference is permanently connected to the non-inverting input and the op-amp $\mathrm{IC}_{2}$ via an f.e.t. so that it can be switched in and out. When the f.e.t. is switched on, $R_{22}$ and $R_{23}$ control the amount of a.f.c. voltage that is added to the reference level. The tuning voltage output is controlled by the feedback chain $R_{26}, R_{27}, \mathrm{RV}_{3}$. The a.f.c. switch operates with the source and drain of the f.e.t. at the a.f.c. reference level. If the gate is connected to 0 V the f.e.t. is biased off; if the gate is left free, $\mathrm{R}_{25}$ turns the f.e.t. on; an f.e.t. with a cut-off bias of less than -4 V has been chosen to ensure clean switching. Values chosen for $R_{24}$ and $R_{25}$ allow the a.f.c. switch to be formed by a pair of
touch-pad contacts between 0 V and the a.f.c. on/off input. Capacitor ${ }^{-}{ }_{16}$ ensures that this input is not excessively sensitive to interference. If touch-pad operation is used it is essential that the chassis is correctly earthed and connected to the 0 V line.
With this system an almost constant a.f.c. performance is achieved across the whole f.m. band because, as the tuning voltage is reduced, the amount of change due to any a.f.c. control is also reduced. The tuning characteristic of the front-end is almost perfectly logarithmic with tuning voltage. The tuning-voltage supply must be free of noise and hum to prevent spurious modulation of the oscillator frequency. This is achieved by the high commonmode supply rejection of $\mathrm{IC}_{2}$, and by the filter $\mathrm{R}_{28}, \mathrm{C}_{17}$ in the supply to $\mathrm{IC}_{2}$.

## Stereo decoder

The decoder circuit in Fig. 4 is essentially the same as that published in the April 1978 edition by M. J. Gay. The capacitor between pins 2 and 12 has been made 10 nF rather than 6.2 nF quoted, and the input capacitor has been raised from $2 \mu \mathrm{~F}$ to $4.7 \mu \mathrm{~F}$. These changes were made because my stock did not contain
the original values, so they need not be implemented.

The TCA4500A has a low impedance output due to the feedback networks $\mathrm{R}_{4}$, $\mathrm{C}_{6}$ and $\mathrm{R}_{7}, \mathrm{C}_{7}$. It is therefore necessary to feed the multiplex filter through resistors which have a value equal to the design source impedance of $4.7 \mathrm{k} \Omega$ for the filter. Because the design load impedance of this filter is also $4.7 \mathrm{k} \Omega$, there will be at least a 6 dB loss. To restore the audio output level, and to isolate the filter from the load, an amplifier with a gain of two is connected to each of the stereo outputs. High negative feedback in each of these amplifiers makes the input impedance very high, so the matching network $\mathrm{R}_{11}$, $C_{12}$ and $R_{12}, C_{13}$ is connected to ensure that the filter sees a resistive termination of $4.7 \mathrm{k} \Omega$. The bias for these two stages is derived from the d.c. output level of the i.c. at pins 4 and 5. The filter has a low d.c. resistance from input to output and the values of $R_{5}$ and $R_{6}$ are insufficient to cause any appreciable loss of voltage to the bases of $\mathrm{Tr}_{3}$ and $\mathrm{Tr}_{5}$.
Gain of the stages is defined by the equal values of load resistors $R_{16}, R_{17}$ and $R_{22}, R_{23}$ so only half of the output is

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| Frequency range | 87.5 to 108 MHz (tuning voltage +1.5 to +11.5 V ) |
| I.f. <br> I.f. bandwidth ( -3 dB ) | 10.7 MHz |
|  | $\begin{aligned} & 220 \mathrm{kHz} \text { (CFSE-10.7 } \\ & \text { filters-FM4) } \end{aligned}$ |
|  | 250 kHz ( 6 -pole LC filter Toko |
|  | 135BBR3132A) |
| Input impedance | nominally 75 ohms unbalanced |
| Limiting input signal $1 \mu$ threshold |  |
| level (mono) | $1.5 \mu \mathrm{~V}$ typical |
| Capture ratio | 1 dB |
| Image response | -48dB |
| l.f. response at input | about -100 dB |
| Oscillator voltage at aerial input | less than 1 mV |
| Oscillator output to counter | about 50 mV into $50 \Omega$ |
| Muting threshold range | adjustable from 0 to about $8 \mu \mathrm{~V}$ |
| Audio output level | 490 mV for $\pm 75 \mathrm{kHz}$ peak deviation |
| Spurious decoder outputs | better than -60 dB at 19 kHz and all harmonics |
| Audio frequency response | $\begin{aligned} & 10 \mathrm{~Hz} \text { to } 15 \mathrm{kH} \\ & \pm 1 \mathrm{~dB} \end{aligned}$ |
| De-emphasis time constant | $50 \mu \mathrm{~s}(75 \mu \mathrm{~s}$ with capacitors raised by 50\%) |
| A.f.c. pull-in range | $\begin{aligned} & \pm 500 \mathrm{kHz}(1 \mathrm{mV} \\ & \text { input level) } \end{aligned}$ |

Mark II tuner is better than the Mk I design on noise performance, especially at low signal levels, and this is very noticeable in listening tests. A.m. rejection of the new tuner is again

better especially in listening tests.
fed back to the emitter of the input transistor. The output d.c. level is blocked by $\mathrm{C}_{14}$ and $\mathrm{C}_{15}$, while $\mathrm{R}_{19}$ and $\mathrm{R}_{24}$ prevent clicks if the output is connected to an amplifier or switched after the receiver has been turned on. Resistors $\mathrm{R}_{18}$ and $\mathrm{R}_{26}$ in the output leads prevent oscillation in these two amplifier stages if very long and capacitive leads are used.

Stereo/mono switching is achieved by transistor $\mathrm{Tr}_{1}$. This is normally biased on, and the decoder is in the mono state. If the $\mathrm{m} / \mathrm{s}$ input is grounded, the biasing is removed and $\mathrm{Tr}_{1}$ is turned off, which restores the decoder to stereo operation.

Capacitor $\mathrm{C}_{10}$, together with the biasing resistor $\mathrm{R}_{10}$, form a h.f. filter to prevent this input from causing interference. A switching transistor was used because the manufacturers' data specifies a maximum capacitance, from pin 9 to ground, of 100 pF and I felt that this was too easily exceeded. Also, a long lead into the 228 kHz oscillator circuit is undesirable.

The signal-level voltage from the CA3189 can be used, as detailed in the TCA4500A article, to reduce the separation at low signal levels and provide a better signal-to-noise ratio. This has been added to the decoder circuit by placing an emitter follower between the signal level input from the CA3189, and pin 11 of the TCA4500A. Use of a p-n-p emitter follower ensures that the current can be drawn out of pin 11, and also provides a low impedance drive for the signal strength meter. The recommended resistor value for $R_{13}$ is $39 \mathrm{k} \Omega$, which gives close to $100 \mu \mathrm{~A}$ for full signal strength. This resistor may be reduced in value for movements up to 1 mA . Diode $D_{1}$ is included to remove the $V_{\text {be }}$ of $\mathrm{Tr}_{2}$ from the feed to the meter. Resistor $R_{14}$ and capacitor $C_{11}$ are included to prevent interference from passing through $\mathrm{Tr}_{2}$. The supply to the decoder is filtered to prevent switching surges from reaching the rest of the tuner.

A stereo indication l.e.d. is driven from pin 7 through $\mathrm{R}_{8}$ and a further $270 \Omega$ on the tuning indicator board. If this board is not used, and only the external l.e.d. to the +12 V supply is required, then $R_{8}$ should be raised to $680 \Omega$.

## Tuning indicator

.The circuit in Fig. 5 is exactly as used with the Mk I toner. It consists of a long-tailed pair feeding two l.e.d.s, whose brilliance will be equal for equal input voltage levels to the bases of the two transistors. A degree of degeneration is applied to lower the gain from the unbypassed resistors in the emitters. The output terminal is connected to the a.f.c. pin of the i.f. board. The stereo indicator l.e.d. in the original circuit had to be compatible with a filament lamp to match the Portus and Haywood decoder, but this requirement is not now

## Parts List

Front end and i.f.
Front end and i.f.
Resistors $-5 \% 1 / 4$ E carbon film unless otherwise stated

R $1100 \mathrm{k} \Omega$
$6 \quad 1 \mathrm{k} \Omega$ (3089)
$39 \mathrm{k} \Omega(3189)$
7 omit (3089)
$39 \mathrm{k} \Omega(3189)$
$8560 \Omega$ (6-pole)
$330 \Omega$ (CFSE/FM4)
$12 \quad 2.7 \mathrm{k} \Omega$ (double-tuned)
omit (single-tuned)
$13 \quad 18 \mathrm{k} \Omega$ (double-tuned)
$3.9 \mathrm{k} \Omega$ (single-tuned)
$8 \quad 4.7 \mathrm{k} \Omega(3089)$
$8.2 \mathrm{k} \Omega(3189)$
19 omit (3089)
$4.7 \mathrm{k} \Omega(3189$ double-tuned)
$6.8 \mathrm{k} \Omega(3189$ (ingletun
$6.8 \mathrm{k} \Omega(3189$ single-tuned)
20 link (stereo)
$4.7 \mathrm{k} \Omega$ (mono)
$21 \quad 100 \mathrm{k} \Omega$
225.6 kS
$33 \mathrm{k} \Omega$
$2.2 \mathrm{M} \Omega 10 \%$
10MQ 10\%
$10 \mathrm{k} \Omega 2 \%$
$6.2 \mathrm{k} \Omega 2 \%$
$680 \Omega$
$47 \Omega$
$1 \mathrm{k} \Omega$ (stereo, or to suit meter on mono)

Capacitors - 20\% tolerance unless otherwise stated

| C 1 | $0.1 \mu \mathrm{~F}$ | 250 V | polyester |
| :---: | :---: | :---: | :---: |
| 3 | 10 nF | 100 V | min. ceramic |
| 4 | 10 nF | 100V (6-pole) | min. ceramic |
|  | link | (CSFE or FM4) |  |
| 5,6 | 10 nF | 100 V | min. ceramic |
| 8 | 47nF | 250 V | polyester |
| 9 | $10 n F$ | 100 V | min. ceramic |
| 12 | $10 \mu \mathrm{~F}$ | 25 V | tantalum bead |
| 13 | 33pF | 100 V (stereo) | min. ceramic |
|  | 4.7n | 100 V (mono) | 10\% ceramic |
| 14.15 | $10 \mu \mathrm{~F}$ | 25 V | tantalum bead |
| 16 | 10 nF | 100 V | min. ceramic |
| 17 | 100 F F | 16 V | min. vertical electrolytic |

Other components
Tr, Siliconix 2N4339 metal can n-channel
f.e.t.

RV $\quad$ or E113 plastic n-channel f.e.t
$R V_{1} \quad 47 \mathrm{~K} \mathrm{~min}$. horizontal skeleton
$R V_{3} \quad 4.7 \mathrm{k} \Omega \mathrm{min}$. cermet horizontal
potentiometer
$L_{1} \quad 22 \mu \mathrm{H}$ min. choke Sigma Products type SC10-22 $\mu \mathrm{H}$
$L_{2} \quad$ TKACS $34342 \mathrm{BM}\left\{\begin{array}{l}\text { (double tuned fo } \\ 3089 \text { or } 3189 \text { ) }\end{array}\right.$
$L_{3}$ TKACS 34343AUO Toko UK Ltd
$\mathrm{L}_{2} \quad$ KACSK586HM (single tuned for 3089 or 3189) - Toko UK Ltd
Front-end module Key Electronics FMT2-0 Integrex Ltd
I.f. filters
Fig.3(a)
$R_{2} \quad 680 \Omega$
$C_{2} \quad 10 p F \quad 100 \mathrm{~V}$ min. ceramic
Filter $\quad$ Toko 135BBR3132A

Fig. 3(b)

| $R_{2}$ | Link |
| :--- | :--- |
| $\mathrm{C}_{2}$ | omit |
| $\mathrm{R}_{3}$ | $220 \Omega$ |
| $\mathrm{R}_{4}$ | $150 \Omega$ |
| $\mathrm{R}_{5}$ | $220 \Omega$ |
| Filters | Toko CFSE or Vernitron FM4 |
|  |  |
|  |  |
|  |  |

Muting circuits

|  |  |
| :--- | :--- |
| 3089 | Fig. |
| 3(c) |  |
| $R_{14}$ | $470 \Omega$ |
| $R_{15}$ | $120 \mathrm{~K} \Omega$ |
| $\mathrm{R}_{16}$ | $\operatorname{link}$ |
| $\mathrm{RV}_{2}$ | $470 \mathrm{~K} \Omega$ min. horizontal skeleton |
| $\mathrm{C}_{10}$ | $0.47 \mu \mathrm{~F} 35 \mathrm{~V}$ tantalum bead |
| $\mathrm{C}_{11}$ | omit |


| $\mathbf{3 1 8 9}$ Fig $\mathbf{3}$ (d) |  |
| :--- | :--- |
| $R_{14}$ | $470 \Omega$ |
| $R_{15}$ | link |
| $R_{16}$ | $47 \mathrm{k} \Omega$ |
| $R_{2}$ | $10 \mathrm{k} \min$. horizontal skeleton |
| $C_{10}$ | $47 \mu \mathrm{~F} 6.3 V$ tantalum bead |
| $C_{11}$ | $2.2 \mu \mathrm{~F} 35 \mathrm{~V}$ tantalum bead |

Stereo decoder
Resistors $-5 \%$ 1/4W carbon film unless
otherwise stated otherwise stated.


## Other components

IC. TCA4500A, Motorola
$\mathrm{Tr}_{1}, 3.5 \mathrm{BC} 109 . \mathrm{BC149} ,\mathrm{ZT} \mathrm{\times 109}$.BC 184 , etc.
$\mathrm{Tr}_{2}, 46$ BC179. BC159, BC2 14 etc.
D. $\quad 1 \mathrm{~N} 4148$

Filter Toko BLR3107N

| Tuning indicator |  |
| :--- | :--- |
| $\mathrm{R}_{1}$ | $4.7 \mathrm{k} \Omega$ |
| 2 | $100 \Omega$ |
| 3 | $330 \Omega$ |
| 4 | $100 \Omega$ |
| 5 | $1 \mathrm{k} \Omega$ |
| 6 | omit |
| $7_{1}$ | $270 \Omega$ |
| $\mathrm{~T}_{1.2}$ | $\mathrm{BC} 109, \mathrm{BC} 149.2 \mathrm{~T} \times 109 . \mathrm{BC} 184$ etc. |
| $\mathrm{D}_{3}$ | 0.2 in red l.e.d. |


| Power supply Transformer |  |
| :---: | :---: |
|  | RS Components GVA type 196. 296 secondaries in series, or type |
|  | 196-303 secondaries in parallel |
| Rectifiers Capacitors | 1 N 4003 (four) |
|  | $0.1 \mu \mathrm{~F} 250 \mathrm{~V}$ polyester (three) |
|  | $470 \mu \mathrm{~F}$ and $1000 \mu \mathrm{~F} 25 \mathrm{~V} \mathrm{~min}$. vertical electrolytic |
| Resistor | 22 ohm |
| Regulator | type 7812 plastic i.c. |
| Fuse | 20 mm 250 or 300 mA anti-surge |



Fig.5. L.e.d. tuning indicator and stereo indicator.

Fig.6. Power supply.
needed, therefore $R_{6}$ is omitted and $R_{7}$ is 2708.

## Power supply

The power supply shown in Fig. 6 uses a full-wave rectifier which feeds two smoothing capacitors. This arrangement produces very little ripple on the +20 V supply to the a.f.c./tuning voltage circuit. The +12 V supply to the tuner and decoder is stabilized by a 7812 i.c. which has two $0.1 \mu \mathrm{~F}$ capacitors across its terminals for h.f. stability.

The capacitor wired across the bridge rectifier input prevents hole-storage noise in the diodes and forms a useful h.f. filter in conjunction with the leakage reactance of the mains transformer.

## Interconnection of the tuner

The method of interconnection is shown in Fig. 7. It is important to ground the aerial input socket to the chassis only through its connection to the i.f. board. The 0 V terminal and wiring must be grounded to chassis at

only one point near to the output. The mains earth must be connected only to the mains transformer frame and the interwinding screen. The transformer frame is insulated from the chassis because the tuner will normally be part of an audio system, and it is usual to connect only the pre-amplifier to the mains earth.
To make such an earthing system safe it is essential to use a transformer of
good construction which has a copper. foil interwinding screen, and a good earth bond to the frame.

To be continued. The concluding article describes the alignment procedure and shows the printed circuit board layouts.

Fig.7. Interconnection diagram.


# Instant tuning 

By Cáthode Ray

SEVERAL TIMES LATELY I have overheard on the radio hints of a great new idea: instead of changing from one radio* channel to another by turning a knob, meanwhile scrutinizing the movement of a pointer along a scale crowded with figures and names of places (quite likely obsolete) to find the position that will give the wanted programme, and then when at last it has been found, via a cacophony of intervening hisses and shrieks, finding it is too late to hear the start; instead, as I say, of this gruelling process several times every day, some inspired scientist (never any mention of an engineer, of course) is on the verge of making possible an instant switchover from one programme to another!
Well, of course, I thought this was just another example of how illinformed so many people are, so that it can be commonly supposed that television was invented about 1950 and that Lindbergh was the first person to fly across the Atlantic (instead of, wasn't he, about the 55th?). But a few days ago I was pulled up short by reading in the IEE's journal Electronics \& Power, July 1977 issue, p.547, an article by Mr James Redmond, Director of Engineering of the BBC (and therefore presumably knowing something about these matters) in which he said as follows:
'At one time there was a large audience of television viewers who remained tuned to one programme because they dared not retune the set in the hope of finding something more to their taste. Pushbutton television receivers quickly freed them from this

[^3]tyranny, and now we impatiently await the pushbutton portable v.h.f. radio. 'Impatiently await!'
I fully appreciate the point about being afraid to depart from a channel, once it has been tuned in, because of the difficulty experienced by the lay public in retuning any radio (in its correct inclusive sense) receiver. My sympathies are entirely with them. What sort of a radio engineer is he/she who designs a set that obliges them to do anything so crude?

In the 9 th February 1939 issue of W.W. (i.e., getting on for 40 years ago) I am on record as stating 'A certain trade list of receivers now on the British market describes 665 models. Of these, 231 are to be found with pushbutton tuning.' I went on to say 'As regards the date of invention, leaving out the inevitable Chinese and Egyptian claims to priority... it can definitely be said that it [pushbutton tuning] was on the market at least as early as 1928' (fifty years ago now) and went on to describe an American Zenith model so fitted, which enjoyed a ready sale, including one to me.

By 1940 (December issue of W.W., p.499) I made fun of the 'ever-patient British public grinding away at their tuning controls' to change from one programme to another. Evidently the 231 pushbutton models less than two years earlier had not prevailed against the 434 others.

But Mr Redmond was writing about v.h.f. radios, which these early p-b models were not. So v.h.f. p-b models may still be in the future?

My wife being one of the lay public and therefore, I consider, fully justified in demanding instant programme changing, I provided her with it almost from the start; i.e., when I married her, which was not yesterday, since our grandchildren are by now doing a bit of demanding of their own in the electronic field. When the three BBC channels became available on v.h.f. (I exclude Radio 1 from consideration, while realizing that for many members of the population this is the preferred channel, but would I be hopelessly wrong in supposing that even they sometimes want a change?) I fully accepted the justice of the BBC claim that from then on all right-thinking citizens should turn exclusively to v.h.f. So, nearly 20 years ago, I scrapped all else and provided my wife with an all-v.h.f. set having a three-position switch covering her 'radio' needs.

All went happily until the BBC (obliged, I am sure, by hidden political
forces) began introducing intensely unwanted Open University lessons into v.h.f. channels. In distress at this unpredicted and frustrating development, my ever-loving wife turned to me; and with unfailing resourcefulness I devised a m.w. unit for Radio 4 making use of the existing switchgear.

This worked reasonably well until the next crisis, when for a time all Radio 4 channels, v.h.f. and m.w., were belting out studies on the life-style of the arachnidae or some such esoteric matter about which my wife did not at that time wish to know. Eventually it transpired that this was not more than a technical hitch lasting for a mere couple of hours, but it undermined my reputation for foreseeing and providing against every eventuality.

For the last year and more, the BBC, no doubt at the receiving end of bitter complaints from those listeners who had done as they had been bid and had changed over to v.h.f., has used every available gap between programmes to plug the necessity for a three-waveband receiver, long-wave, medium-wave and v.h.f., in order to be able to hear everything they provided. (I hope no unprincipled radio dealers take advantage of customers who remember only the 'three-waveband' bit, by unloading stock that technically conforms to this description but lacks v.h.f. and includes instead the not widely demanded short waves.) Among the ordinary undiscriminating British public these exhortations are unlikely to have been heeded, because the OUB public are able to get everything from the $B B C$ on their cheap imported 'transistors,' many of which are one-waveband sets. Any whose interest may have been kindled to the extent of inquiring would quickly be deterred by the price of the a.m. plus f.m. models.

Some of the OUBP-housewives chiefly - got their first jolt when they suddenly found that their Tuesday and Thursday afternoon plays had turned into Questions in Parliament (cleverly disguished as live broadcasts from the Zoo) and 'Disgusted, Tunbridge Wells' became thick with complaints. The BBC could always say, in polite euphemistic terms, of course, 'We told you so.' The same thing will happen again in November, only much more so, among the one-waveband brigade when, not having taken in the oft-repeated warnings, they find themselves unable to get any Radio 4 programmes at all, unless they acquire new sets at least with long waves. Moreover, those of

# Logic design - 15 

## Action / status interface design

by B. Holdsworth" and D. Zissost *Chelsea College, University of London

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The operation of action/status interfaces, which was the subject of part 14 , continues, and design of interfaces is discussed, both in general and in two specific examples. This is the final article in the series on logic design.

With the exception of the go/no-go interface shown in Fig. 6, handshake system configurations which use two states to define the read/write cycle are easier to design and implement. Unless otherwise specified, the 'go' mode will start with a write operation. The diagram shown in Fig. 12(a) is used to define the read and write operations. For ease of reference the flip-flop used to generate the go/no-go signal $G$ is shown in Fig. 12(b). The starting point in the design process is the basic system developed next.

## Basic system

Read/write cycle. The implementation of a read/write cycle is straightforward. A block diagram of the two-device system is shown in Fig. 13(a), and its step-by-step operation is flow charted in Fig. 13(b). The state diagram of the interface logic is shown in Fig. 13(c).

By direct reference to the state diagram the following equations are obtained:

Turn-on set of $A=\bar{r}_{2}$,
turn-off set of $A=\bar{r}_{1}+R$.

$$
\begin{aligned}
& \mathrm{A}=\overline{\mathrm{r}}_{2}=\mathrm{Ar} r_{1} \overline{\mathrm{R}} \\
& \mathrm{a}_{1}=\mathrm{S}_{1} \mathrm{r}_{2}=\mathrm{Ar} \mathrm{r}_{2} \\
& \mathrm{a}_{2}=\mathrm{S}_{0} \mathrm{Gr}_{1}=\overline{\mathrm{A}} \mathrm{Gr}_{1}
\end{aligned}
$$

The NAND implementation of these questions is shown in Fig. 13(d).

The timing diagram for the read/ write cycle is shown in Fig. 13(e). Initially, it is assumed that $r_{1}=0$ and that the system is in state $S_{0}$, which implies that the system is writing and that device 1 is active. When device 1 has fully responded, $r_{1}$ goes from 0 to 1 , $\mathrm{a}_{2}=\mathrm{Gr}_{1}$ and device 2 is activated. On activation its status signal $r_{2}$ goes from 1 to 0 and thus initiates the transition from $S_{0}$ to $S_{1}$ and also turns off the action signal $\mathrm{a}_{2}$.

In state $S_{1}$ the system is reading and it continues to do so until device 2 has completed its resnonse, when $r_{2}$ changes from 0 to 1 and device 1 is activated. The cycle is completed when


Fig. 12. Read/write cycle.

(a)
(b)

(c)

Fig. 13. Read/write cycle schematic (a) and flow diagram (b). State diagram is at (c) and NAND implementation and its timing are at (d) and (e).

(e)
the status signal of device $1, r_{1}$ goes from 1 to 0 , thus initiating a transition back from $S_{1}$ to $S_{0}$ and simultaneously turning off the action signal $a_{1}$.
Write-inhibit cycle ( $i_{1}=1$ ). To inhibit the write operation in the basic read/ write cycle, the write operation is replaced by a read operation as shown in the flow chart of Fig. 14(a). The modification to the state diagram in Fig. 13(c) as a consequence of introducing the write-inhibit process is implemented by interchanging the values of $a_{1}$ and $a_{2}$ in state $S_{1}$, when $i_{1}=1$. Expressed algebraically, the entries for write/ inhibit are $a_{1}=r_{2} \bar{i}_{1}$ and $a_{a}=G r_{2} i_{1}$, as shown in Fig. 14(b).

Read-inhibit ( $i_{2}=1$ ). Similarly, to inhibit the read operation in the basic read/write cycle, a read operation is replaced by a write operation as illustrated in the flow chart of Fig. 15(a) and the corresponding modification to the state diagram is shown in Fig. 15(b).

The block diagram of the basic system with reset, go/no-go, read-inhibit and write-inhibit facilities is shown in Fig. 16(a). Its step-by-step operation is described by the flow chart of Fig. 16(b). The following equations are obtained directly from the state diagram shown in Fig. 16(c).

$$
\begin{aligned}
\mathrm{A} & =\overline{\mathrm{r}}_{2}+\mathrm{AR}_{1} \overline{\mathrm{R}} \\
\mathrm{a}_{1} & =\overline{\mathrm{AGr}}_{1} \overline{\mathrm{i}}_{1}+\mathrm{Ar}_{2} \overline{\mathrm{i}}_{1} \\
\mathrm{a}_{2} & =\overline{\mathrm{A} G r_{1} \overline{\mathrm{I}}_{2}}+\mathrm{AGr}_{2} \mathrm{i}_{1} \overline{\mathrm{i}}_{2}
\end{aligned}
$$

## Design steps

The design of interfaces can be accomplished in the five steps listed below, and illustrated in Fig. 17. These steps are general and can be used in all interface design problems.
Aims of the design. The system specification is first expressed in the logic interface designer's terms. This step is introduced to ensure that the system requirements are interpreted correctly by the designer.
This stage is critical and requires cooperation between the interface designer and the system designer.
Device characteristics. In this step the designer specifies the terminal characteristics of the devices to be interfaced. Consideraion of the purely internal characteristics of the devices should be avoided if possible.
System design. The interface designer specifies the system characteristics in general terms by means of a block diagram and a system flow chart and consults the designer for approval.
Hardware design. This step is provisional, and hardware design may well be modified as a consequence of the experience obtained in software design. It is accomplished conventionally using well-established methods described in this series.
Software design. On the basis of the hardware design and assuming the necessary instructions, the basic software for the operation of the device is designed. This process may well indi-

(a)


Fig. I4. Flow-chart for write-inhibit (a) and state diagram at (b).

(c)

Fig. 16. (a) shows block diagram of basic system, with flow chart (b) and state diagram (c).

(a)

(b)

Fig. 15. Read-inhibit flow chart is at (a) and the state diagram at (b).

(b)


(a)

Fig. 18 (a) is block diagram for "rub-out characters" interface, with flow-chart at (b). State diagram is shown at (c) and circuit at (d).

(c)
(d)


(b)



(b)

(e)
cate modifications to the hardware design which may lead to improvements. In fact, software and hardware design should be regarded as complementary and should be repeated until a satisfactory design of both hardware and software has been achieved.

## Problems and solutions

The design steps described are illustrated by means of two typical problems and their solution. For further design problems the interested reader is referred to the second edition of Digital Interface Design, by Zissos and Duncan, published by Oxford University Press, and to "System Design with Microprocessors,"' by Zissos, published by Academic Press.
Rub-out characters. Given a paper tape reader and a tape punch, design and implement a small system that allows a new tape to be produced in which the rub-out characters (all l's) are deleted.
-The aim is to reproduce data after deleting specified characters, in this case the rub-out characters.

- Both reader and tape punch are action/status devices.
-The block diagram of the solution is shown in Fig. 18(a). The AND gate detects the rub-out characters on the data-bus. Its output $d$ is logical ' 1 ' when all the digits on the data-bus are l's. When $d=1$ the data is inhibited from being punched and the input tape is advanced. This is equivalent to $i_{1}=0$, and $i_{2}=d$ in the basic read/write cycle notation.
The flow chart describing the step-by-step operation of the system is shown in Fig. 18(b).
The state diagram of the interface car be derived either directly from the system flow chart in Fig. 18(b) or by substituting $\mathrm{i}_{1}=0$ and $\mathrm{i}_{2}=\mathrm{d}$ in Fig. 16(c).

From the state diagram, which is shown in Fig. 18(c), the following equations are obtained:

$$
\begin{aligned}
& \mathrm{A}=\overline{\mathrm{r}}_{2}+\mathrm{Ar}_{1} \overline{\mathrm{R}} \\
& \mathrm{a}_{1}=\overline{\mathrm{A}}_{\mathrm{A}} \mathrm{Gr}_{1} \mathrm{~d}+\mathrm{Ar}_{2} \\
& \mathrm{a}_{2}=\overline{\mathrm{A} G \mathrm{r}_{1} \mathrm{~d}}
\end{aligned}
$$

The implementation of these equations is shown in Fig. 18(d).
Reader-to-plotter interface. The first four tracks of an eight-track tape specify eight actions a digital plotter can take, namely move $0.1 \mathrm{~cm} N, N E, E$, SE, S, SW, W and NW with the stylus up or down. The other four tracks indicate the number of times each command is to be executed. For example 10010110 is interpreted as: "Draw a line 0.6 cm long from NW to SE." Design a suitable interface between the reader and the plotter. The coding of the various directions specified in the problem is shown.
-The aim is as specified above.
-Both the reader and the plotter are action/status devices.
-The block diagram of the solution is shown in Fig. 19(b). In addition to the
two action signals $a_{1}$ and $a_{2}$, the interface must reset the counter with signal $R_{1}$ and increment it with signal $c$ at the appropriate times.
Initially the counter is cleared with the system reset signal $R$ prior to the interface being activated by the signal G.

Activating the interface causes the pen to move one space (in this case 0.1 cm .) in the direction specified, with the stylus up or down, as specified by the first four tracks of the tape. When the pen begins to move, $r_{2}$ becomes 0 , and the counter is incremented. When the pen stops, indicated by $\mathrm{r}_{2}$ becoming 1 , the output of the comparator circuit shown in Fig. 19(b) is tested. If the output $\mathrm{k}=0$, the pen is moved again and the counter is incremented. This continues until $\mathrm{k}=1$, indicating that the stylus has moved through the number of distance units specified by the second set of four tracks on the tape. At this point the input tape is advanced and the counter is cleared. The process continues until the system is turned off, that is until $\mathrm{G}=0$.
-As in the previous problem the state diagram can be derived directly from the flowchart of Fig. 19(c) and

$$
l_{2}=b_{b_{4}} \sum_{b} . \quad \text { in the state diagram of }
$$

the basic system. Note $1_{2}=1$ when the last four digits on the tape are zeros.
The modified state diagram is shown in Fig. 19(d) and by direct reference to this figure the following equations are obtained:

$$
\begin{aligned}
& \mathrm{A}=\overline{\mathrm{r}}_{2}+\mathrm{AR} \mathrm{R}_{1} \\
& \mathrm{a}_{1}=\mathrm{AGr}_{1} k L_{2}+\mathrm{Akr}_{2} \\
& \mathrm{a}_{2}=\overline{\mathrm{A} G r}_{1} \overline{\mathrm{~L}}_{2}+\mathrm{AGr}_{2} \mathrm{ki}_{2} \\
& \mathrm{R}_{\mathrm{I}}=\overline{\mathrm{r}}_{1} \\
& \mathrm{c}=\overline{\mathrm{r}}_{2}
\end{aligned}
$$

The implementations of these equations is shown in Fig. 19(e).

## Acknowledgements

The authors are grateful to Mr J. Bothoroy, a research assistant at the University of Calgary, for his contribution towards the development of action/status interfaces.

## References

5. "Digital Interface Design," D. Zissos and F. G. Duncan, Oxford University Press 1974. 6. Microprocessor System Design," D. Zissos, Academic Press 1978.

## SIXTY YEARSAGO

In late 1918, the triode (a name not yet used) was beginning to gain ground, and several valve receivers were in use. But its acceptance was not as rapid as it might have been, particularly under the stimulus of war-time development. A note appeared in this issue on the Marconi Double Note Magnifier which evidently exhibited a gain of three times per stage.
"Briefly, the instruments consist of two three-electrode valves connected in series with one another in such a way that the telephone currents from the magnetic detector or crystal receiver are magnified in two successive stages before being led to the telephones themselves. The valves thus act not as detectors but as amplifiers. All circuits have been simplified to the highest degree so as to remove the need for adjustments. Electrically there is no difference between the model for the magnetic and that for the crystal except in the design of the first transformer, the primary for which has, of course, to be of lower resistance for the magnetic detector than for the crystal. In one model a switch takes the place of three sets of terminals, and is connected in such a way that, when working direct without magnification, the valve filament circuits are broken. When using first magnification, one valve only is in circuit, whilst for second magnification both valves are in circuit. Otherwise the arrangements are the same.

The total magnification obtained with this new instrument is such that signals from the magnetic detector are at least three times as strong as those obtainable with a crystal receiver. It will be noticed that a 200 -volt battery is used for the plate circuit.
By the addition of the note-magnifier to the magnetic, we have available a receiver which possesses the notable reliability of the magnetic, and far greater sensitiveness than the crystal, which. - with the exception of the more complicated forms of valve receivers - has hitherto formed the most sensitive commercial type."

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# The f.e.t. as detector 

## Improvements in performance over the diode detector

by Roger S. Amos, B.Sc.

The author sets out his views on the advantages of field-effect transistors over semiconductor diodes as demodulators for a.m. receivers. Reduced distortion and improved signal-to-noise ratio are claimed and a design for a receiver using the technique is presented.
most modern radio and television receivers use silicon or germanium diodes to demodulate the output of the i.f. amplifier. Such detectors are purely passive, giving no power gain, but in superhet receivers this is generally of no account, since the r.f. and i.f. stages provide all the sensitivity that is needed and deliver the power to drive the detector. Before the advent of the superhet, however, the detector in a t.r.f. receiver was often a triode valve which gave a.f. amplification besides detection, contributing to the sensitivity of the receiver.
Today, with the ready availability of f.e.ts, which in many respects behave like triode valves, it is possible to apply the advantages of semiconductor technology to the triode detector circuits of yesteryear. And this is no purely academic matter, for some popular a.m. superhet i.cs, such as the NE546A, LM1820N, $\mu \mathrm{A} 720$ and CA3123E (which are all pin-for-pin equivalents) and the rather different TBA651, have an uncommitted i.f. output, leaving the manufacturer or constructor to add the detector of his choice.
The application of f.e.ts as a.m. detectors, however, demands an understanding of both f.e.t. parameters and triode detector operation. The latter may not be familiar to those who have learned radio theory in the age of the superhet and especially the semiconductor superhet. The three most popular triode detectors were the leaky-grid detector, the infiniteimpedance detector and anode-bend detector. But when these are simulated using f.e.ts, unexpected results are often obtained. This article seeks to explain those results and to show how the f.e.t. may provide an attractive alternative to the diode as an a.m. detector.

## Leaky-grid (or-gate) detector

The leaky-grid detector, a typical circuit of which is shown in Fig. 1, is essentially an extension of one form of thermionic


Fig. 1 A basic leaky-grid detector. The component values shown are typical.


Fig. 2 Practical circuit for the f.e.t. equivalent of the leaky-grid detector shown in Fig. 1. Component values are typical.
diode detector, the grid of the triode acting as the anode of a diode. Hence the RC network betweeen tuned circuit and valve are similar to those used in diode circuits, and for high-quality a.m. detection would be chosen to give a time-constant of say $50 \mu \mathrm{~s}$. Positive half cycles of input cause grid current to flow (hence the circuit's name) rapidly charging the reservoir capacitor $C$. This charge leaks away slowly through resistor $R$, because this is high compared with the forward resistance of the "diode" when charging. Thus, before the charge has leaked away it is restored by the next positivie half cycle. The voltage across the capacitor there-
fore follows the modulation of the incoming carrier and because this voltage is applied to the valve between grid and (through the tuning inductor) cathode, the circuit gives both detection and audio amplification.

An f.e.t. equivalent of this circuit would probably employ a junction-gate f.e.t. since, in these, sufficient positive bias on the gate (an n-channel device is assumed) drives current across the gate-channel junction. Insulated-gate f.e.ts in normal use would be unsuitable since gate current is precluded, but a device having a separate substrate or base terminal could possibly be used with that ierminal as the "grid"; if the
device is an enchancement-type, the gate would need to be biased forward to establish drain current.

Direct substitution of the valve in Fig. 1 by a suitable j.u.g.f.e.t. with appropriate amendment of component and supply values generally yields disappointing results, the output being feeble and distorted. The principal reason for this is that the gate-channel junction, like a silicon diode, does not conduct appreciably if the forward voltage is below about 0.6 V . When silicon diodes are used as a.m. detectors, it is regular practice to apply forward bias to them to bring them into conduction, while maintaining the working point on a sufficiently non-linear part of the characteristic to achieve the required detection; often this bias is provided through the a.g.c. loop in the receiver. The addition of positive bias to the gate of the f.e.t., as shown in Fig. 2, greatly improves the audio quality and efficiency of detection. In the valve circuit of Fig. 1 no positive grid bias was needed because some electrons are emitted from the cathode with sufficient energy to land on the grid even when it is slightly negative relative to the cathode.

There are two main advantages in this form of detector. Firstly, like the diode detector on which it is based, it imposes a load on the tuned circuit through which power is delivered to drive the grid-cathode or gate-channel "diode". This loading can cause distortion since the "diode" resistance is nonlinear, falling with increasing positive half-cycle amplitude. In addition there is some steady damping of the tuned circuit through $R$ in Fig. 1 and the effectively parallel $R_{1}$ and $R_{2}$ in Fig. 2. These reduce the $Q$ of the tuned circuit, limiting the gain available from the previous stage. In superhet receivers, of course, the slight power loss is no great problem since sensitivity and power are available from the preceding i.f. amplifier; in early t.r.f. receivers the loss of power, sensitivity and also selectivity was less tolerable. In high-quality applications, however, the distortion may be less acceptable. Damping of the tuned circuit can be counteracted in some measure by driving the detector from a tapping or a secondary winding, although this further reduces the power available.

Secondly, because the "diode" must be capable of giving appreciable conduction on positive half cycles of input, no reverse bias may be applied to it, hence the absence of cathode and source resistors in the circuits of Figs. 1 and 2. Consequently current consumption is heavy, the f.e.t. giving best results as its saturation drain current is approached; this can be as high as 20 mA for a $2 \mathrm{~N} 3819,15 \mathrm{~mA}$ for a BF244B and 13 mA for a BF256LB. While this is of little consequence in mains-driven equipment, it is clearly wasteful in battery equipment. The detector

circuits to be described avoid both these disadvantages.

## Infinite-impedance detector

As Fig. 3 shows, the infinite-impedance detector closely resembles one form of thermionic diode dectector. The difference is that in the former the power to drive the detector comes from the supply rather than the input tuned circuit. This gives it two advantages over the leaky-grid detector. Firstly, anode current in the cathode load resistor provides grid bias, ensuring that an effectively infinite impedance is presented to the input tuned circuit. This minimizes damping and enables the detector to make full use of the available input voltage. Consequently, distortion is low and at one time this detector was favoured in high-quality applications. Secondly, the cathode resistor limits anode current, which may be very low. Indeed, if the valve is near cut-off the non-linearity of its grid bias/anode current characteristic enhances the efficiency of detection; this non-linearity is not, however, essential to the operation of the infiniteimpedance detector.

Positive half cycles of input voltage cause peaks of anode current which rapidly charge the reservoir capacitor. Since the only discharge path presents a comparatively high resistance, the charge leaks away slowly. Consequently, negative half cycles of input voltage oppose the charge on the reservoir capacitor, causing the valve to
be biased back. Providing the cathode resistor and capacitor are chosen to give a suitable time constant, the cathode voltage will accurately track the peaks of the positive exclursions of input voltage. Since the detector is a cathode follower, it resembles a diode circuit in that it gives no voltage gain; it does, however, give appreciable power gain in that it transfers voltage from its practically infinite input impedance to the comparatively low output impedance at the cathode.

The valve in Fig. 3(b) may be directly substituted by an f.e.t. Since the gate. will not be required to conduct, insulated-gate and junction-gate types are equally suitable, but enhancementmode devices would require more complex biasing arrangements. Since junction-gate types are inexpensive and easier to handle, they will probably be preferred.

Figure 4 shows an infinite-impedance detector using an n-channel j.u.g.f.e.t. The circuit generally gives an excellent signal-to-noise ratio, low distortion and a higher level of audio output than might be expected; the reason for this will be discussed below. Mean drain current rises as signals are received and if amplified can be used to derive a.g.c. or " $S$ " meter drive. In many respects this is the most promising f.e.t. detector.

The circuit, has, however, a disadvantage. In an f.e.t. there is some capacitance between gate and source; this is given as typically 8 pF for the 2 N 3819 and 4 pF for the BF244 and

BF256. Fig. 5 shows the circuit of Fig. 4 including this capacitance and a decoupling capacitor redrawn to show its similarity to a shunt-fed Colpitts oscillator. If the f.e.t. is a high-gain type or if it has a high gate-source capacitance, instability may occur. This can generally be eliminated by making the source resistor large (and the reservoir capacitor correspondingly small to maintain the required time constant) thereby reducing the gain of the f.e.t. In practice it is often helpful to make the source resistor a pre-set pot. ( $50 \mathrm{k} \Omega$ is a useful value) which can be adjusted for stability and the reservoir capacitor ( 470 pF is a typical value) can be replaced by a more suitable value if necessary. If the detector is fed from a damped tuned circuit or from a voltage step-down transformer (as in Fig. 8) instability is unlikely to cause any problems.
There are circumstances in which the potential instability of this detector can be an advantage. In receiving c.w. and s.s.b. transmissions it is often helpful if the detector can be made regenerative, and this can often be achieved very simply by the addition of a suitable external capacitor between gate and source. A variable source resistor will usually give smooth control of regeneration. If the f.e.t. is a low-noise type such as the BF256, the detector alone at the threshold of regeneration exhibits remarkable sensitivity; one constructed by the author having only a ferrite aerial and followed by a low-noise audio stage feeding a power amplifier equalled several domestic superhet receivers in sensitivity, but gave better signal-tonoise ratio and less distortion. Almost certainly the inherent positive feedback through the f.e.t's internal gate-source capacitance and associated external circuitry accounts for the unexpectedly high level of audio output.

## Anode- (or drain-) vend detector

Like the infinite-impedance detector, the anode-bend detector, of which a typical circuit is shown in Fig. 6, presents an infinite impedance to the input tuned circuit. Detection is, however, by virtue of the non-linear characteristics of the valve near cut off. Thus, this detector shares with that previously described the advantages of minimal damping and low current consumption; furthermore, it offers the extra advantage of useful audio amplification.

The operation and design of this detector are most easily understood if it is regarded as a stage of audio amplification in which the input is at r.f. If the valve were perfectly linear and if the input signal were too small to cause overloading there would, of course, be no audio output. But because the valve is biased nearly to cut-off and is nonlinear the increase in anode current on each positive half cycle of r.f. input is greater than the diminution caused by an equal negative half cycle and the


Fig. 4 An infinite-impedance detector using an n-channel j.u.g.f.e.t. The component values are typical, but there are complications - see Fig. 5 and text.


Fig. 5 Part of the circuit of Fig. 4 including the internal gate-source capacitance of the f.e.t. $\left(C_{g s}\right)$ and a decoupling capacitor across the supply ( $C_{\text {decouping }}$ ) redrawn to show the similarity of the infinite-impedance detector to a shunt-fed Colpitts oscillator. This explains the instability sometimes encountered in this form of detector.


Fig. 6 Theoretical circuit of an anode-bend detector. The component values are typical.
mean anode current follows the modulation of the incoming carrier. This is converted to a voltage output by the insertion of an RC combination with a suitable time constant in the anode circuit of the valve. The author's experiments (with f.e.t. circuits) have shown that the cathode (or source) resistor needs to be decoupled at audio
frequencies, because it is common to input circuits and, without decoupling, introduces negative feedback, reducing the audio gain. With a source resistor of $47 \mathrm{k} \Omega$ the optimum bypass capacitor is about $10 \mu \mathrm{~F}$; higher values cause distortion and lower ones upset the audio frequency response.

The transfer characteristics of f.e.ts
resemble the grid voltage/anode current characteristics of triode valves, their non-linearity near cut-off suiting them theoretically for use in "drainbend" detectors. As in the infiniteimpedance detector, the gate will not be required to conduct, so that junctiongate and insulated-gate types are equally suitable. If an enhancementmode device were used, it would be necessary to hold the gate bias just above the threshold voltage, which could cause complications.
If an f.e.t. were substituted for the valve in Fig. 6 and the component and supply values amended accordingly, the performance of the resulting drain-bend detector would probably be disap. pointing compared with that of the simpler infinite-impedance detector shown in Fig. 4. In fact the drain-bend detector poses a number of problems.
Firstly, there is a loss in sensitivity caused by the Miller effect, which consists of negative feedback at radio frequencies through both internal and stray drain-gate capacitance. Although great losses might be expected because of the high value of drain load resistance and small drain bypass capacitor,
'this is largely counteracted by the low $g_{m}$ (or $y_{f s}$ ) of the f.e.t. near cut-off. Miller effect can be overcome by the use of neutralization or a cascode circuit; a dual-gate i.g.f.e.t. can be employed since it behaves like a cascode. Although the helpful regenerative tendency seen in the infinite-impedance detector is destroyed by the high drain load resistance and extensive source decoupling, it can be re-introduced by partial decoupling at the source, as shown in Fig. 7. At the sacrifice of some audio voltage gain this permits the introduction of an external capacitor between source and gate to neutralise Miller effect, improving the sensitivity. It also facilitates regeneration if required. The resulting detector, however, is no longer a pure drain-bend detector - it is a hybrid between drain-bend and infinite-impedance types.
Secondly, the output impedance at the drain may be as high as $50 \mathrm{k} \Omega$. If this is coupled to a common-emitter audio stage having a low input impedance ( $2 k \Omega$ is typical), the detector output voltage will collapse, leading to apparently poor results. For this reason it is advisable to follow a drain-bend
detector with either an emitterfollower, as in Fig. 7, or an f.e.t. audio stage.
Thirdly, a high supply voltage may be needed. With the f.e.t. operating near cut-off, the voltage across the source resistor approaches the pinch-off voltage. If the f.e.t. were a 2 N 3819 this may be as high as 5 V , and if the drain load resistor were, say, four times the source resistor, clearly 20 V would be needed to drive the drain current through it. Ideally, at least 3 V should be maintained between drain and source so that the minimum supply voltage under these conditions is 28 V . This can be overcome by deliberately selecting an f.e.t. with a low $\mathrm{V}_{\mathrm{p}}$; some BF244s have $V_{p}$ less than IV and, using these, drain-bend detectors can be constructed which will operate satisfactorily from 9 V and even 6 V supplies. The BF256 can also be used with a 9 V supply if component values are chosen carefully.
Fig. 7 shows a practical circuit for a hybrid drain-bend/infinite-impedance detector with emitter follower output stage in which all the performancesaving steps outlined above have been

Fig. 7 Practical circuit diagram for a hybrid drain-bend/infinite-impedance detector with buffer emitter-follower audio stage; the latter is necessary to match the high output impedance of the detector. Neutralization is applied via the 25 pF trimmer to counteract Miller effect. But the performance may in some respects be inferior to that of the simpler infinite-impedance detector in Fig. 4.


Fig. 8 Circuit of an a.m. superhet using an i.c. for main functions and an f.e.t. infinite-impedance detector. The circuit combines performance, versatility and


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taken. While it will generally give a greater audio output than its counterpart in Fig. 4 from the same input signal, the signal-to-noise ratio may well be inferior, and the circuit may suffer from treble-cut caused by the extra positive feedback introduced to neutralize the depredations of Miller effect. The infinite-impedance detector shown in Fig. 4 followed by a stage of low-noise a.f. amplification would give results superior in most respects using almost the same components. For this reason the latter circuit will probably be preferred.

## Complete receiver

Figure 8 shows the circuit of a superhet receiver in which a readily available integrated circuit provides the functions of r.f. amplifier, local oscillator, mixer, i.f. amplifier and a.g.c. detector, an infinite-impedance detector like that in Fig. 4 providing a.f. output and a d.c. feed for an " S " meter circuit. Although a two-gang mechanical tuning capacitor could be used, the author's prototype employed a Motorola MVAM2 dual varicap diode, the tuning voltage being selected from an array of seven pre-set and one variable $100 \mathrm{k} \Omega$ pots by an eight-way push-button switch; the 27 V bias was provided by three 9 V dry batteries in series, which proved remarkably stable, the pre-set stations remaining in tune over many weeks.
Source-follower $\mathrm{Tr}_{1}$ matches the aerial tuned circuit to the r.f. stage and, helped by inherent regeneration as in an infinite-impedance detector, contributes to the sensitivity and signal-tonoise ratio of the receiver. The combination $\mathrm{R}_{4} / \mathrm{C}_{8}$ provides broadband coupling between r.f. stage and mixer. Coupling from mixer to i.f. amplifier is via the selectivity block $L_{3} / F_{1} / L_{4}$ which may consist either of two discrete i.f. transformers and a ceramic filter or, as in the author's prototype, an integrated block containing these elements; these are available commercially with a choice of bandwidths. The i.f. output appears at pin 6, a portion being fed through $\mathrm{C}_{11} / \mathrm{R}_{7}^{-}$to a voltage doubling diode pair in the i.c. which provides a.g.c. for the r.f. stage. Signal is transferred from i.f. amplifier to detector through a discrete i.f. transformer, which in the author's prototype gave a
voltage step-down, being intended for coupling to a diode detector. Although this transformer was far from ideal for its present detector, the detector nevertheless gave more than adequate output to feed a domestic high-quality amplifier. The step-down transformer also improved the stability of the detector. The combination $\mathrm{L}_{6} / \mathrm{C}_{13}$ removes any stray i.f. which might cause distortion in the amplifier or " S " meter circuit.
The optional " S " meter drive consists of the long-tailed pair $\mathrm{Tr}_{3 / 4}$ and associated circuitry. Mean drain current in the infinite-impedance detector rises when a signal is received; this appears as an increased voltage across the source resistor $\mathrm{R}_{8}$, which biases $\mathrm{Tr}_{3}$ forward; in the absence of a signal $\mathrm{Tr}_{3}$ is biased off by $\mathrm{Tr}_{4}$. For meters up to $500 \mu \mathrm{~A}$ f.s.d. $R_{10}$ and $R_{11}$ can be adjusted for meter sensitivity and zero respectively; meters over $500 \mu \mathrm{~A}$ f.s.d. may be satisfactory if $\mathrm{R}_{9}$ is reduced. Additional a.g.c. could be derived from $\mathrm{Tr}_{3}$ collector by the inclusion of a suitable resistor in series with the meter; this could be fed to the gate of $\mathrm{Tr}_{1}$, the signal being fed in through a suitable capacitor.
Sensitivity of the receiver compares favourably with that of domestic superhet receivers; signal-to-noise ratio is superior and distortion very low. The latter two features are due in some measure to the nature of the detector. With junction-gate f.e.ts costing little more than silicon diodes, the infiniteimpedance detector surely offers scope for an improvement in a.m. receiver design.

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# Frequency synthesizers - 1 

## The generation of wanted frequencies from other frequencies

by R. Thompson, M.I.E.E.

The term "frequency synthesis" is applied to processes involving the generation of some wanted frequency from one or more other.frequencies. The most common forms of frequency synthesizer use high-grade fixed frequency references to generate fixed- or variable-output frequencies with stabilities similar to those of the references. Like many definitions, this one cannot be considered to be precise and we shall see that frequency synthesis represents a particular grouping of techniques, most of which are widely used in other applications of electronics.
the need to generate variable frequencies with the stability of a fixed reference has led to a concentration of effort on frequency synthesis over the past 15 years. A major application of this type of synthesizer is in radio communication equipment where narrowband modulation methods with precise carriers are required. Another major application has been in modestly priced instruments capable of very accurate
measurement $c_{-}^{-}$time, frequency and phase.

A feature of great importance with many modern synthesizers is the ease with which the required frequency can be selected. Communication equipment design has had increasing emphasis on ease of operation, aiming in many cases to eliminate the need for specialist operators. Nowhere has this pressure been greater than with military applications. Here, the trend has been from equipment requiring tediously repetitive adjustment to switch selectable operation, and now to radio equipment having entirely automatic frequency control.

The basic requirement for generating one frequency from another can be stated as: $f_{2} / f_{1}=X / Y$, where $X$ and $Y$ are rational numbers. In principle, therefore, synthesis only requires multiplication (X) and division (Y). However, as we shall see, practical considerations limit the attainable values of $X$ and $Y$. where such practical limitations occur


Fig. 1. Simple rectangular waveforms and graphical representations of their associated frequency spectrums.


Fig. 2. A squarewave $f_{1}$ being used to switch a sinusoidal signal $f_{2}$ to produce frequency spectrums. In (a) each burst of $f_{2}$ starts in the same phase. In (b) the squarewave forces each burst of $f_{2}$ to start in the same phose.
$X / Y$ can be factored as: $X / Y=(x / y)$ ( $X_{1} \pm X_{2} / Y_{2}$ ). The introduction of the $\pm$ allows the multiplication/division factors to be reduced. We shall be coming back to this in more detail later; the important point here is to see that we are interested in the four basic arithmetic functions.

## Addition and multiplication

If we start with some very simple waveforms and their spectra we can get an appreciation of the possibilities and difficulties of adding and multiplying frequencies.

Figure 1 shows simple rectangular waveforms and their associated frequency spectrums. Frequencies are only present at integral multiples of the fundamental frequency $f_{1}$ and, from the general expression for the Fourier series shown below, it can be shown that their amplitudes follow a $\sin x / x$ law.
$a(t)=\frac{A T}{T}\left|1 / 2+\sum \frac{T}{T m \pi} \sin \left(\frac{\tau m \pi}{T}\right) \cdot \cos \left(\frac{2 \pi m}{T}\right)\right|$
Obviously a wide range of frequencies can be generated, but if a reasonably flat spectrum is required up to high order harmonics, very narrow pulses will be required. This will, however, result in very little energy being available at any selected frequency.
If we are interested only in a particular range of harmonics, one method is simply to filter the output of the square wave, even though the level quickly reduces as we increase the harmonic number. An alternative method is to use the squarewave to switch a sinusoidal signal frequency $f_{2}$, as shown in Fig. 2.
In Fig. 2(a) there is an integral relationship between $f_{1}$ and $f_{2}$, and each burst of $f_{2}$ starts in the same phase. The resultant spectrum is a double-sided version of that in Fig. 1 centred on $f_{2}$.
In Fig. 2(b), where $f_{2} \neq \mathrm{m} f_{1}$, the squarewave not only switches $f_{2}$ on and off but forces it to start each burst in the same phase. This gives a spectrum similar to that in Fig. 2(a) but now, although the spectral envelope is centred on $f_{2}$, there is no component there. Since the waveform is periodic at $f_{1}$ the components will be at harmonics of $f_{1}$. The forced synchronisation of an oscillator, producing an output as in Fig. 2, gives high level outputs with a frequency stability dependant only on $f_{1}$ and it can be seen that by controlling

(a)

Fig. 3. Diagram showing three frequency spectrums resulting from a mixing process which eriables frequencies to be added or subtracted. (a) is probably a simplification because it would normally also contain harmonics of $f_{2}$. In (b) the ratio of $f_{2} / f_{1}$ is high and $f_{2}+f_{1}, f_{2}$ and $f_{2}+2 f_{1}$ are difficult to separate. In (c) the problem is that of removing $2 f_{2}-f_{1}$.
(b) Fig. 4. The use of equal on and off periods in Fig. 3 enables the $f_{2}+2 f_{1}$ frequency component to be suppressed. This diagram shows how, by also replacing the off period with a phase-reversed $f_{2}$, $f_{2}$ may be suppressed too. In (a), where the periods are equal, the nearest components are $2 f_{1}$ away.
(c) (b) shows what can happen when the periods are not equal.
the switching pulse width a form of filtering is provided.
One of the recurrent problems in frequency synthesis is the selection of the wanted signal from a host of unwanted signals and any method capable of providing this selection is of interest. The most obvious methods are L,C,R and crystal filtering. However, these are often bulky, expensive and difficult to tune. Their selectivity is governed by the percentage frequency separation of the wanted and unwanted signals, selection becoming more difficult as percentage separation decreases. Because of these difficulties, any techniques providing selectivity, such as those illustrated in Figs. 1 and 2, are of potential interest in frequency synthesis.

Figure 3 shows a situation similar to that of Fig. 2(a) but with $f_{2} \neq m f_{1}$, and no forced phase synchronism. The spectral envelope in Fig. 3(a) is again the $\sin x / x$ shape centred on $f_{2}$. However, in this case, components do not in general occur at harmonics of $f_{1}$, but at $f_{2} \pm m f_{1}$. This follows from the fact that the waveform is no longer periodic at $f_{1}$ but at some rational fraction of this and the periodicity is no longer controlled only by $f_{1}$ but by the combination of $f_{1}$ and $f_{2}$.

The process shown in Fig. 3(a) is of course normally referred to simply as "mixing" and it provides a means of adding or subtracting frequencies. As with multiplication, a major problem is the rejection of unwanted frequencies. The situation is normally worse than that shown in Fig. 3(a) because harmonics of $f_{2}$ are present at the input or generated in the switching process. Figure 3(b) and (c) illustrate the problem created. In (b) the ratio $f_{2} / f_{1}$ is high, making it difficult to separate $f_{2}+$
$f_{1}$ from $f_{2}$ and $f_{2}+2 f_{1}$. Components from the spectrum centred about the second harmonic of $f_{2}$ will also come close to $f_{2}+f_{1}$. However, in this case, they will only be small amplitude signals.

In Fig. 3(c), the ratio $f_{2} / f_{1}$ is low, easing the separation of $f_{2}+f_{1}$ from $f_{2}$. The problem is now that of separating out $2 f_{2}-f_{1}$. Filtering problems therefore set upper and lower boundaries on mixing ratios.

The problem in the case shown in Fig. 3 (b) can be eased by using the selective characteristics of the spectral envelope. To start with, the use of equal on and off periods will suppress the $f_{2}+2 f_{1}$ component. If, in addition, the off period is replaced by a phase reversed $f_{2}$, we can
achieve cancellation of $f_{2}$ as well. This is illustrated in Fig. 4(a), the nearest components are now $2 f_{1}$ away. In practice this cancellation will not be complete; for instance, if the periods are not equal, the result will be as shown in Fig. 4(b). However, very useful attenuations of 20 to 40 dB , can be obtained.
These simple considerations of multiplication and addition lead us to some of the practical circuits used for these operations.
Harmonic generation can be achieved by a variety of standard pulse circuits, and modern integrated circuits can generate pulses with harmonics up to about 1 GHz . Traditionally, of course, class C amplifiers have been used in transmitters as frequency multipliers
and are normally a harmonic generator with some frequency selectivity incorporated.

Multiplication to very high frequencies, tens of GHz , is possible with step recovery diodes (s.r.ds). The s.r.d. functions as a switch with switching times in the region of $50 \times 10^{-12}$ seconds. Figure 5 shows a particular arrangement using a s.r.d. with a resonant transmission line. The diode acts as a short circuit while passing forward current, and continues to maintain this condition after the current reverses. When all the current carriers have been swept out of the diode it rapidly switches to an open circuit, shunted by the reverse capacitance of the diode. With a suitable drive level and bias voltage $V_{b}$ it can be arranged that there is a maximum current in the inductor $L$ at the instant of switching due to $L$ resonating with the diode capacity and the line impedance $\mathrm{R}_{\mathrm{o}}$, through the harmonic by-pass capacity $C$. The pulse generated across the diode travels down the line, is reflected at the open circuit, and returns to the diode. At this time the diode switches to forward conduction again. The energy therefore continues to be reflected up and down the line. It can be seen that the output is characterised by a forced phase synchronism of the waveform on the line. The frequency is therefore a harmonic of $f_{1}$ and independent of the line tuning. The line tuning of course, provides the selection of the required harmonic.

The quenched oscillator shown in Fig. 6 gives a similar spectrum to that of the step-recovery-diode multiplier, though this is normally used at lower frequencies. Transistor $\operatorname{Tr}_{1}$ operates as a grounded-collector Hartley oscillator tuned by $L$ and $C$ to approximately the required harmonic frequency. Transistor $\mathrm{Tr}_{2}$ is switched by $f_{1}$ causing the LC circuit to be heavily damped, thus stopping oscillation. The damping period must be sufficient to dissipate the stored energy in $L$ and $C$. When $T_{2}$ switches off the circuit transient causes oscillation to restart, in the same phase every cycle of $f_{1}$.

In our initial consideration of mixing, Fig. 3, it was seen that simple switching of one frequency by another produces the $f_{2} \pm f_{1}$ frequencies wanted for addition or subtraction. Mixer circuits normally apply such a switching action, though $f_{1}$ and $f_{2}$ are usually in sinusoidal form.

Figure 7(a) shows a transistor mixer in simplified form. Source $f_{2}$ switches the transistor into conduction on positive half cycles and the lower amplitude source $f_{1}$ modulates the amplitude of the pulses producing the output shown. This has components at $f_{2} \pm f_{1}$, but simple inspection shows that there are also components at $f_{1}$ and $f_{2}$.

The possibility of cancelling components has already been mentioned and Fig. 7(b) is a reasonably obvious modification of Fig. $7(\mathrm{a})$ to provide cancellation. In (b) the two transistors


Fig. 6. Quenched oscillator circuit which provides a similar spectrum to the multiplier in Fig. 5. See text.


Fig. 5. Circuit diagram and waveforms for a step-recovery-diode multiplier on a resonant transmission line. When conditions are right, the diode switches to open circuit and the pulse generated across it travels down the line, is reflected, and returns to the diode. At this time the diode switches to forward conduction again and the process is repeated - the pulse being reflected up and down the line at a frequency equal to $a$ harmonic of $f_{1}$, depending upon the line tuning. See text.

Fig. 7. Circuit (a) is a simple transistor mixer which produces frequency components at $f_{2} \pm f_{1}, f_{1}$ and $f_{2}$. In circuit (b) the $f_{2}$ components in the two transistors are in antiphase, resulting in a waveform having no $f_{1}$ component. Circuit (c) consists of two type (b) circuits and produces a resultant waveform having neither $f_{2}$ or $f_{1}$ components.



4 Fig. 8. Circuit and waveforms for a diode ring modulator. This circuit produces a waveform having only $n f_{2} \pm f_{1}$ components.

Fig. 9. Logic bistable used as a mixer. Waveforms shown are outputs resulting from $(m+x)$ cycles on $D$, where $m$ is an integer. Transfer characteristic, also shown, has both a positive slope and a negative slope indicating that increasing $f_{2}$ may increase or decrease $f_{\text {out }}$, depending upon the frequency ratio of $f_{1}$ and $f_{2}$.

operate similarly to that in (a) but the $f_{2}$ components are in opposite phases in the two transistors. The resulting output waveform has no $f_{1}$ component and is in fact the waveform of a conventional amplitude modulated carrier.

The logical step from the method used in Fig. 7(b) is to arrange the cancellation of $f_{2}$ as well, as shown in Fig. 7(c). This consists of two circuits of the type shown in (b) with antiphase $f_{2}$ signals. The phase of the zero crossings, at the frequency $f_{2}$, reverse every cycle of $f_{1}$, and there is in fact no frequency component at $f_{2}$. The waveform shown is that of a suppressed carrier, double sideband signal.

Mixers that suppress only one of the input frequencies are called balanced and those that suppress both of the input frequencies are called double balanced.

While the transistors in the mixers of Fig. 7 are conducting, they can introduce spurious mixing products due to
the nonlinearities of the transistor characteristics. Many mixers use diodes or transistors as near ideal switches and the most common circuit is the diode ring modulator shown in Fig. 8. In this modulator the amplitude of $f_{2}$ is much larger than $f_{1}$ and switches on diodes 1 and 4 , or 2 and 3 . This chops the $f_{1}$ signal as shown, producing an output having. suppressed $f_{1}$ and $f_{2}$ components. High. drive levels and fast-switching Schottkey diodes are used to make the circuit operate as an ideal switching mixer. The only terms in the output are $n f_{2} \pm f_{1}$, giving good separation of higher order products; that is, no $n f_{2} \pm m f_{1}$ terms.

Another type of mixer uses a logic bistable. This 'D' flip flop, which is readily available in integrated circuit form, when clocked, simply transfers the logic state on its D terminal to its output. If two separate frequencies are applied to the D and clock inputs, the output waveform will contain a beat frequency pattern varying as the two

## The author

Raymond Thompson was born in Belfast but has spent most of his life in England. He was educated at Cheltenham and Birmingham Technical Colleges, and in 1960 joined the Plessey Company at West Leigh. In 1966 he went to Westinghouse Research in the USA where he worked on the design of high power convertors and inverters. including special inverters for h.f. fluorescent lighting and lightweight power supplies for X-ray units. In 1969 he rejoined Plessey and he is currently at their Roke Manor Establishment designing v.h.f. and u.h.f. digital radio systems for military applications. Raymond Thompson has had several papers published, including two articles in Wireless World.

waveforms move with respect to each other. Figure 9 shows the types of output waveform obtained with such a mixer and the mixer transfer characteristic.

Any number of cycles may occur on the $D$ terminal between clocking pulses. If there are an exact integral number of cycles, the logic level on $D$ at the clocking instants will always be the same. The output will be d.c. If there are $m+$ $x$ cycles, where $x$ is a fraction of a cycle, the level at $D$ will vary between some successive clocking instants. Where $1 / x$ is an integer, the output will be a simple waveform with a fundamental frequency equal to $x f_{1}$ or $(1-x) f_{1}$, see Fig. 9.

When $1 / x$ is not an integer, the fundamental frequency will be a subharmonic of $x f_{1}$, with $x f_{1}$ being present as a harmonic of that.

An important point to nute is that there is a positive slope and a negative slope on the mixer transfer characteristic. This means that increasing $f_{2}$ may increase or decrease $f_{\text {nu1 }}$, depending on the frequency ratio of $f_{1}$ and $f_{2}$.

Part 2 of this article will discuss frequency division circuits which use digital binary counters. It will also explain how prescalers can be used to extend the frequency range of such circuits.

# Versatile microwave source 

## Multiband unit comprises u.h.f. source and step recovery multiplier

by G. D. Lean, B.Sc., A.R.C.S., M.I.E.E.

Designed for simple communication links using Gunn diodes in either professional or amateur equipment, this unit improves frequency stability and reduces bandwidth. It can also be used as a replacement for klystrons in radar assemblies and communication systems. Comparing favourably in noise output to many earlier designs of solid-state sources it offers simplicity and improved reliability.
this simple multipurpose microwave source is capable of providing milliwatts of r.f. power at frequencies between 4.5 and 8 GHz . It can be used as a local oscillator or as a lower power transmitter or driver with frequency modulation of up to 80 kHz peak-to-peak deviation. By using scaled versions of the final


Modulation amplifier $\mathrm{Tr}_{6}$ of Fig. 1 can be situated in bottom left-hand compartment of this u.h.f. driver.


Fig. 1. Capacitor valves marked thus * in this driver circuit should be adjusted on test for best results. Variable types are Mullard C80905/02. Decoupling capacitors are ceramic discs and others have a polystyrene dielectric. See p. 56 for inductor details.
multiplier, frequencies in the range 2.5 , 4.5 GHz and 8.0 to 12 GHz can also be obtained. Output powers in the range 1 to 10 mW can be achieved depending on the output frequency and the varactor in use. The source comprises three separate units which can be built and tested separately: a u.h.f. driver, steprecovery multiplier, and harmonic selection filter.

## Driver circuit

The driver circuit develops about 500 mW of power at around 384 MHz , with an optional output of 50 mW so that Mullard modules. BGY22 can be driven to operate high power multipliers for transmitter or up-convertor applications. The circuit shown in Fig. 1 is an adaptation of earlier designs ${ }^{1}$ originally based on a compact transmitter design by G3TDZ. It uses readily available cheap transistors and the complete unit can be built for less than $£ 10$ including the crystal.
The crystal oscillator stage uses a fifth overtone crystal to give positive feedback from collector to emitter with $\mathrm{L}_{1}$ tuned to the overtone frequency of 96 MHz . Slight pulling of the oscillator can be achieved by adjusting $L_{1}$ so that the exact crystal frequency is obtained. The variable-capacitance diode and capacitor $\mathrm{C}_{4}$ form part of the tuned circuit together with $L_{1}, C_{6}$ and $C_{7}$, and changes in varicap bias cause slight frequency shifts in the oscillator. The resultant narrow band f.m., with a peak deviation of about 1 kHz at the crystal frequency, produces adequate deviation for telephony communication when multiplied up to the microwave bands. Transistor $\mathrm{Tr}_{6}$ gives some gain for the modulation voltage and provides d.c. bias for the varicap diode via $\mathrm{R}_{5}$. Resistor $\mathrm{R}_{8}$ damps coil $\mathrm{L}_{1}$ and prevents any tendency for self-oscillation of $\mathrm{Tr}_{1}$ due to its internal collector-emitter capacitance. Oscillation should cease if the crystal is removed and should not vary by more than 10 kHz as $\mathrm{L}_{1}$ is tuned. Capacitors $\mathrm{C}_{6}$ and $\mathrm{C}_{7}$ form a capacitive tap to match into the base of the first doubler stage. Components $L_{2}, C_{9}$ and $\mathrm{C}_{10}$ form a tuned circuit at 192 MHz and provide matching into the second doubler $\mathrm{Tr}_{3}$. About 10 mW can be measured at $\mathrm{L}_{3}$ and $\mathrm{C}_{4}(384 \mathrm{MHz})$ by using a one-turn coupling loop feeding a power meter. Resistor $\mathrm{R}_{15}$ is a base "stopper" to prevent parasitic oscillations which are common in BFY90 amplifiers.
Matching into the first amplifier is achieved by tapping the coil $\mathrm{L}_{3}$ and selecting $\mathrm{C}_{12}$. About 70 mW can be measured at the output of $\mathrm{Tr}_{4}$, which is driven into conduction by the self bias created across $\mathrm{R}_{17}$. Components $\mathrm{L}_{5}, \mathrm{C}_{15}$, $\mathrm{L}_{6}, \mathrm{C}_{16}$ form a two-stage matching network going through 50 ohms at the link point. For low power requirements the final output can be connected to $\mathrm{C}_{15}$ instead of the wire link. For higher power $\mathrm{Tr}_{5}$ gives the extra gain to provide 0.5 -watt output at $\mathrm{C}_{18}$. This final


Fig. 2. Step recovery multiplier gives a comb of frequencies spaced at 384 MHz , one of which is filtered out, to give a few milliwatts at 5.76 GHz . (Use BXY41D for X-band). Variable types are Mullard C80905/03.


Fig. 3. Step recovery multiplier unit and matching for 5.6 to 6 GHz . Dimensions in brackets apply for 10.4 GHz in waveguide 16 .
stage draws 60 mA and is self-biased through $\mathrm{RFC}_{2}$ and $\mathrm{R}_{19}$.

## Driver construction

The unit is constructed on double-sided glass-fibre board, etched on one side as shown in the diagram. For "one-off" production the easiest way is to photocopy the pattern and stick the copy onto a suitably-sized piece of board. Then drill through the paper into the board and clean up the holes carefully. Mark out the circuit using an etch-resist pen or resistant transfers and completely paint over the earth plane side.


This view of the step-recovery multiplying circuit omits screening cover and matching screws.


Fig. 4. Multiplier output at 5.76 GHz has 300 kHz bandwidth (top), improved to 30 kHz by Fig. 5 filter (bottom). Horizontal scale 100 MHz div.

Then etch the board and remove the resist. Countersink the holes on the earth plane side and fit pins to provide the external connectors. Copper or tinplate screens can then be soldered to the top side and coils $\mathrm{L}_{1}$ and $\mathrm{L}_{2}$ fitted. Fit the components, starting with the oscillator stage and test each stage before starting the next. Finally when the board is finished and tested mount it in a small die-cast box on 6BA nut spacers beneath the underside of the board. Modulation input, power supply and r.f. output connections can then be taken through convenient sockets in the box sides.

## Multiplier design and construction

 The multiplier shown in Figs. $2 \& 3$ is a C-band version of an X-band design ${ }^{2}$ by P. Tunbridge (G8DEK) which uses a Mullard varactor diode to generate a comb of frequencies, one of which is selected by the output filter. The input matching components are $\mathrm{L}_{9}$ and $\mathrm{C}_{19}$ while $C_{20}$ provides some capacitance trimming to the input capacitor formed inside the waveguide by the shaped diode-support pillar. Resistors $\mathrm{R}_{21}$ and $\mathrm{R}_{20}$ provide a d.c. bias return for the varactor; these are low impedance to give improved high-order multiplication. Matching into the waveguide is provided by four tuning screws in the broad face of the waveguide together with a sliding short circuit which provides a resonant cavity at the output frequency.First fabricate two cover mounting blocks, a diode holder nut and a matching screw block from brass, and solder these to the waveguide, together with a suitable flange. The matching screw holes can be drilled and tapped into the block and guide and only three holes spaced at $\lambda / 8$ are all that are strictly necessary. However, if four or five holes are drilled at about 7 mm spacing, a wider range of output


Fig. 5. Two-section filter has 25 MHz bandwidth at 5.76 GHz . Dimensions in brackets refer to 10.4 GHz in waveguide 16.
frequencies can be accommodated. Two slots for the short circuit can be cut in the narrow face of the waveguide and a $3 / 16$-in hole in the broad face for the connection to the diode support pillar. Underneath the guide the diode-holder nut should be cleared with a 2BA tap which should also continue the thread through the waveguide wall. Screwing a pointed tap right the way through until it touches the top wall will mark the position for the connection hole which can then be pilot drilled through the nut. A diode support pillar can either be turned down from a brass bar or made up of a disc of $16 \mathrm{~s} . \mathrm{w} . \mathrm{g}$. brass or copper soldered to some $1 / 4$-in brass rod. The broad end is tapped 8BA for the connection screw. A disc of insulation material such as Micalex or just Sellotape is placed on the top of the pillar which can then be slid into the waveguide under the $3 / 16$ in connection hole. A thin $3 / 16$-in dia insulating washer is then dropped in the hole to centralize the pillar. A further $1 / 4-$ in insulating washer is used with an 8BA bolt and tag to clamp the pillar and provide electrical connection to the diode.

The diode holder is made of 2BA copper studding with a tapped hole in one and for the diode. Some diodes have untapped ends and a $1 / 16$-in hole is all that is required to mount them in the holder. Two side cheeks of 16 s.w.g. aluminium are bolted to the covermounting blocks with one supporting the BNC input socker. The rest of the input components can then be soldered into circuit.

## Testing

Select a Mullard BXY39D for 5.7 GHz (a BXY40D may give more output around

## Inductor details for Fig. 1

$L_{1}$ five turns 22 s.w.g. wire tapped one turn from "cold" on 3/16-in dia former and slug.
$\mathrm{L}_{2} \quad$ three turns 22 s.w.g. wire on $3 / 16$-in dia former and slug.
$\mathrm{L}_{3}$ half turn 18 s.w.g. wire loop tapped halfway.
$L_{4}, L_{5}$ one turn 18 s.w.g. wire.
$\mathrm{L}_{6} \quad 1 / 2$-in of track.
$L_{7}, L_{8}$ one turn 18 s.w.g. wire.
$\mathrm{RFC}_{1} 10 \mu \mathrm{H}$.
$\mathrm{RFC}_{2}$ two turns 26 s.w.g. on ferrite bead.

6 GHz ). This should be screwed into the diode holder using a dab of thermal grease on the threads. The holder is then screwed into the multiplier unit so that the diode flat contacts the bottom of the support pillar firmly, but not too tightly. A lock-nut should then be tightened on the diode holder.
With the diode in place the v.h.f. source can be connected and powered. A 96 MHz crystal will result in an output of the driver source at 384 MHz , which when fed into the multiplier produces a comb of frequencies from about 4 to 10 GHs with a spacing of 384 MHz . Some products of 96 MHz and 188 MHz are also present, depending on the purity of the driver output.
Alignment of the multiplier is best carried out with a spectrum analyser, but if the filter described below is made it is possible to align the multiplier with only a diode detector or power meter. Using the analyser the required final frequency is displayed together with the two adjacent 384 MHz spaced frequencies. Frequencies every 96 MHz will also be present about 30 dB below the 384MHz harmonics. First slide the short circuit in or out until maximum power is obtained at the output frequency. Then adjust $\mathrm{C}_{19}, \mathrm{C}_{30}$ and $\mathrm{R}_{21}$ again for maximum power of the output. There is some interaction between $R_{21}$ and the capacitors; $R_{21}$ should be set in several positions whilst $\mathrm{C}_{19}$ and $\mathrm{C}_{20}$ are adjusted for optimum each time. When no more improvement in output can be obtained from the input tuning, a 6BA tuning screw can be tried in each hole in turn until the best output is obtained. It is then left in the hole. More screws should be tried in the holes until the required output frequency is peaked about 6 dB more than other harmonics with the $\pm 96 \mathrm{MHz}$ products well down, as shown in Fig. 4 (top). Output purity can be much improved with the filter, see Fig. 4 (bottom).

## Filter

The filter is a two cavity design, adjustable from about 6.1 to 5.2 GHz with the dimensions given. The design was achieved by accident as a gross mathematical error resulted in incorrect theoretical dimensions which work
well in practice. Basically if a post is put in the centre of waveguide it creates a large susceptance which reflects the incident wave. If another post is a quarter wavelength away from the first it will create an equal and opposite susceptance which will cancel the effect of the first at one frequency. Two such pairs of posts spaced at a quarter wavelength will cancel any residual susceptance giving unhindered matched transmission to the design frequency. This simple theory doesn't work exactly in practice and the modified dimensions should be adhered to. The dimensions can be scaled for other frequencies, remembering to scale waveguide dimensions to $\lambda_{\mathrm{g}}$ rather than the free-space wavelengths. Scaled versions have been made up to 11 GHz and perform just as the 5.7 GHz version shown in Fig. 5.
Construction is fairly obvious and for quick prototypes the posts need not be soldered into the guide but just pushed into tight fitting holes drilled through the guide. As the 4BA tuning screws are screwed in, the pass-band frequency is reduced and the response at midadjustment is shown in Fig. 6. Without a spectrum analyser the filter can be adjusted first on a fundamental signal source such as a Gunn diode ${ }^{3}$ or klystron whose frequency has been adjusted using a wavemeter. Once the filter is aligned on the correct frequency the multiplier can be adjusted for maximum output through the filter. The filter can the be slightly readjusted for maximum


Fig. 6. Response of two-cavity filter as measured at mid-adjustment of the tuning screws
output on the exact frequency multiple; it is unlikely that the wrong harmonic has been chosen unless the original frequency measurement using the wavemeter was more than 188 MHz out of true. Provided an output of 3 to 5 mW is obtained through the filter it is unlikely that any spurious products are present.

A single cavity version of the filter can be made for simple equipment when total suppression of other products is
unnecessary. The suppression of out of band products is about half that of the larger two-cavity design.
The multiplier input will accept frequencies in the range 350 to 450 MHz and so 432 MHz low-power f.m. transmitters can feed into the multiplier via an attenuator, although for amateur band use not all the bands can be covered as would be possible with a single 384 MHz generator. It is also possible to change crystals in the generator to produce local oscillator frequencies suitable for use with low intermediate frequencies. But to obtain the best results high intermediate frequencies ( 140 MHz ) should be used to reduce local oscillator noise getting into the receiver. This does mean that a separate local oscillator is required for each band, but for a low-power transmitter a single modulated u.h.f. source can be used with several multipliers for all the bands.

A printed board pattern and component layout appear on page 63.

## References

1. Lean, G.D. Simple solid-state converter \& tripler, Radio Communication 1976, page 506.
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3. Hosking, M. W. Microwave voice link, Wireless World, vol. 83, October 1977, pages 49-52, and November 1977, pages 69-71 \& 92.
continued from page 40
them situated peripherally and at present served with Radio 4 by local m.w. booster stations will get a much weaker signal and one more vulnerable to electrical machine noises.

But what about the discriminating public; i.e., the people who appreciate the merits of the f.m. service and are prepared to pay for it? They have already found that many BBC programmes are not available on v.h.f. It is all very well to be repeatedly reminded that with three-waveband receivers they can get all that the BBC offers, but these reminders studiously refrain from mentioning that owing to the increasing practice of subdivision many of the programmes are, and will continue to be, available only on a second-class service, i.e.,
(1) No stereo
(2) Lo-fi (restricted audio frequencies)
(3) Greater liability to noise

As regards those programmes that are on v.h.f., in fairness to Mr Redmond again I must recall that his impatient awaiting was for portable v.h.f. radios. Ever since v.h.f. receivers became available in quantity, some of the better-class non-portable models have
included instant tuning. The wonder to me is that anyone has the nerve to market any such model without inis facility. And I cannot see any major problem in extending it to portables. In fact, being no longer in touch with the trade I was really astonished by the authoritative statement that such sets were still being awaited - hence my present eruption into these now usually erudite and well-behaved pages.

Having, as I said, every sympathy with the general public in rejecting as totally outmoded and unacceptable a technology that requires from them a skilled and time-wasting procedure every time they want to change programmes, I suspect that there are many households that have several sets, each permanently tuned to one of the limited number of channels they use. Perhaps this wasteful solution is so good for the radio trade as to account for the tardiness of that trade in fulfilling Mr Redmond's earnest hopes.

Finally I will seize this opportunity to protest about what I regard as that design monstrosity advocated - I am sure very reluctantly and unwillingly by the BBC; the combined f.m. and a.m. receiver. The design requirements are
so different that a receiver which works on both is almost two different sets in one. A few years ago an even worse monstrosity was put before the British public: the combined 405 and 625 line TV set, which consisted virtually of two quite different - opposite, really receivers with a very complicated multi-contact (and therefore doubtfully reliable) hard-to-turn switch. I refused utterly to admit such a thing to my home, and did without colour until all-625-line sets were available. The solution to that problem was to provide all the programmes on one system.

The same solution is the only right one for radio. So my impatient waiting is for a broadcasting system in which all the programmes are obtainable on v.h.f. If they, or some of them, are also available on a.m., so much the better. But we ought to be able to go back, if we wish, to the v.h.f.-only receiver. It can be done by making available here more of the 88 to 108 MHz v.h.f. broadcasting band for the purpose of broadcasting, thus allowing the industrious student, the hard-working housewife, the parliamentary enthusiasts (if any), the pop addict and the music lover each to enjoy the benefits of f.m. stereo undisturbed.

# Tunable audio equalizer 

Flexible parametric equalizer with variable Q
by Martin Thomas

MOST AUDIO EQUALIZER circuits represent a compromise between cost, facilities and ease of use, and the Baxandall tone control has been by far the most successful design. For domestic audio equipment its simplicity and ease of use outweigh its disadvantage of providing only a limited degree of equalization, although the circuit can be modified to increase its flexibility ${ }^{1}$. Clearly, however, the "bass" and "treble" subdivision of the audio band is insufficient for many purposes, and the graphic equalizer approach of having a larger number of frequency bands becomes necessary. The only problem with this approach is that a large number of controls must be used to cover the audio band if the individual frequency bands are narrow, so even with this circuit the number of controls is a compromise.

Unquestionably the most versatile equalizer is the parametric, or tunable, type. In its simplest form it can consist of only a single boost/cut element, but its centre frequency can be varied continuously over a wide range (possibly over the whole audio band), and the $Q$ can also be varied so that either a broad or a narrow frequency band can be equalized. This approach allows almost any equalization requirement to be met with only a small number of such elements, and since the elements are iden-


Fig. 1. Basic equalizer design shows how to achieve either boost or cut with a single active element.
tical, no more than are actually needed can be connected together for any particular application. A parametric equalizer may not be so straightforward to use as a graphic equalizer, but once you become accustomed to the rather different controls it's much easier than you might expect.

Circuits of this type have been around

Three years ago, Martin Thomas left
Cambridge University, having collected the B.A. and M.A. degrees in Natural Sciences and a Ph.D. in neurophysiology, to become first a research fellow and later an assistprofessor at Boston University. Now, he's returning to the UK to join the Physiology Department at Oxford University.

His audio interests developed while he was at Cambridge, and he designed the prototype for the equalizer there. He says his research activities aren't directly related to his audio and electronics interests, although in practice there's a lot of overlap between them. Using ion-specific dyes to follow changes in ion concentrations inside nerve and muscle cells during excitation is basically a technical problem. "I had to build a sensitive microspectrophotometer to be able to resolve the very small changes in dye absorbance, and it involved a fair amount of electronics.'

Would he ever consider moving out of research and into industry? "That depends. Most companies really aren't very interested
in my type of background, and I'd probably have to set up a business of my own. But I certainly wouldn't rule out doing that at some time in the future. "

for a number of years, but relatively little has been published about them. In this article I shall take the opportunity to discuss some aspects of the design theory, in addition to describing my own design. The circuit has continuously and independently variable centre frequency, boost/cut amplitude and Q , and also allows a choice of two different sets of boost/cut amplitudefrequency response curves as the control setting is varied; more about that later.

The circuit for a tunable equalizer can be broken into two sections, by which point the basic design is almost complete! The first problem is how to use a single active element either to boost or to cut a given frequency range, and this can be achieved by the circuit shown in Fig. 1. The filter used in the present design is phase-inverting, so its output is connected to the non-inverting input of the amplifier to give overall negative feedback. With this connection there is a gain of two from the filter output to the amplifier output, so the filter transfer function is specified as -G/2 to express the system transfer function in its simplest form. When the boost/cut potentiometer is at either end of its travel, the filter is entirely in either the forward or feedback signal path, giving transfer functions of $-(1+G)$ and $-1 /(1+G)$ respectively. An exact expression for the transfer function at other control settings will be developed later, but for now notice that when the control is at its midpoint, the forward and feedback contributions will be equal, giving a transfer function of -1 ("flat"). An extension of this circuit to include several filters and potentiometers yields the basic design for a graphic equalizer, of course, and a typical circuit is described in ref. 2.

The second problem is the design of the tunable filter. In theory this is very simple, but there are several practical difficulties. Although capacitors can be switched to change the frequency range, the variable control clearly has to be resistive, which rules out some otherwise very promising circuits such as the multi-feedback filter ${ }^{3}$, since the $Q$ will then also vary. The Wien-bridge configuration does meet this requirement if the resistors in the forward and feedback arms of the bridge are varied together ${ }^{3}$, but the $Q$ is sensitive to mismatch between these resistors. As the
sensitivity increases with Q , the circuit is suitable for use only at low $Q$, and the long-term reliability is questionable, once the resistor tracks start getting dirty!
The state-variable filter, which is synthesized from integrators, meets the
requirements very well, and has the additional advantage that it is inherently stable even at high Q . Its only drawback is that it uses three operational amplifiers rather than one, but the number of passive components is almost the same as for other circuits,


Fig. 2. "State-variable" bandpass filter is inherently stable even at high $Q$, (a). Modification for constant centre-frequency amplitude, (b). Varying Ry gives independent control of $Q$.

Fig. 3. Circuit diagram for a single-section tunable equalizer. Ganged resistors $R_{21}$, 22 determine centre frequency, together with range switch $\bar{S}$. $\bar{B}$ control is $\bar{R} \overline{19}_{19}$ while $Q$ is varied with $R_{20}$. Fig. 5 illustrates function of $\bar{S}_{2}$
and it has been chosen for the present design. Fig. 2 shows the circuit diagram. Further information on state-variable filters is given in the appendix and in ref. 4, but the basic equations are reproduced below. Referring to the component values in Fig. 2, the transfer function is

$$
\begin{gathered}
\frac{V_{0}(s)}{V_{i}(s)}=-\frac{R_{2}\left(R_{3}+R_{4}\right)}{\left(R_{1}+R_{2}\right) R_{4}} \times \\
\frac{R_{6} C_{2} s}{R_{5} C_{1} R_{6} C_{2} s^{2}+\left[R_{1}\left(R_{3}+R_{4}\right) /\right.} \\
\left.\left(R_{1}+R_{2}\right) R_{4}\right] R_{6} C_{2} s+R_{3} / R_{4}
\end{gathered}
$$

and the bandpass centre frequency is

$$
\omega_{\mathrm{o}}=\frac{R_{3}}{R_{5} C_{1} R_{6} C_{2} R_{4}^{+}}
$$

For $R_{3}=R_{4}$, and $\omega_{0}=1 / R_{5} C_{1}=1 / R_{6} C_{2}$, the transfer function becomes

$$
\begin{gathered}
\frac{V_{\mathrm{o}}(s)}{V_{\mathrm{i}}(s)}=-\frac{2 R_{2}}{R_{1}+R_{2}} \times \\
\frac{s / \omega_{\mathrm{o}}}{s^{2} w_{0}{ }^{2}+\left[{ }_{2} R_{1} /\left(R_{1} /\left(R_{1}+R_{2}\right)\right] s / \omega_{\mathrm{o}}+1\right.} \\
=-\frac{2 R_{2}}{R_{1}+R_{2}} \times \\
\frac{\omega_{\mathrm{o}} s}{s^{2}+\left[2 R_{1} /\left(R_{1}+R_{2}\right)\right] \omega_{\mathrm{o}} s+\omega_{0}^{2}}
\end{gathered}
$$

Comparison of this last equation with the generalized second-order bandpass transfer function

$$
\frac{V_{0}(s)}{V_{i}(s)}=\frac{\omega_{0} A_{0} s / Q}{s^{2}+\omega_{0} s / Q+\omega_{0}^{2}}
$$




Fig. 4. Effect of varying $Q$ with boost/cut control at maximum boost:
shows that Q is $\left(R_{1}+R_{2}\right) / 2 R_{1}$, and the centre-frequency gain $\mathrm{A}_{0}$ is $-R_{2} / R_{1}$. By varying $R_{5} C_{1}$ and $R_{6} C_{2}$ together, it is thus possible to vary $w_{o}$ independently of $Q$ and $A_{0}$. The $Q$ will change if there is any mismatch between these two time constants (although $A_{0}$ will remain constant), but you can see from the transfer function that the sensitivity does not increase with $Q$, and hence accurate component matching is not necessary.

The $Q$ can be varied independently of $\omega_{0}$ by varying $R_{1}$ or $R_{2}$, but this will also alter $A_{o}$, and the relation between $A_{o}$ and $Q$ is non-linear. Fortunately, a simple modification to the basic circuit, as shown in Fig. 2(b), overcomes these problems. Resistance $R_{1}$ is replaced by two resistors, $R_{x}$ and $R_{y}$, so $A_{o}$ is now $R_{2}\left(R_{\mathrm{x}}+R_{\mathrm{y}}\right) / R_{\mathrm{x}} R_{\mathrm{y}}$. Resistors $R_{\mathrm{x}}$ and $R_{\mathrm{v}}$ also form an attenuator for the input signal, the gain being $R_{y} /\left(R_{x}+R_{y}\right)$. The overall filter gain is the product of these two terms, i.e. $-R_{2} / R_{x}$, hence by varying $R_{y}$ the $Q$ can be varied independently of the centre-frequency gain. The only disadvantage of this modification is that the overall centre-frequency gain of the filter with the component values used in the final design is only 0.5 , so additional amplification in the filter path is necessary. The extra amplifier is placed before the filter, where it also provides the necessary low-impedance source.

The complete circuit is shown in Fig. 3. A low-impedance source is provided by $\mathrm{IC}_{1}$ for the boost/cut control and its associated amplifier $\mathrm{IC}_{2}$. The input amplifier for the filter is $\mathrm{IC}_{3}$, and $\mathrm{IC}_{4}-\mathrm{IC}_{6}$ comprise the filter itself. Although the circuit may appear elaborate, the number of passive components is relatively small, and the size and component count can be reduced further if dual or quad ICs are used. It should be borne in mind, however, that the circuit was designed around the LM318, which is a high bandwidth device. Use of other ICs will result in inferior performance at high frequencies, although it may still be satisfactory for many applications,
and possible substitutions will be discussed later on.

The present design is a generalpurpose one, but the range of centre frequencies, boost/cut and $Q$ can easily be modified if required. The centre frequency is determined by the ganged variable resistors $R_{21}, R_{22}$ and by capacitors $\mathrm{C}_{4}$ to $\mathrm{C}_{9}$ (see Fig. 3). The variable frequency range is just over tenfold, and capacitor switching gives a total range of three decades, the nominal frequency ranges being 30 to 300,300 to 3,000 and 3,000 to $30,000 \mathrm{~Hz}$. The range switch $S_{3}$ should be a make-before-break (shorting) type, so that the capacitative feedback paths around $\mathrm{IC}_{5}$ and $\mathrm{IC}_{6}$ are not interrupted during the instant of switching, otherwise the circuit could oscillate. The sliders of $R_{21}$ $\& \mathrm{R}_{22}$ should be connected to the clockwise end of their track, so that there is a d.c. path through the control even if the slider loses contact with the track, as the control has to provide a path for the input bias currents of $\mathrm{IC}_{5}$ and $\mathrm{IC}_{6} . A$ logarithmic control has been specified, although it will have to be turned anticlockwise to increase the frequency, whereas a clockwise law would be preferable. A clockwise law can be obtained by using a dual antilog control (in which case the sliders should be connected to the anticlockwise ends of the tracks), but this component may not be readily available from most suppliers.

The boost/cut range is determined by the gain of $\mathrm{IC}_{3}$ and the attenuator at the input of $\mathrm{IC}_{4}$. There is a gain of two from the output of the filter network (at $\mathrm{IC}_{5}$ ) to the output of the circuit (at $\mathrm{IC}_{2}$ ), and a two-fold attenuation at the centre frequency through the filter itself, so the overall gain through the filter path at the centre frequency is given by the gain of $\mathrm{IC}_{3}$, which is 10 with the component values shown in Fig. 3. Reference to Fig. 1 shows that the maximum boost and cut are $-(1+10)$ and $-1 /(1+10)$, i.e. $\pm$ just over 20 dB , but these values could easily be modified by changing the gain of $\mathrm{IC}_{3}$. which is of course $\left(R_{8}+R_{9}\right) / R_{9}$. Input
bias current for $\mathrm{IC}_{3}$ is provided by the boost/cut control $\mathrm{R}_{19}$ and by $\mathrm{R}_{7}$, which provides an independent path in case of poor slider contact in $\mathrm{R}_{19}$.

The method of $Q$ variation is as already described with reference to Fig. $2(b)$, and $R_{y}$ is represented by $R_{20}$ and its series resistor $R_{11}$ in Fig. 3. The $Q$ range of the circuit is 1 to 30 with the component values shown, and although the upper limit is unnecessarily high for many applications, the value of 30 was chosen simply because it can be achieved over the entire audio range when LM318s are used. It can be reduced by increasing $R_{11}$. Once again we have a law problem with the control; if $R_{20}$ is a logarithmic control, the $Q$ will increase as the control is turned anticlockwise. An antilog control can again be used to obtain a clockwise law (slider now connected to the anticlockwise end of the track), or a range of fixed, switch-selected $Q$ values can be obtained by replacing $R_{20}$ and $R_{11}$ by a series of fixed resistors, $Q$ being

$$
\frac{R_{12}}{R_{10} / /\left(R_{20}+R_{11}\right)}
$$

Obviously it is useful to be able to switch out the filtering, and this is achieved by $S_{1}$, which simply shorts the non-inverting input of $\mathrm{IC}_{2}$. If the d.c. output of the $\mathrm{IC}_{5}$ is not exactly zero, the d.c. output of $\mathrm{IC}_{2}$ will shift when $\mathrm{S}_{1}$ is closed, but the shift on the prototypes was only a few millivolts and did not cause any audible effects. In fact, the circuit is d.c. coupled throughout (apart from the input to $I C_{1}$ ), and it may be advisable to add a coupling capacitor at the output of $\mathrm{IC}_{2}$ if the possibility of output offset cannot be tolerated.

The output resistor $\mathrm{R}_{6}$ is not for protection, but rather to isolate any capacitative load from $\mathrm{IC}_{2}$ to ensure stability. Capacitors $\mathrm{C}_{2}$ and $\mathrm{C}_{3}$ also help to ensure stability by rolling off the amplitude response above 100 kHz . The only other stability precaution - but which is perhaps the most important is to decouple the $\pm 15 \mathrm{~V}$ supplies with


Fig. 5. Two sets of amplitude-frequency response curves can be generated by the circuit with $S_{2}$ open circuit (a) and with $S_{2}$ closed (b). Corresponding cut curves are symmetrical about log. frequency axis.


Fig. 6. Modified circuit suitable for use with any number of filter sections.
$0.1 \mu \mathrm{~F}$ capacitors. Such decoupling is very important with high bandwidth devices like the LM318, and if any instability problem's are experienced, they can almost certainly be traced to this cause. On the prototypes it was found helpful to connect a capacitor directly between the supply lines in addition to the normal practice of decoupling between each supply line and ground, and the capacitors should of course be sited as close as possible to the ICs. In spite of the impression which may have been gained from these remarks, I was pleasantly surprised by the high stability of the prototypes, and there is no reason to suppose that such results cannot be obtained consistently if standard layout practices are followed.

Performance details of the circuit are given in Figs 4 and 5. These graphs have all been obtained for a centre frequency $\dot{\Omega} / 2 \pi$ of 1 kHz , but the performance for any other frequency in the audio band can be obtained by appropriately shifting the log. frequency axis. Only the boost curves are shown; the corresponding cut curves are symmetrical about the log. frequency axis. Fig. 4 shows the effect of varying the $Q$ control when the boost/cut control is at maximum boost, and gives an idea of the very wide range of equalization curves which can be generated by the circuit. The centrefrequency gain remains independent of the $Q$ control setting as the boost/cut control is varied, but the effect of the boost/cut control on the frequency response is not as straightforward as might be imagined. Fig. 5(a) shows the effect of the boost/cut control when $R_{20}$ is set for a $Q$ of 3 , from which it can be seen that the $Q$ is reduced as the control is rotated toward its centre ("flat") position. By a simple modification to the circuit, however, it is possible to generate the curves shown in Fig. 5(b), where the shape of the response' remains relatively constant as the boost/cut control is varied. The reason why these two families of curves can be. obtained may not be intuitively obvious, but it can be explained by the following analysis.

It has already been shown that the system transfer functions at full boost and full cut are given by $-(1+G)$ and $-1 /(1+G)$. At intermediate positions of the boost/cut control $R_{19}$, both forward (boost) and feedback (cut) signals will pass through the filter. Let the fractional rotation of $R_{19}$ be represented by $x$, such that at full boost $x=0$ and at full cut $x=1$ (see Fig. 1). Resistor $\mathrm{R}_{19}$ will act as a potential divider for the two signals, so the forward signal contribution to the transfer function will be $-(1+(1-x) G)$, and the feedback contribution will be $-1 /(1+x G)$, which yields the system transfer function -$(1+(1-x) G) /(1+x G)$. This reduces to the forms previously given for $x=0$ and $x=1$, and to -1 (flat) when $x=0.5$. Gain $G$ can be written as $A N / D$, where $N$ and $D$ are the numerator and denominator terms of $G$, and $A$ is the centre-
frequency gain through the entire filter pathway (including the $A_{0}$ term, defined previously), which is equal to 10 in the present circuit. The system transfer function now becomes

$$
-(D+(1-x) A N) /(D+x A N)
$$

Setting $\omega$ to 1 for convenience, we have $N=s / Q$ and $D=s^{2}+s / Q+1$.

We are now in a position to explain the curves in Fig. $5(\mathrm{a})$. When $\mathrm{R}_{19}$ is close to the full boost setting, $x$ is close to 0 . As $x$ increases ( $\mathrm{R}_{19}$ rotated away from full boost), the numerator of the transfer function is reduced, but since $A$ is large this reduction will be small compared to the increase in the denominator. Thus when $x$ is close to 0 the transfer function can be approximated by $-A N /(D+x A N)$, which is to say that as $\mathrm{R}_{19}$ is rotated away from the full boost position, the change in frequency response can be accounted for primarily by a change in the pole positions. The denominator of the transfer function is $s^{2}+(1+x A) s / Q+1$, so the effect of increasing $x$ is to reduce the $Q$ to a new value $Q^{\prime}$, equal to $Q^{\prime}(1+x A)$, which explains the curves in Fig. 5(a). An analogous argument can of course be developed to explain the symmetrical form of the corresponding cut curves when $x$ is close to 1 .

Whether or not the behaviour in Fig. $5(a)$ is desirable is a debatable point, but fortunately one can have it both ways! Suppose the feedback end of $\mathrm{R}_{19}$ is grounded instead of being connected to the output of $\mathrm{IC}_{2}$. The circuit will now only boost, and the transfer function will be $-(D+(1-x) A N) / D$. Since $A$ is large, the transfer function can be approximated by $-(1-x) A N / D$ except when $x$ is close to 1 , so the major effect of changing $x$ is now to change the centre-frequency gain without affecting the $Q$. The response curves obtained under these conditions are shown in Fig. 5(b).

There are several ways of modifying the circuit to obtain these curves, and the method used is to some extent a matter of personal choice, but here are three! First, changeover switches could be used to ground one or other end of $\mathrm{R}_{19}$ to obtain either the boost or cut curves. Second, the gain of $\mathrm{IC}_{3}$ could be made variable, when the curves in Fig. 5(b) would be obtained with $\mathrm{R}_{19}$ at maximum boost. The third possibility is my personal favourite, and I have indicated it on the circuit diagram (Fig. 3). This is to use a centre-tapped control for $\mathrm{R}_{19}$ (I really must apologise for continually recommending obscure potentiometers!) and to ground the tap via $S_{2}$ to obtain the Fig. 1(b) curves. The advantage of this method is that the boost/cut setting is determined only by the control setting, just as before, although the control law will be changed. As will be appreciated from the change in the form of the transfer function, the centre-frequency gain will approach 0dB less rapidly as the control

## State-variable filters

Although the present circuit uses the state-variable approach to provide only a bandpass filter, highpass (HP) bandpass (BP) and lowpass (LP) outputs are available simultaneously, as indicated in the accompanying derivation. Note that the basic form of the transfer function is quite simple, and the final expression is relatively cumbersome only because of the form of the $a_{1}$ and $a_{2}$ coefficients. The derivation also shows more clearly how it is possible to change the $Q$ independently of the centrefrequency gain. Since $a_{2}=1 / Q$, we merely have to vary $a_{1}$ and $a_{2}$ together, which is achieved in the present circuit by a variable resistor $\left(R_{20}\right)$ to ground from the $a_{1}$ and $a_{2}$ summing point. This obviously requires that the two signals go to the same amplifier input, and since the $a_{2}$ coefficient must be positive, $a_{1}$ has to be as well. If this facility is not required, $a_{1}$ could of course be either positive or negative.

A further advantage of the statevariable approach is that it can provide any second-order function, although this has not been exploited in the present circuit. The HP, LP and BP outputs are summed by a further amplifier (see ref. 4 for the system transfer function), which allows the corresponding reject functions to be synthesised. By making the appropriate coefficients variable, it would be possible to generate a continuous range of bandpass and band reject functions within the filter itself, rather than by changing the position of the filter within an amplifier feedback loop as in the present circuit. There may not be much to choose between the two methods, but I preferred the feedback loop method because it can be used with
any kind of filter, and any number of filters can be placed within a single feedback loop as shown in Fig. 6. It also allows the choice of two sets of frequency response curves (see Fig. 5), which may not be so easy to arrange by the other method.
$B P=H P \times-1 / R_{5} C_{1} S$, where $S=\mathrm{j} \omega$
$L P=B P \times-1 / R_{6} C_{2} S=H P \times 1 / R_{5} C_{1} R_{6} C_{2} S^{2}$
$H P=a_{1} \times$ input $+a_{2} \times B P-a_{3} \times L P$
$a_{1} \times$ input $=H P-a_{2} \times B P+a_{3} \times L P=$
$H P\left(1+\frac{a_{2}}{R_{5} C_{1} S}+\frac{a_{3}}{R_{5} C_{1} R_{6}^{*} C_{2} S^{2}}\right)$

$=\frac{a_{1} R_{5} C_{1} R_{6} C_{2} S^{2}}{R_{5} C_{1} R_{6} C_{2} S^{2}+a_{2} R_{6} C_{2} S+a_{3}}$
Referring to Fig. 2, the $a$ coefficients are
$a_{1}=\frac{R_{2}\left(R_{3}+R_{4}\right)}{\left(R_{1}+R_{2}\right) R_{4}} ; a_{2}=\frac{R_{1}\left(R_{3}+R_{4}\right)}{\left(R_{1}+R_{2}\right) R_{4}} ; a_{3}=\frac{R_{3}}{R_{4}}$
Thus the complete highpass transfer function is
$\frac{H P}{\text { input }}=\frac{R_{2}\left(R_{3}+R_{4}\right)}{\left(R_{1}+R_{2}\right) R_{4}} \times$


The bandpass and lowpass transfer functions are obtained by multiplying the high-pass transfer function by
$-1 / R_{5} C_{1} S$ and $1 / R_{5} C_{1} R_{6} C_{2} S^{2}$.
When $R_{5} C_{1}=R_{6} C_{2}, a_{2}=1 / Q$.

is rotated towards its midpoint when the centre tap is grounded. Well, you can't have everything!

This effect can be reduced, however, by connecting a $1 \mathrm{k} \Omega$ resistor between the slider of $\mathrm{R}_{19}$ and ground when the centre tap is grounded, which will mean using a double-pole switch for $S_{2}$. The compensation is not exact, but it reduces the worst-case centre-
frequency mismatch to below 3 dB . The ultimate solution would be to replace $\mathrm{R}_{19}$ by two parallel chains of resistors, one of which is grounded at the centre, and to select a point along one or other chain by a multiway switch. The resistor values would be chosen to obtain equal dB steps between the switch points, and it would probably be much quicker to determine the correct values
by measurement than by calculation!
We can row consider the remaining aspects of circuit performance. When LM318 devices are used, the distortion is extremely low, and it was difficult to make any reliable measurement at midfrequencies. For a $+20 \mathrm{dBm}(22 \mathrm{~V}$ peak-to-peak) output signal at 20 kHz however, I managed to obtain a value of $0.015 \%$, but this fell rapidly as the signal level was reduced. In general, the control settings affected the distortion only insofar as they changed the output signal level. This excellent performance is a result of the very high bandwidth ( 15 MHz ) and slew rate ( $70 \mathrm{~V} / \mu \mathrm{s}$ ) of the LM318, but there is sufficient latitude to allow the use of other devices for many applications

The best alternative devices are the various families of f.e.t. input high bandwidth operational amplifiers, and the circuit performance was also evaluated with one of these, namely the Fairchild $\mu \mathrm{AF} 356$, which has a 5 MHz bandwidth and $15 \mathrm{~V} / \mu$ s slew rate. Using this device throughout, the distortion for $\mathrm{a}+20 \mathrm{dBm}$ output at 20 kHz rose to $0.05 \%$, and when the Q was increased at high centre frequencies, the centrefrequency gain also increased slightly an effect not observed with the LM318. Device substitution showed that the effect, which occurred only at high Q, originated at $\mathrm{IC}_{4}$, and most of the extra distortion was generated by $\mathrm{IC}_{3}$. Both the LM318 and the $\mu \mathrm{AF} 356$ have an input voltage noise of around $15 \mathrm{nV} \sqrt{\mathrm{Hz}}$ at midfrequencies, but the $\mu \mathrm{AF} 356$ may be slightly quieter since its input current noise is lower. I have not given any noise specification for the circuit, since the amplitude and frequency content of the noise will be greatly affected by the. control settings, and to quote one or two blanket values could be misleading. However, I have tried to keep circuit impedances below $10 \mathrm{k} \Omega$ wherever possible in order to keep the noise down to a level where it should be dominated by that of the ICs.
As mentioned previously, the circuit could be made more compact by the use of dual or quad i.cs. A possible i.c. is the Texas TL074 series, but the bandwidth is only 3 MHz , which will limit the performance at high frequencies. By the time this article appears, however, a wider range of quad devices may have become available.

Many applications will call for the use of more than one equaliser section, and the sections can be combined in two ways. The easier method is to connect them in series, and if the connection is. permanent the buffer stage $\mathrm{IC}_{1}$ can be omitted from the subsequent sections. This approach is best suited for a modular design, as it allows each section to be used independently. The other method is for the filter sections to be connected in parallel (as for a graphic equalizer), where $\mathrm{IC}_{3}-\mathrm{IC}_{6}$ and all the controls are duplicated, but share the same $I C_{1}$ and $I C_{2}$. The circuit configuration must be changed, however, to
allow the filter outputs to be combined, and the modified circuit is shown in Fig. 6. Circuit $\mathrm{IC}_{2}$ is now a virtual-earth mixer, which can sum any number of filter outputs without interaction, but to achieve this the outputs have to be sent to the inverting instead of the noninverting input of $I C_{2}$, so we need to make a compensating phase reversal in the filter path. In theory, this could be done by moving the filter input connection from the noninverting to the inverting input of $\mathrm{IC}_{4}$, but we would then lose the interaction which allows the $Q$ to be varied independently of the centrefrequency gain (see appendix). The solution adopted is to rewire $\mathrm{IC}_{3}$ as an inverting amplifier, which has the minor disadvantage that the gain of this stage will interact slightly with the setting of $R_{19}$, but the effect will make no difference in practice. Each filter section can be switched out independently as shown in Fig. 6, or they can be switched out together by a single switch between the common ends of the $\mathrm{R}_{5}$ resistors and the inverting input of $I C_{2}$.

How does the circuit sound? My advice is to build it and find out! At low $Q$, the response can be corrected over a large frequency range by as few as two stagger-tuned sections, and in this mode the circuit is a very useful "shelf" filter. As the Q is increased, the circuit becomes more like a graphic equalizer, and ultimately resembles a musical in-
strument! A wide variety of special effects can be created by tuning one or two high-Q sections up and down the audio band, and at high $Q$ the circuit also becomes a useful notch filter. Obviously this design is too complex for it to pose a significant threat to the popularity of the Baxandall tone control, even though it is a lot more versatile. But if you really prefer the mode of action of the Baxandall circuit, don't worry - this design will give quite a reasonable approximation to it if you tune one section to each end of the audio band and set them both to minimum $Q$. Now all you have to do is to label one control bass and the other one treble. Well, I told you it was versatile!

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## 50 years of "Empire' broadcasting

Each year the callsign of the late Gerald 'Marcuse, G2NM, is re-activated by the Chichester club to commemorate the many facets of his remarkable pioneering activities that extended over the period from about 1912 until his death in 1961. This year the event emphasised that it was Marcuse who during the period 1927 to 1929 provided the first series of broadcasts from the UK aimed at listeners in many parts of the "British Empire." These began in September 1927 some months before the first experimental BBC service from G5SW at Chelmsford and several years before the official start of the old BBC "Empire Service" in December 1932. Wireless World played a prominent part in campaigning for the broadcasts, against BBC opposition.
With Post Office permission, Marcuse broadcast daily from his home in Caterham, Surrey, including concerts and song recitals from a "studio" set up in the home of Percy Valentine and also unofficial relays of BBC medium-wave programmes. Even full-sized orchestras were fitted into the studio and many well-known musicians and singers took part. "Outside broadcasts" included bird songs from his garden and it was the G2NM broadcasts that first enabled listeners in many parts of the world to hear Big Ben. With a 100 ft mast and Zepp aerial he ran about 1.5 kW on 32.5 m and the station was well received in many parts of the world until Post Office permission was withdrawn in 1929 and the role of Empire broadcasting was left to G5SW.

## A British Oscar?

Almost a decade ago an attempt was made by a number of British amateurs to plan the construction of an amateur satellite for inclusion in the Oscar series ("Project Trident"). Although little came of these proposals, the idea has been revived, this time with the emphasis on providing the more technical amateurs with an experimental facility rather than a purely communications aid. The project is being formulated jointly by the University of Surrey UOS-AMSAT group and AMSAT-UK and will be run by the university's Space Studies Research Group, relying on support from industrial and research organisations and with the aim of establishing AMSAT-UK as a flight hardware group in its own right.

A number of suggestions on useful experimental facilities that could be included in such a satellite have been formulated, including the provision of real-time information for h.f operators on the state of the ionosphere by using h.f beacons as "topside sounders." It is also hoped that WARC 1979 will make provision for amateur satellites to carry beacons on microwave bands including


10 GHz . Martin Sweeting, G3YJ0 of the University group emphasises however that as a first venture the spacecraft would have to be kept simple and power consumption of all experiments might need to be restricted to an average of 5 or 10 watts.

## WARC 1979

Although for most countries official proposals for frequency allocations to be formulated at the World Administrative Radio Conference next year still seem to be in a state of flux, radio amateurs have welcomed the news that the latest US proposals (although not necessarily representing final American policy) include three new amateur bands at 10,18 and $25 \mathrm{MHz}-10.1$ to $10.2 \mathrm{MHz}, 18.068$ to 18.162 MHz and 25.11 to 25.21 MHz - and in general represent an attitude favourable to the hobby. However, it is recognized that since the creation of the three separate ITU "regions" in 1947, amateurs in Region 2 (the Americas) have enjoyed significantly more favourable allocations than those in Region 1 (Europe and Africa) where European delegations have often proved among the most hostile to amateur allocations. The Home Office report "Preparation for the WARC 1979" (see July issue, News) notes that the decreased reliance on $h . f$ for international fixed service communications makes it possible to consider additional frequencies for various categories of users, including radio amateurs.

## Spotty sun

Solar activity - and consequently maximum usable frequencies - continue to run ahead of predictions. This points either to an extremely high peak of activity during 1980, possibly even exceeding the remarkable Solar Cycle 19 peak of 1958 or to the peak being reached earlier than 1980. With several h.f daylight radio blackouts this year, with transequatorial paths extending up to u.h.f. and the many auroral events
(averaging almost one day in three), it may well appear that we are already approaching peak conditions. Chris Bartram, G4DGU, however believes that the considerable number of 144 MHz TE-mode contacts reflects the greater number of well-equipped $144 /$. 432 MHz amateur stations resulting from Oscar satellite operations.

## In brief

In an article in the UK FM Group (London) newsletter, Kris Partridge G8AUU proposed the introduction of 12.5 kHz channel spacing in place of the current 25 kHz in the f.m simplex and f.m. repeater sections of the 144 MHz band ( 145.0 to 145.837 MHz ). This year's RSGB National Mobile Rally is at Woburn Abbey on August 6 . . Arthur Milne, G2MI, read his 1000 th GB2RS news bulletin on May 7 ... The Home Office has resumed licensing of the "Phase 3" u.h.f. repeaters and will also consider applications for experimental repeaters on microwave bands although additional v.h.f. repeaters are still excluded . . . Special event "v.h.f./u.h.f." stations, with the prefix GB8, are being licensed by the Home Office through the RSGB

British amateur (maritime) licences for stations on board ship are no longer restricted to crystal control on h.f. bands . . . A beacon station, W6IRT, at Hollywood, California on 28.888 MHz has been licensed for 6 months by the FCC.. The International Amateur Radio Union is sponsoring a special amateur radio training.course in Colombo, Sri Lanka with instructors from West Germany .. Over 70 West German amateurs are operating on 10 GHz . Activity is also reported from East Germany, Switzerland and Luxembourg ... REF reports that 7 repeater stations ( 144 MHz ) are in operation in France, 5 are undergoing tests; 5 are in construction; and 4 in the planning stage. Output ranges up to 100 -watts and heights above sea level to 1,200 metres According to Pierce Healy, VK2APQ in "Electronics Australia," an experimental amateur moonbounce installation of the University of Woolongong was wantonly damaged by vandals early this year. The station ("Project Dapto") has been built up over the past 8 years and may now have to be moved elsew, here ... Evening classes for those taking the Radio Amateur's Examination in December or (with the new syllabus and with multi-choice questions) in May 1979 are being run in many parts of the country with enrolment during early September. Enquiries should be made at local adult education centres ... Yukon, Canada is to use the prefix VY1 ... The Yeovil Mobile Rally is to be held at the STC/ITT Social Centre, Brixham Road, Paignton, Devon, on August 27.

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# Trends in microprocessors 

## An analysis of types now available on the market

by David A. Russell, B.Sc. Computer Technology Ltd

Since the last survey of microprocessors in Wireless World (December 1975 issue) a great many new devices with a wide range of capabilities have been introduced. This article provides a background to the current situation and discusses in general terms the directions that developments seem to be taking.

THERE are a number of starting points that could be considered when attempting to categorise the available devices, such as word width or technology. My own preference is to start from the product/market situation and determine where in the cost, performance and volume spectra the product will be. High volume, cost-sensitive applications will generally use a completely different type of microprocessor system from that used in a high performance, low volume application, even if both systems use 8 -bit words. See Fig. 1, an adaptation of some information pro-

|  | Chips | Quantity per annum |
| :---: | :---: | :---: |
| 4-, 8 - and 16 -bit single chip microcomputers | 1 | $5 k-1 \mathrm{M}$ |
| 8-bit chip sets | $2+$ | 1-100k* |
| 8-, 12- and 16-bit general-purpose systems | 3 | 1-10k |
| 16 -bit high-performance systems | 10 | $1-1 \mathrm{k}$ |
| 2 - and 4-bit slices | 30 | 1-100 |

duced by Intel. Also, the memory and i/o requirements vary considerably, and this affects, for example, the type of memory used and the design of the i/o.
Before considering the details of the various configurations, it is worth looking at the costs of minimum sets of basic I.s.i. parts typically used in various microprocessor systems (see Fig. 2). The wide range of performances is reflected in a similarly wide spread in costs; it will be appreciated that, in practice, there are overlaps between the various cate-
gories shown. These costs do not include any allowance for overheads such as translators or buffers and drivers, printed circuit boards, power supplies and so on. Also, the quantities are assumed to relate to the application; for example, the minimum order quantity, for single chip microprocessors is typically $1000-5000$ pieces because they use mask-programmed r.o.ms., whereas $100+$ volumes are shown for the high performance bit slice systems.
At the design stage, the integrated


Fig. 1. Application markets related to types of microprocessors and relative memory capacity.

## Explaining diagrams Fig. 4 onwards

In the diagrams, the rectangles drawn in thin lines are functional blocks, while the areas enclosed by thick lines represent actual chips. Where a functional block protrudes outside of a thick line, this means that extra logic, external to the chip, is required to take full advantage of the facilities available. Shaded areas imply that the part uses an interface specific to the microprocessor.



Fig. 2. Costs of l.s.i. parts used in microprocessor systems showing the falling trend over a number of years.
circuit manufacturers face specific trade-offs between costs and chip size, gate packing density, gate delays power dissipation and package pin counts. To meet the requirements of the wide spectrum of applications the manufacturers are obliged to produce a range of products having differing implementations of the hardware functional blocks in a microprocessor system, depending on cost, performance, instruction set and flexibility (expandability) objectives. I shall illustrate this in the diagrams by using a standard format for the functional blocks (thin lines) and "overlaying" the actual chip functions (thick lines).
The blocks generally include programme memory (r.o.m., e.p.r.o.m., or r.a.m.); data memory (r.a.m.); peripheral interface logic (general purpose or specific to a particular peripheral); timers (hardware timers are tending to replace software loops) and interrupt or test inputs. For high speed peripherals, a direct memory access facility is often included. These functions usually connect to the microprocessor unit (m.p.u.) via a common bus, and a clock generator and timing circuitry will control transfers across the bus.

Within the m.p.u. there will be an arithmetic logic unit (a.l.u.); the working registers (available to the programmer); internal registers (e.g. temporarily storing the current instruction or the next address); a control r.o.m. or equivalent logic, and instruction decode and sequencing logic (see Fig. 3). The established 6800 family is an example of a system in which the m.p.u. and the other functional blocks are provided by individual packages and the system is expanded by connecting more memory or i/o chips onto the bus.

## Single chip microprocessors

The i.c. manufacturers seem to be agreed that a single chip microprocessor is a device that contains all the essential functional blocks (r.o.m., r.a.m., m.p.u., $\bar{i} / \overline{0}$, timer) to allow it to be


Fig. 3. Typical arrangement of functional blocks in a microprocessor unit.


Fig. 4. Examples of single-chip microprocessors: (a) the 4-bit TMS1000, and (b) the expandable 8-bit 6801
used in low cost, high volume applications such as microwave ovens, washing machines and electronic games. One can identify two basic types available: first, very low cost, nonexpandable microprocessor chips, with fixed capacity of memory and $i / o$. These are devices like the 4 -bit TMS1000 (Texas), the 8 -bit 3870 (Fairchild, Mostek) and the more recent 8 -bit 8021 (Intel), see Fig. 4(a). Because the applications are very cost-sensitive, the manufacturers are producing variations on the theme to meet particular requirements, with extra r.o.m. or i/o on larger chips, and, in the case of Intel's $8022^{1}$, they have integrated much of the external logic normally required in microwave oven applications by including a two-channel analogue multiplexer and an analogue to digital converter on the chip. Intel say that this is the first of a number of 802 X parts that will be designed for specific high volume applications.
The second type of single chip microprocessor is expandable, allowing the use of more memory and/or i/o than is


Fig. 5. Examples of two-chip set microprocessors: (a) the 8-bit 6802 and (b) the expandable 8-bit 8049
included on the basic chip. This would also be useful where the design requires both r.o.m. and e.p.r.o.m.; the e.p.r.o.m. would allow specific customer variants to be produced, while the main programme would be in r.o.m. to reduce cost. Examples are the 8048 and 8049 families (Intel), 6801 (Motorola) ${ }^{2}$, Z8 (Zilog) and 9940 (Texas). See Fig. 4(b) and 5(b). Some of these types are available with serial i/o for distributed processing, and in due course versions with e.p.r.o.m. rather than r.o.m. should be increasingly available, allowing low quantity applications to use single chip microprocessors. Minor variants of these microcomputers can be used as peripheral controllers on microprocessor systems such as the 8080,6800 and Z80. Examples are the 8041 universal interface (u.p.i., Intel) and the 6801 E (Motorola).
It is interesting to consider that the performance of the faster types of microprocessors exceeds that of the early 8 -bit microprocessors, such as the 8080 and 6800 , even though the faster devices contain so much extra logic!

## Two-chip expandable systems

Another approach for obtaining flexibility is to base the system design on chips that split the minimum system into two packages and can be expanded by the addition of bus-compatible devices. The longest established example is probably the F8 (Fairchild). Others to consider are the 6802 (m.p.u. and r.a.m.) used with the 6846 (r.o.m., i/o and timer) from Motorola, the 6500 series m.p.us used with r.o.m., r.a.m., i/o and timer chips from M.O.S. Technology, and similar systems for the National Semiconductor SC/MP and Signetics 2650 (see Fig. 5(a)). More recently "cutdown" versions of single-chip microprocessors have been made available, such as the 8035 , which can then be used with r.o.m., i/o combination chips or e.p.r.o.m., i/o chips from Intel.

As can be seen, with the introduction of the combination memory-andperipheral chips, the various single chip microprocessors and the m.p.u., i/o and memory combinations, the designer has plenty of scope if r.o.m. based systems are required. The choice is more limited if e.p.r.o.m. is needed, but this situation should improve during early 1979.

## 8,12 and 16 bit general purpose microprocessor systems

There are various situations where the previously mentioned systems would not be appropriate. For example, if greater performance is required or if large amounts of r.a.m. are to be used, such as in intelligent terminals or development systems, the familiar microprocessors such as the $8085,6800, \mathrm{Z} 80$ or 9980 would probably be the next types to consider (see Fig. 6). The families generally included selected high speed versions, with the manufacturers leapfrogginc each other as new devices are introduced. The 8085A-2 and Z80A seem to be the fastest available at the moment (it depends on who is running



Fig. 6. Examples of general-purpose microprocessors: (a) the 8 -bit 8085, (b) the 8 -bit Z80, (c) the 16 -bit TMS9980 and (d) the 16-bit PACE or CP1600.
the benchmarks as to which wins!). They will shortly be challenged by the $6809^{2}$ (Motorola) which, like the Z 80 , has a large instruction set and extended register set, some features of which are described below (see Fig. 7).

The applications where these more powerful devices are used will often involve interfacing to a variety of peripherals, and the recent and continuing developments in peripheral controller chips are significantly reducing the design complexity, costs and chip counts incurred. Devices like s.d.l.c./h.d.l.c.* chips, floppy disc controllers, and c.r.t. controllers can replace a whole board of t.t.l. m.s.i. logic. Some of the more recent peripheral controller chips are actually based on universal peripheral interfaces (u.p.i.), so that the specific requirements of a high volume user can be taken into account by modifying the programme in the u.p.i. (e.g. the Intel 8278 matrix printer controller).

Another point to consider is that in some cases it is possible to use one manufacturer's peripheral chips with another's microprocessor, which may
*Synchronous data link control/high level data link control.
be useful where your own manufacturer's device doesn't have the facilities required, or is not available.

## The new 16-bit microprocessors

There is a lot of activity in the 16 -bit microprocessors, with the $8086^{3}$ (Intel) and Z 8000 ( Zilog ) coming onto the scene and the MACS (Motorola Advanced Computing System) in the de. sign phase, to join the existing devices such as the 9900 (Texas), F100C (Ferranti) and more recent 9440 (Fairchild). The 9440, 8086 and Z8000 are all claimed to have performances comparable with powerful minicomputers (i.e. Nova range, PDP 11/45 and PDP 11/70 without cache memory), and have very much larger instruction sets than most 8 -bit microprocessors. In common with the 6809, the software features being emphasized by some manufacturers include the ability to use position independent code (which facilitates the use of r.o.m. libraries such as maths packages, interpreters and so on), the availability of a large number of registers, and instruction sets designed for array and repetitive operations, such as are required in compilers, editors and executives.


Fig. 7. High performance microprocessor systems: top, the 8-bit 6809; below, the 16 -bit 8086 in the "minimum mode" and the "buffered mode".

Another feature of some units is the inclusion of hardware and software controls for use in multi-microprocessor systems.

The 8086 and $Z 8000$ are both able to address more than 64 K bytes of memory, and to achieve the large address ranges both manufacturers use different configurations for small and large systems. The 8086 (1Mbyte addressing) has a pin that is strapped to $\mathrm{V}_{\mathrm{cc}}$ or ground to determine whether the "minimum mode" or "buffered mode" is selected, while the 5 Mbyte version of the Z 8000 will use a 48 -pin package instead of the 40 -pin package used on the standard version (see Figs, 7b, 7c). Whilst such large address ranges may seem out of place in microprocessor applications, with the rapidly increasing capacity of memory chips and the advent of the r.o.m. libraries, addressing beyond 64 Kbytes seems likely to become a useful feature in many applications.

## Bit slice systems

The bit slice families have been developed as an extension to the existing Schottky t.t.l., e.c.l. and c.m.o.s. logic families, to combine the desirable performance characteristics and design flexibility of the logic families with the reduced costs and reduced package counts of l.s.i. Various chips are available that allow the designer to implement the functional blocks within a processor, such as a.l.u. and registers, control p.r.o.m., microinstructions sequencer (which determines the next address of the p.r.o.m.) instruction decode, and memory interface ${ }^{4}$.

In the schematic examples shown in Fig. 8 using the 2901 family (Advanced Micro Devices) the a.l.u., registers and some control is implemented in 4-bit slices, known as slice microprocessors. These can be cascaded to make a system of the desired word width; four of them are used to make a 16 -bit system. The microinstruction sequencing is controlled by a set of chips that are also cascadable 4 bits at a time, although recently a single-chip sequencer has been introduced (2912).

## The author

David Russell graduated from Southampton University in 1969 and completed a sandwich course at A.E.R.E. Harwell the following year. In 1970, he joined CTL, starting in the Circuits and Memories group. After working with semiconductor memories and high speed logic families he moved into the Systems group and was involved in the design of a number of products including power supplies, peripheral controllers and switching units for ultra-reliable systems. More recently in the Product Group. in which he is now the company authority on microprocessors, he has been working in applications using microprocessors in peripheral controllers, and has presented papers reviewing the microprocessor scene at several symposia in the last two years.


Fig. 8. Schematic showing the principle of bit-slice systems, here for example, $1 \times$ 29811, $2 \times 2911$, etc.

These systems can, for example, be used for emulating existing minicomputers, or in the design of controllers for high speed peripherals such as rigid disc drivers ${ }^{5}$. The instruction set to be obeyed by the system is determined by the contents of the control p.r.o.m. The width of this is also in the hands of the designer, and may be in the region of 28-36 bits for small processors or peripheral controllers and 48-60 bits for emulation of powerful minicomputers.

The available families include the 9400 (Fairchild); 745481 (Texas) 6701 (Monolithic Memories) and the 10800 (an e.c.l. system from Motorola), but the market leader is the 2901 family, which has been very widely second sourced. A recent addition is the 2903, a 4-bit slice much like the 2901, except that more registers can be added onto the basic set via expansion ports and multiply and divide instructions are included.

To improve the performance of bit slice systems, more powerful memory control chips are being introduced that include an a.l.u. and registers, dedicated to calculation of the next address, while the main 4-bit slice microprocessor system continues with the current instruction. (This arrangement is also used in the 8086 16-bit microprocessor.)

As for future improvements in performance, the basic (internal) cycle times of Schottky t.t.l. systems probably cannot be reduced much below 150 ns , so some of the i.c. designers are turning towards the use of e.c.l. circuitry inside the slice family chips, while retaining s.t.t.l. or l.s.t.t.l.-compatibility by putting buffers on the chips ${ }^{6}$.

An alternative that may become attractive to the minicomputer designer is to switch to using an e.c.l. bit-slice family to overcome the speed limitations of the other technologies, such as the 10800 (Motorola), especially as translators to s.t.t.l. bus systems are available, as are development systems.

## Conclusion

When microprocessors were originally introduced they were cheap, but slow and very basic, requiring a considerable amount of support logic around them. We are on the verge of a
new phase, where the microprocessor manufacturers can provide practically tailor-made l.s.i. systems, for example at the high volume, low cost end, using the application oriented single-chip microprocessors, and, for larger systems, using peripheral controller chips and standard r.o.m. packages with high performance microprocessors. With the steadily falling cost and increasing performance trends, the point will soon be reached where conventional uses of microprocessors will leave a lot of power to spare, and new and novel uses for them will be devised.

A major problem the manufacturers now face is ensuring that designers are able to use the increasingly more powerful microprocessor in sufficient volume to justify the enormous cost of development.

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# NEWS OF THE MONTH <br> <br> Government conclusion <br> <br> Government conclusion <br> The advantages of citizens-band radio are more than outweighed by the disadvantages 

THE SUBJECT of citizen's-band radio was again raised in the House of Lords, on July 11, and again Lord Wells-Pestell made it clear that the Government had no intention of providing it in the UK. This time, however, his answers were far more relevant to the subject than they were last time (see News, July issue, p47).

Prior to the debate, Lord Torphichen told Wireless World that Lord Tanlaw would be posing a question in the House, and would be avoiding the use of the words "citizen's-band radio" - in the hope that it would be better. received. Lord Tanlaw's question was as follows: "To ask Her Majesty's Government whether they will accept a recommendation of the National Electronics Council to improve public communications by allowing individuals access to the radio spectrum for $A$ to B communication." Lord W-P, saying "No" and introducing the description "citizen's-band radio," replied that the Government remained of the view that the aḍvantages of introducing such a service would be outweighed by the disadvantages.
To this Lord Tanlaw asked whether the Minister had conveniently overlooked that the radio spectrum ignored all national boundaries and was governed only by the law of nature, and if so could he justify his reply when there was no legal or con-
stitutional basis for any nation State to claim a part or the whole of the magnetosphere, or to prevent an individual from having access to it. Would he then say why the UK was one of the few democracies outside the Communist bloc that had not allocated a frequency over which members of the public could communicate freely with one another.
In reply, Lord W-P said that it was the view of the Government not to provide citizen's band radio for a whole variety of reasonstoo many to go into at question time. The Government had taken advice and had looked at what had happened in other countries. "There are many competing demands by the necessary users of radio, by mobile radio and by commercial industrial firms."

Reminding the-House that there was evidence of abuse and misuse in countries that have c.b. radio, he then made reference to part of an Electronics Australia editorial, printed in the RSGB's journal Radio Communication, which told of things that could be heard over the air, such as school kids swopping dirty yarns and prostitutes touting for business. The noble Lord did not think that this was funny and quoted another piece from the editorial: "It seems possible that citizen's band may even have played a key 'role in a recent murder." The final part of his quote said that c.b. radio was becoming
notorious, and many people were suggesting that the authorities should reverse last year's decision and try to suppress it altogether.

Lord Wells-Pestell concluded his answer by saying, "We see no reason to introduce the possibility of that kind of thing here."

When asked by Lord Tanlaw whether he was prepared to say that the examples which he had given did not take place over the telephone system, Lord W-P said that he did not think that the two were to be compared and pointed out that conversations on the telephone took place between two people and were not necessarily heard by a large number of people.

Lord Torphichen then wished to know whether the noble Lord, representing the Government, thought it wise that casual would-be users of radio communication should be forced to use either the already overloaded Post Office radio telephone network or, worse, to misuse the amateur frequencies. Seemingly becoming impatient, Lord W-P repeated that the Government had studied c.b. radio in other countries and had come to the conclusion that the advantages were more than outweighed by the disadvantages.
After the debate Lord Tanlaw told Wireless World that he will still continue to press for c.b. to be heard in the House of Commons. $\square$

## Mini-Nyquist speech prototypes in 18 months

CONTRACTS ARE already being negotiated for the commercial exploitation of a speech processing technique which can transmit at one-seventh of the Nyquist sampling rate. The technique, developed by Brigadier Reginald King with the collaboration of the School of Electronic Engineering under Professor William Gosling at Bath University, reduces speech to an "alphabet" of 27 "letters" which are then transmitted at about 1000 five-bit words per second. One of the problems yet to be overcome is that the samples appear at non-regular intervals and present transmission techniques allow only for regular transmission rates, but Brigadier King told Wireless World that a prototype device, using microcprocessors, would be ready in about 18 months, and that commercial devices would appear in about four to five years if all went well. He emphasised that the technique did not make current techniques obsolete overnight.
Brigadier King, who completed his work during a sabbatical year at Bath University, said his work had been based on a wellknown paper published many years ago in the United States in which the authors described the effects of severely limiting the amplitude of human speech. They discovered that with $100 \%$ limiting, when all that was left of the speech waveform was a series of events corresponding to the zero-crossingpoints in the speech, the intelligibility was still $97 \%$. This meant that although the odd
word was lost the sentences could still be understood. Brigadier King first worked on this and other speech processing techniques at the Royal Military College of Science at Shrivenham eight years ago.

The sound of such "infinitely-clipped" speech was, as Brigadier King says, "pretty awful to listen to," but it aroused a great deal of interest. King and others were sure that the time intervals between zero-crossingpoints in human speech conveyed the bulk of the information, "but there was something missing; some mislaid clue that we had yet to discover."

Then in May last year Bell Laboratories published a paper explaining why researchers into zero-crossing frequency were barking up the wrong tree. The paper "proved" that the reduction of data rates by using zero-crossing was theoretically impossible. A lot of the other researchers turned to other things. King stuck with it. It was after this that he began a year's sabbatical at Bath. He stresses the value of Bath's co-operation. He was given the use of staff and a PDP8 computer. Just as valuable, though, was that he and Professor Gosling were agreed on their approach, and that Bath, too, had done some work on zero crossing.
"We were looking for a way of sampling without involving the Nyquist rate. We had got to dispense with amplitude descriptors and linear processing." The mathematical model was shortly provided in a book by two

Russians: "Distribution of Zeros of Entire Function's." Speech, said King, was an entire function, having real and complex components. "The thing that was missing was the locations of the complex zeros."

The task was to identify one sub-set of complex zeros which would identify speech. The technique was not entirely accurate, said Brigadier King, "But all modulation systems are approximations."

The work so far, using computer simulations, has only confirmed that the technique works. "We've only cracked open the oyster, as it were." Bath is now refining the method, identifying, perhaps, other sub-sets of complex zeros which might improve it. A key to the technique is that, effectively, the packets of five-bit words occur in regular clusters. and this makes further condensation possible. Although there are 27 letters in the current alphabet, alphabets with as few as seven or eight letters have proved intelligible, though unpleasant to listen to.
Brigadier King says the technique is much simpler than current vocoder techniques. According to an Army statement, the equipment could be sold "at less than a tenth of the cost of any other existing system and will be housed in a terminal smaller than a shoebox."
The Army statement went on to say that the details of the technique were classified, but it appears that this is as much for commercial as for military reasons.

# Custom i.cs produced by computer aided design 

EQUIPMENT MANUFACTURERS can now get integrated circuits custom designed and produced for them by a British computer aided design service and chip manufacturing plant which offers speed and convenience as its main features. Conventional draughtsmen's work in the layout of masks is eliminated and layout design time is reduced from weeks to minutes. Customers, it is claimed, can get a price quotation for a given number of completed devices on the same day that they bring in a diagram of a prototype system that is to be integrated. Finished samples of the manufactured devices are available in ten weeks. The convenience comes from the fact that a computer system can provide a quick feedback of information that enables the
customer to check the design process as it is taking place. For example, the computer will run a simulation of a customer's logic system to make sure that what is specified on the system diagram will actually do what the customer requires of it when it appears as a manufactured device.

## Design it yourself

The new service has just been started by GEC Semiconductors Ltd, who specialize in custom designed i.cs, and is called "Cellmos" This name derives from the principle that the customer designs his own integrated circuit using standard "cells" or circuits from a library of circuits taken from standard 4000

## NEB details confirmed

THE NATIONAL Enterprise Board has published further details of the newly-formed microelectronics company into which $£ 50$ million of public money is to be invested. A statement was issued on July 22 saying that, initially, $£ 25$ million was to be invested in a new company called Inmos, and an agreement to that effect had been signed by the NEB, Inmos, and the three founders: Dr Richard Petritz, Dr Paul Schroeder and Mr Iann Barron, of whom more later

The NEB say that provision of the second $£ 25$ million will depend on the achievements of the company. The funding will be in the form of ordinary and convertible preference shares. "Key employees will have the opportunity to purchase ordinary shares in the company. When investment in the company reaches the level currently envisaged, the founders and the future employees could hold up to $27.5 \%$ of the voting shares in the company." The NEB's investment will have a preferred position because of the differentiation of rights attaching to the ordinary and preference shares.

Inmos will concentrate on the next generation of m.o.s. technology, according to the NEB. Their products will include very large scale integration (v.l.s.i.) memory and microcomputer devices. This means the production of 64 K ra.ms, compared with the current maximum of 16 K . Inmos will also, it is hoped, produce microcomputers - central processing units (c.p.us) on a single chip.

The company's headquarters and production will be based in the UK but technological and product development will be split between here and the United States, where the biggest market is. Operations will start simultaneously here and in the USA. A prototype production line will be based in the USA but by 1981 Inmos plan to establish volume production in the UK. The first task will be to establish design teams and plan production facilities. An NEB spokesman said that, as yet, no other appointments had been made than the three founders, but that the company was having "discussions with people about recruitment." By the middle of the $1980 \mathrm{~s}, 4,000$ people would be employed in the UK and 1,000 in the US. The NEB said they were looking at sites though no definite decisions had been made. The statement said
that areas of high unemployment would be given special consideration, and the spokesman said this meant careful study of the North and North-West.

Dr Richard L. Petritz, 55, received a Ph.D. in Physics from North Western University. After lecturing and Navy work he was director of Texas Instruments' semiconductor $\mathrm{r} \& \mathrm{~d}$ laboratory for ten years from 1958. He established TI's UK lab at Bedford. In 1968 he founded New Business Resources to launch new electronics companies and, in 1969 , Mostek. As a consultant he advised the World Bank and the Korean Government in setting up electronic business in the Korean Republic.

Mr Iann Barron, 42, did Army and Air Force research after receiving a Cambridge MA. He was head of systems research in the computer research laboratory of Elliott Automation. In 1965 he founded Computer Technology Ltd, the first UK minicomputer company, and was managing director until 1971. Since then, as a consultant, he has advised the Department of Industry on future developments in computers and information technology.

Dr Paul Schroeder, 38, won a Ph.D. in Physics from Massachusetts Institute of Technology in 1967 and worked for Bell on memory design until 1974. He moved to Mostek becoming, in 1967, director of memory design engineering. He is thought of as a leading expert in m.o.s. dynamic storage devices design.

One of the most interesting aspects of these appointments is that the head of the trio, Petritz, has based his business in Dallas on the finding of funds for new ventures. It is a comment on the willingness of the holders of risk capital to take risks that he has now thrown in his lot with the NEB, though he is said to find the new venture attractive because it will eventually allow the company to regain its independence from the NEB. Another attraction is that labour costs in the UK are lower than in the US, a fact which has also encouraged Japanese investment here of late. The memory products, first off the production line, will be made in the US to build up the company, and the microprocessors will be made in the UK.
series c.m.o.s. logic parts. At present the GECS Ltd library at their Wembley plant runs to about 500 items. Of course, this restriction of the customer's design options to a given library of elements is what "pays for" the gain in speed and convenience.
After a customer's engineer has designed his required system in these standard parts, and perhaps built a prototype in breadboard form, he supplies a logic diagram to GECS. At Wembley an accurate copy of this diagram is made and on it all the cells, or discrete logic circuits, and their connections are given code numbers and letters. From these code symbols a list is compiled which defines the logic completely in known terms and is a full description of the customer's requirements. In addition. the customer can supplement the circuit information with a set of test waveform definitions.
The list. either handwritten, typewritten or in computer readable form, is fed into a GEC 4070 computer to carry out compatibility checks (such as that an $x$-input gate is in fact receiving $x$-inputs). This may take anything from 15 to 30 seconds. If waveforms are supplied the service will run a logic simulation of the circuit on the same computer and return the results of this to the customer for him to verify that the circuit works as he intended and that the data have been transferred accurately. This takes from 1 to 13 minutes according to the complexity of the integrated circuit, and a similar time is needed to print out the results.

Once all this data has been verified by the customer's engineer and approved, it is released to the part of the service which lays out the integrated circuit. The layout process, claimed to be unique, optimises the placing of the cells and interconnections on the chip and then-generates two plots on paper. One plot shows the proposed physical layout of the i.c. with pad positions and so on. The second plot is a diagram of the chip in logic diagram form, and allows the customer to check this version against his original circuit.

## Checking the layouts

At this stage changes can still be made in the initial list to correct errors or modify the circuit. If this is done, the procedure is repeated until the customer's requirement is met. Once the two layout plots are certified correct by the customer's engineer, the layout is translated into magnetic tape format for the preparation of masks on computer-based equipment. If waveforms were supplied, these can be used to produce a test programme for an automatic test equipment used by the service.

The final cost of such a custom-made integrated circuit is determined by the chip area and size of package. The greater the number of cells, the greater will be the chip area, the larger the package and the higher the cost. But GECS claim that, because of the reduction in the time required for design and the convenience of the whole design approach. "the overall cost of developing custom I.s.i. circuits can be significantly reduced for the smaller quantity user."

# Post Office approve phone-line tv system a new aid to the British police 

THE POST OFFICE have accepted for evaluation an application by Aero \& General Supplies, of Nottingham, for a slow-scan television (s.s.tv) system to be used as a private attachment to their public switched telephone network and private circuits. In addition, the British police, who were earlier given technical approval by the Post Office for a similar system for use on private circuits have now put s.s.tv into their research and development programme.

The heart of the system which has been proposed for the public switched telephone network is a slow-scan transceiver called the Robot Model 530. This unit is already in use in a number of phone-line tv systems in America and Canada, and has recently been technically-approved in Spain and the Netherlands. According to Aero \& General Supplies, the Post Office had four months previously similarly approved the system for use on private telephone networks in the UK but policy issues delayed approval for the public network. One may be forgiven for speculating that these policy issues could have had something to do with the fact that a s.s.tv system of this kind could in many cases compete with the Post Office's proposed Viewphone.
The Model 530 s.s.tv system, however, must run the Post Office gauntlet - the usual process of assessments and trials - because the approvals are subject to it meeting their technical requirements, which are to ensure the system's compatibility with the PO's networks and systems. Robot are confident that these requirements, which should involve only minor modifications to the equipment, can be met. These modifications will be discussed later.
Both the police system and the proposed switched-telephone network system, which
are manufactured by Robot Research Incorporated of California, can be used with telephones, or any other "speechcommunication" medium because they only require audio bandwidths to convey all of their picture information. The picture obtained is stationary and updated about every eight seconds (almost like a slide show)


Fig. 1
or can be held as long as required. The frame, in each case, is composed of a 256 line display having $12 \overline{8} \times 128$ discrete picture elements retained in a memory, and each coded into one of 16 grey shades. Although the picture definition does not compare with that of 625 -line fast-scan $t v$, it is nevertheless surprisingly good, as shown in Fig. 1. A normal 625 -line fast-scan tv camera is used to obtain the picture and this is sampled at a slow-scan rate and then transmitted immediately or recorded for later transmission if required. The display may be shown on a normal 625 -line monitor, or even on a domestic tv receiver (slightly modified).
S.s.tv systems have created enormous interest throughout the world and are already being used by security firms, banks, police and meteorologists. They are, for example, ideal for the quick transmission of "mug shots," fingerprints, cheque signatures and for security surveillance. When connected to a telephone answering machine they enable one remote operator to contact any chosen premises, a bank for example, and see a picture of the strongroom within seconds. When a system is connected to a


## More about s.s.tv

In s.s.tv a television picture is slowed down so that it may be contained within audio bandwidths. This slowing down results in a picture having about. 120 lines ( 128 lines in the case of the Model 530) with a scan time of 7.2 (for a 120 -line. 50 Hz system).
Because the total bandwidth of s.s.tv lies well within the audio spectrum, it is possible to convey pictures using normal radio transmitters, telephones or other audio systems. In addition, the signals may be recorded on ordinary domestic tape recorders for later playback or for programme construction.
A slow-scan signal usually consists of a 1200 Hz audio subcarrier which is frequency modulated by the composite video signal. The resultant f.m. signal is normally used by radio amateurs to modulate a s.s.b. transmitter. Figure 3 shows the frequency composition of part of the f.m. signal - a single slow-scan line - in which an audio frequency of 1500 Hz represents a black level, and an audio frequency of 2300 Hz represents a white level. Intermediate shades of grey are represented by the frequencies between 1500 Hz and 2300 Hz . In the case of the Model 530 , each picture element is represented by one of 16 grey shades in a digital memory. The memory being a 65,536 -bit store, made up of sixteen 4,096-bit r.a.ms.
The aspect ratio of a s.s.tv picture is usually 1:1, mainly because the surplus cathode-ray tubes, generally used by radio amateurs for s.s.tv, are round, and the square format used
the maximum available screen area
All synchornisation pulses are transmitted at the 'ultra-black' subcarrier frequency of 1200 Hz and consequently they do not appear on the screen. As shown in Fig. 3, the line scan consists of a 5 ms sync pulse at 1200 Hz followed by the frequency variations representing the light intensities of the visual image which has been scanned. The spot on the monitor screen flies back during the sync pulse penod. At the beginning of each complete frame the 5 ms sync pulse is replaced by a 30 ms frame sync pulse during which the spot resets from the bottom right to the top left of the picture.

Although s.s.tv is almost entirely an amateur development pioneered by an American team headed by Copthorne Macdonald, W4 ZII, in 1958, it was only fairly recently ( 1968 in the USA and 1976 in the UK) that the controlling bodies (the FCC and the Home Office, Directorate of Radio Technology) put it into the standard radio amateur licence. Before about 1975, UK responsibility was with the Ministry of Posts and Telecommunications, who set fairly rigid standards for s.s.tv. These standards are no longer applicable, and providing s.s.tv is confined to the allocated frequency bands 3.5 to $3.8,7$ to $7.1,14$ to 14.35 , 21 to $21.45,28$ to 29.7 and 144 to 146 MHz , and the normal limitations of power and bandwidth are complied with, amateur s.s.tv transmissions may use any standards the operator wishes. This will not, however, be true of any unit proposed for private or public use with Post Office networks.
burglar alarm, the police can not only receive an alarm call, but can see a picture of what is happening. The eight-second interval between samples is sufficiently short for monitoring high security areas, and future developments are likely to include a system which compares one frame with the next to trigger an alarm after any picture-content change, caused by an intruder for example.

A typical s.s.tv system is shown in Fig. 2. This is a one-way system using transmitter and receiver separates. Transceivers can be used for two-way communications.

In the last few months, at least one British police force has carried out experiments with one of Robot's s.s.tv systems to determine its usefulness. They are using Robot's Model 400 which, being the amateur version of the Model 530, has more controls. As one might expect, they will be exploiting the system to the full and many modes and methods of transmission have been investigated - including telephone lines, and v.h.f., and h.f. radio - over both long and short distances.

For the system to be most beneficial to the police, they will be doubling the memory capability of the system and increasing the grey scale from 16 to 64 levels. Certainly, an s.s.tv system having 64 levels would be very useful for the transmission of both pictures and fingerprints. Robot also intend to make this modification some time in the future.
Continued on page 74

Because the Post Office insist on good mains isolation, one modification to the Model 530 will probably be to fit special transformers having screens between primary and secondary windings. The alternative is to add a mains isolation unit to each piece of equipment: this is how the police have solved the problem.

Problems are bound to arise because there is a Post Office line-signalling tone within the 1200 to 2300 Hz bandwidth required for the s.s.tv transceiver. This tone, at 2280 Hz , is used in the most common private (acl3 or $\mathrm{acl5}$ ) and public (ac9 or acl1) networks to seize and release the line during phone calls. A $2280 \pm 15 \mathrm{~Hz}$ receiver is used to sense this tone and it responds, only when the tone is pure and of sufficient duration, by cutting off the call. Since the s.s.tv system is f.m. it may be possible for it to produce a pure tone, which for one reason or another occurs within the bandwidth of the receiver and is, because of the picture content, of sufficient duration. This would also cut out the call.


Fig. 3
Other tones on 2280 Hz are for address signalling (dialling) and will therefore not affect the s.s.tv transmission.

One way in which this problem could be overcome is for the s.s.tv unit to produce a second tone (at 1000 Hz say) so that even if a tone of 2280 Hz was produced, it would not be pure because of the presence of the second tone - this would be interpreted as speech. Alternatively, the whole s.s.tv frequency band may be shifted down a few hertz to avoid 2280 Hz . Robot considered the use of a
second tone but the idea was dropped when it was discovered that. whatever second frequency was chosen, the beat frequencies created either interfered with the picture or fell outside the frequency bands permitted by the Post Office. Eventually Robot chose a bandwidth from 800 to 1900 Hz and are now trying to make this a standard acceptable to the whole of Europe and America. The police are also going to comply with this.

At the moment the model 530 is being retailed by Aero \& General Supplies, but their intention is to distribute it through a franchised network consisting mainly of established closed circuit video dealers.

According to the company, a price cannot, at this stage, be fixed for the equipment if it is used on P.O. lines, but it is anticipated that a phone-line television transceiver, as it will be called, will be offered for less than $£ 1,500$. The equipment is available now for uses which do not require P.O. approval, for less than this, and transmitters and receivers for one way transmission are available for even less.

## Large-size l.c.ds to have longer lives

glass frit sealing, currently recognised as the best method of sealing liquid crystal between glass plates for the production of long-life l.c.ds, has been so well mastered by ITT Components Group Europe that they are now successfully mass producing displays having character heights as large as 13 mm . Few l.c.ds are given a life expectancy of greater than two years. but this UK company feels confident that their products will last for at least five years.

If a l.c.d. is to have a long life it is essential that the seal between the glass plates is impervious to all materials which could contaminate the liquid crystal. There are two types of seal in common use; glass frit and plastic. Since glass is more inert. physically and chemically, than plastic, it can withstand much worse environments, and is consequently expected to exhibit higher reliability. Unfortunately, the glass frit technique. which involves depositing low-softening point glass (frit), in paste form, on to the edges of the glass plates, and firing at about $500^{\circ} \mathrm{C}$, is extremely difficult to master. Difficulties arise because the glass plates must be separated by only about $1 / 2$ thousandth of an inch ( 12 microns), over the whole display surface and, of course, as the display gets larger these become even harder to overcome. Although a number of manufacturers are using the glass frit technique on smallsize displays, Siemens in Germany, Brown Boveri in Switzerland, Electrovac in Austria
and Motorola in the USA, ITT claim to be the first company in Europe and probably in the world to produce large-size displays in quantity.

ITT, although late entering the l.c.d. market, recognised the long-term potential of glass-frit seals and decided. at an early stage, to concentrate on this technique. The real importance of the technique is expected to be seen in the future when the ambient temperature range of l.c.ds is extended. With the current. restricted, operating temperature range (typically 0 to $60^{\circ} \mathrm{C}$ ) the difference in reliability between glass-frit seals and plastic seals is not market.

ITT intends to achieve, within the next five years. a $25 \%$ share of the European market and a $10 \%$ share of the World market for displays having a minimum 12 mm character height. The actual display that they are investing in is a field effective (twisted nematic) l.c.d. measuring $82 \times 34 \mathrm{~mm}$ overall. (For a description of the constructions and types of l.c.d. available see p230, May 1975 issue of Wireless World.)

General production area for ITT's liquid crystal displays. To achieve high yields it was accepted that airborne particles had to be virtually eliminated, and this purpose-built clean room area was constructed to create ultra-ciean conditions for all critical operations.


The company is currently supplying l.c.ds at a rate of about 1,000 pieces per week, and they forecast that this figure will be up to 3.500 by the end of 1978 and 10,000 by the end of 1979. At the moment, however, ITT admit that yields are unrealistic and a figure of $60 \%$ is the most any manufacturer could expect to obtain. They are working to improve the performance of twisted nematic l.c.ds, in particular to extend their operating temperature range. In the future we can expect to see larger displays with drive circuits mounted directly on them.

At ITT's Central Research Laboratory at Harlow, completely new types of l.c.d. are being developed. These include a display based on cholesteric I.c. which contains a dye enabling it to have its own intrinsic colour and avoid the need for polarizers. Research is also progressing into the use of smectic l.c. materials in displays. These l.c.ds would have memory and continue to display a message after removal of the drive signal.

## News in Brief

The Radio Industries Club of Great Britain announce that Howard Thomas, Chairman of Thames TV, is to take over from Douglas Muggeridge, Deptuy Managing Director of BBC Radio, as the Club's President. John Record, Thorn Industries' National Sales Manager, will take over from Alan Pederson, of Antiference, as the Club Chairman.
Admiral of the Fleet Sir Edward Ashmore, G.C.B., D.S.C., Chief of the Defence Staff until his retirement last year, has accepted an invitation to join the Board of Racal Electronics Ltd.
Robert Telford, Managing Director of GECMarconi Electronics Ltd, has been knighted for his services to export. Under Sir Robert's guidance the GEC-Marconi Electronics Group's overseas sales have risen from about £ 18 million in 1968 to over $£ 230$ million, and since 1966, the Group has been awarded 19 Queen's Awards, of which 11 were for export.
A Marshall (London) Ltd have moved their offices, sales and stores departments to new premises at Kingsgate House, Kingsgate Place, London NW6.

# Mains interference and filtering 

## Protecting logic systems from mains borne noise

by I. Catt and M. F. Davidson (CAM Consultants), and D. S. Walton (Icthus Instruments Limited)

Although great trouble is taken when designing d.c. power supplies for large digital systems, interference on the mains power lines is often overlooked or underestimated. This article outlines the types of noise that occur, and describes a suitable filter for overcoming the problem.

INTERFERENCE from the mains can be classified into three types. Balanced, where the noise signal travels equally down the live and neutral lines, and the earth line acts as a return path. This is often called common mode noise, and it causes earth currents which can upset high gain linear circuits. Unbalanced, where the noise signal travels down the live line and back on the neutral line, leaving the earth line unaffected. This is often called differential mode noise and may be lost or suppressed in the d.c. power supplies of a circuit. It can be shown that any complex signal travelling down the three lines can be resolved into a common mode component and a differential mode component. Mains borne radiated noise, which can be both balanced and unbalanced, enters the equipment via the three power lines, and then radiates directly into the logic.

## Susceptibility of a digital system to mains noise

Differential mode noise on the live and neutral lines tends to be smoothed out at the unregulated and regulated d.c. points. However, because large value capacitors have a significant series inductance, some of the noise, if not suppressed before the transformer primary, will pass through the power supply and cause transient variations which can disrupt the logic operation. Screening the transformer will not help significantly because differential noise is fed through the transformer from primary to secondary and not via interwinding capacitance.

Common mode noise, however, does pass through the transformer via interwinding capacitance, so a screened transformer will help to suppress the interference. The typical inter-winding capacitance for an unscreened transformer is 100 pF . With screening, this falls to around 1 pF . Any common mode noise that does pass through the transformer tends to raise the positive voltage relative to 0 V , and tends to lift the
level of 0 V at some points but not others. The use of a choke rather than a link between 0 V and earth will help to render the logic immune to this noise because all of the logic supply lines will tend to move together. Therefore, any common mode noise which does pass through the regulated d.c. supply will see three loads in series. The link between the earth line and frame, the link between frame and 0 V , and the line carrying 0 V across the logic to the link. If the 0 V to frame to earth link has a high impedance, such as a choke. most of the noise will appear harmlessly across it. The d.c. resistance of the choke should be below $0.1 \Omega$ to conform to BS3861. If, however, the 0V to earth link is a low impedance, the noise will lift the potential at one point on the 0 V grid. This will degrade the logic signals and tend to cause a malfunction.

Mains borne radiated noise, which is emitted from the mains wiring, can be greatly reduced by screening the live and neutral lines, and earthing the screens to the frame. Another method of reducing the radiated noise is to include a mains filter at the point where the power lines enter the circuit module. A third approach is to have mains lines in the module separated from the vulnerable logic by correctly earthed bulkheads. Once past the mains filter, the mains cables do not normally need to be screened. If power is switched on and


Fig. 1


Fig. 2
off to loads within the module these power lines should also be screened.

## Magnitude of mains borne interference

A reasonable noise amplitude to design against in a 240 V single phase supply is 2 kV over the range 100 kHz to 10 MHz . The noise may be common mode or differential mode and can be caused by, for example, switching off an electric motor which is on the same supply. It is wise to assume large amplitude noise above the nominal 240 V of the line, and also that it is both common and differential mode.

The source impedance of the noise is difficult to determine, but it is safest to assume a very low source impedance of, say, two ohms. Both of these assumptions might surprise the reader, but they have been chosen to give a reasonable safety margin.

## Mains filtering

Mains filters are constructed from capacitors and inductors. The capacitors require an adequate voltage rating and also have to be able to dissipate the heat generated from the maximum current. By Ohm's law, $V=I Z$ so $I=$ $V / 1 / 6 \mathrm{fC}$ which is 240.300 . C. Therefore, for a 1 fiF capacitor the current is around 100 mA . It is worth noting that the mains filter can significantly alter the power factor of a load. The series inductance of such a capacitor can be as low as 10 nH , which is very satisfactory in this application.

With inductors, it is important to make sure that they do not saturate at the peak current. If the power taken by a circuit is around 1 kW , the r.m.s. current is around 4A and the peak current may be as high as 10A. A choke which saturates at 20 A and has an inductance of $200 \mu \mathrm{H}$ can have a parallel capacitance as low as 10 pF which again is satisfactory for this application. The d.c. resistance of such a choke is around 0.1 ohms, so it is possible to meet the safety requirements even if the choke is placed - in the earth line.

A mains filter is a low pass device and the usual circuit is a double $\pi$ as shown in Fig. 1. High frequency signals entering either the live or neutral lines see a high impedance inductor ahead and are shunted to earth through a low impedance capacitor. Typically, at


Fig. 3


Fig. 4

1 MHz , with $1 \mu \mathrm{~F}$ capacitors and $200 \mu \mathrm{H}$ inductors, $Z_{c}$ is around $0.2 \Omega$ and $Z_{L}$ is around $\mathrm{lk} \Omega$. If the source impedance of the noise is $1 \mathrm{k} \Omega$ or higher, the noise is attenuated by a factor of $1 \mathrm{k} \Omega / 0.2 \Omega=$ 5,000 or about 70 dB . If the source impedance of the noise is low, the first capacitor is ineffective, but the potential divider formed by the inductor and the second capacitor still gives around 70 dB attenuation at 1 MHz .

Any high frequency signals approaching from either direction see a short to earth, and a high impedance series inductor blocking the path ahead. This arrangement works well if noise is the only problem. But, because the input and output of both lines are connected together at high frequency, an "earth loop" pickup of externally radiated noise can occur. Also, the possibility of electrostatic discharge into the circuit is much more likely. From the point of view of radiated noise
the circuit in Fig. 2, which blocks the passage of high frequency signals down all of the lines, is preferable. It makes the path down the lines an open circuit to high frequencies, and tends to isolate the system. This filter does however cause a disquieting amount of earth current. If the capacitors are $1 \mu \mathrm{~F}$, the total earth current is about 150 mA . With the circuit rearranged as in Fig. 3, the noise suppression is virtually unaltered and the earth current is reduced to around 2 mA . This circuit is also safer because there are no components linking live directly to earth, and a single shorted capacitor does not present a safety hazard. The two resistors discharge the capacitors if the filter is disconnected from the mains.

## Commercial mains filters

Medium performance commercial filters have a specification of around

60 dB insertion loss in the region of 1 MHz . A filter of this type would cause 2 kV of noise to be reduced to a mere 2 V , which would easily be suppressed on its way through the power supply. Higher performance filters, specified at 100 dB insertion loss, reduce noise of 2 kV down to an unnecessarily low 20 mV . The most serious shortcoming in commercial units is when the windings of both chokes are on the same core as shown in Fig. 4. The theory is that the currents in the live and neutral lines, being equal and opposite, create zero total magnetic flux in the choke. This means that for a heavy live and neutral current the core will not saturate, and a single toroid can be used in place of two separate and more expensive chokes. However, instead of two chokes there is a transformer which will not stop any differential mode noise. The author has not seen a manufacturer's specification where insertion losses for both differential and common mode noise have been unambiguously defined. The insertion loss is the ratio of the output amplitude from the filter into a load divided by the input from a source with the same impedance. Sometimes the specification does not state this impedance so it is virtually impossible to determine the filter performance. The correct specification is the minimum insertion loss when the source impedance and load impedance are independently varied from zero ohms to open circuit.

This article is based on material from a book "Digital Electronic Design", by the above authors, published by C.A.M. Publishing, 17 King Harry Lane, St Albans, Herts, price £8.00 including postage. For information on the availability of the mains filters described write to Icthus Instruments Ltd, Princesway, Team Valley Estate, Gateshead. C.A.M. Consultants will be giving their next seminar on digital electronics design in St Albans, October 9 and 10. Information from 17 King Harry Lane, St Albans, Herts.

## New 3-D military radar

MODERN RADAR DEFENCE SYSTEMS require faster reaction capabilities in order to combat the dramatic increase in aircraft speeds and manoeuvrability of targets. To achieve this, automatic data processing facilities, capable of carrying out high speed tracking and prediction from radar returns, have become essential. To operate with maximum effectiveness these automatic systems require continuous height information on all targets as well as their plan positions. Marconi Radar Systems Ltd have introduced a 3-D radar, called Martello, which does just this.

Martello has been designed for long range cover, transportability and to provide frequent height measurement to ensure that tactical height changes can be detected in good time. It is also equipped with full electronic counter-countermeasures (e.c.c.m.).

The radar, which operates in $L$ band and provides automatic detection and plotting of targets, even in hostile environments, detects intruders at ranges in excess of 300 nautical miles and altitudes in excess of 100,000 feet Elevation cover extends from zero to 30 degrees.
Range, azimuth and height is recorded for every target detected, on every revolution, with accuracies of $0.05 \mathrm{~nm}, 0.5 \mathrm{~nm}$ (at ( 100 nm ) and 1,000 feet (at 100 nm ) respectively. For height finding Martello uses what is claimed to be a unique parallel receiving system. This has a vertical stack planar array antenna comprising sixty identical horizontal linear array elements, each with its own receiver. Each array has the same-shaped amplitude distribution, giving a narrow azimuth beam width. By precisely controlling the amplitude and phase feeds to each array, the side lobes
are kept to a minimum. In elevation, the transmitted r.f. power is distributed between the arrays to give cosec ${ }^{2}$ target illumination, and every target within the elevation coverage is illuminated on every transmission.

Returns from a target are received by all of the arrays and the individual receiver out, puts are then combined in a simple passive beam-forming network. This synthesizes the $\operatorname{cosec}^{2}$ surveillance pattern and eight elevation patterns matched to the required elevation coverage. The surveillance pattern and the lowest elevation pattern are pulsecompressed and processed either automatically or manually. Target range and azimuth are extracted from series of individual returns by a plot-forming unit and the height data is obtained by measuring the returns in adjacent elevation patterns.

The system is designed to self-adapt to the radar environment and it has comprehensive facilities for monitoring system performance necessary for complete control of the system parameters.

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## Technical specification

DC Volts (4 ranges)
Range: 1 mV to 1000 V .
Accuracy of reading $1.0 \% \pm 1$ count.
Note: $10 \mathrm{M}(1$ input impedance.
AC Volts ( $\mathbf{4 0} \mathbf{~ H z - 5 ~ k H z ) ~}$
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Accuracy of reading: $1.0 \% \pm 2$ counts.
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## MOBILE RADIO BANDWIDTHS

It is with great interest that I read Mr W. M. Pannell's article on mobile radio bandwidths in the June issue. Some of the proof of the pudding has been tasted by us, a public utility, some five years or more. In the face of great scepticism we obtained permission to operate some standard USA made 30 kHz fm fixed and mobile equipment at 15 kHz spacing using a little more than 2 kHz deviation. The equipment is used in hilly and mountainous country.
The practical result of our experiment, using single frequency simplex in the twometre band, can be summarized quite simply: 1. Utilisation of speech under difficult conditions with no perceptible degradation of intelligibility.
2. If base stations ( 100 watt) are placed at least some 25 km apart, possible use of the next channel at a 15 kHz spacing.

Please note all this with standard 30 kHz equipment. It is also necessary to state that propagation conditions in this area are such that they may almost be defined as antifading conditions, such that good results are a little surprising even to us. However, the peripheral mobiles suffer less noise (see par. 1) as a result and so operator acceptance was maintained.
Mr Pannell is to be congratulated on the redefinition of a technical problem which has very many economic and investment aspects. However, I hope that good sense will win out and that $121 / 2 \mathrm{kHz}$ or 15 kHz channels will become a standard. The latter seems the more likely to me, due to existing channel allocation in the USA and also its greater suitability for future 2.5 kHz narrow band allocations (see June issue, p. 48; "FCC produces ideas for better spectrum use"), which may yet be interleaved with the existing allocation scheme.
$P$. Hirschmann
The Israel Electric Corporation Ltd
Haifa
Israel

## AUDIO EQUIPMENT REVIEWS

In a report in your July issue (p.49) you asked the question: "How accurate are audio reviews?" Instead of attempting to give the answer your reporter used his available space to explore the inner details of particular events. From this the reader could come to some extremely unpleasant conclusions. May I, therefore, through the courtesy of your pages and as a reviewer of long standing, attempt to supply the answer that you failed to give?
The accuracy of a review will depend upon a number of factors. Among these some of the most important are:

1. The personal experience of the reviewer himself which will obviously be variable from individual to individual.
2. The adequacy of his measurement facility to cope with the fine limits of distinction that must be observed in a scientific appraisal.
3. The ability of the reviewer to interpret correctly the integration of his measurements and subjective impressions, taking into account the further variations induced by programme source material, listening room acoustics and the fallibility of his own ears.


It follows that just as there is no "perfect" piece of equipment neither is there a "perfect" review. The eventual arbiter is in every case the customer, upon whom there devolves the final decision which is expressed through his cheque book.
As a matter of interest I have been using Bowers \& Wilkins DM7 loudspeakers in my listening room for several weeks. My subjective impressions are slightly different to the opinions quoted from the Gramophone review by Mr John Gilbert, but more closely accord with his feelings than with the comments of Mr Attewell as expressed in your extracts from his Hi-Fi News review. This fundamental difference does not in any way invalidate either of the two published reviews but serves only to underline the dangers of confusing "accuracy" with "conformity".

It seems to me that your reporter was preoccupied with considerations of integrity rather than accuracy. Let me therefore add that I have in the past been pleased to accept hospitality from Bowers \& Wilkins and in my capacity as publisher of Hi-Fi Trade Journal have indirectly accepted money from that company as payment for advertising space. The nub of the question is surely this: Have the "wicked men" of Worthing "got at me" to express the opinion quoted above? 1 find it very hard to believe that any serious minded person could entertain such a preposterous proposition for a single moment. The truth of the reviewing business is very, very different indeed.
In some twenty years I have frequently written critical reviews, some of which have been so condemning that the product has been withdrawn from the market. In all that time I can recall only one instance where any unpleasantness arose between the manufacturer/distributor and myself. To the contrary, an accurate but "bad" review is regarded as helpful by any sensible manufacturer since it points to the direction of essential improvement if he is to be successful in the market-place. This success is, after all, his final objective.

1 have never at any time been put under any pressure to alter or suppress measurements; such a thing has never even been suggested to me and if it were the proposal would be most forcefully rejected.
In order to consolidate my own position as an "accurate" reviewer I have made a personal investment in test equipment and facilities amounting to a five-figure sum. My laboratory is used for consultancy work on behalf of clients, some of whose products I have reviewed or will be reviewing. The object of this exercise is to advise on performance characteristics so that improvement can be effected where necessary. Is this not in the true interests of both the consumer and
the distributor? To suggest any kind of malfeasance is to imply a contrary endeavour; i.e. a deliberate attempt to market a poor quality product to a gullible and unsuspecting public. Any firm which attempts to embark on such a course within this highly competitive industry would be taking the shortest possible road to disaster.

Perhaps my point of view might be regarded as naive, originating from a simple mind. If so, such a judgement will be accepted as a compliment.
Denys G. Killick
Pontypridd
Mid Glam.

## WANTED: EARLY WW VOLUMES

The Radio Society of Great Britain is anxious to obtain copies of the early volumes of Wireless World for the period when it was the official organ of the Wireless Society of London and later the RSGB.
The volumes required are: volume 8,3 April, 1920-19 March, 1921 ; volume 9, 2 April, 1921-10 March, 1922; volume 10, 1 April, 1922-30 September, 1922; volume 11, 7 October, 1922-31 March, 1923; volume 12, 7 April, 1923-26 September, 1923; volume 14, 2 April, 1924-24 September, 1924; and volume 15, 1 October, 1924-4 February, 1925.
The Society is also anxious to obtain volume 2 of Experimental Wireless, October, 1924-December, 1925.
If any of your readers could help the Society by supplying any of these volumes 1 would be glad to hear from them.
G. R. Jessop, G6JP

Consultant
Radio Society of Great Britain
35 Doughty Street
London WCIN 2AE

## POOR PROSPECTS IN ELECTRONIC ENGINEERING

How wholeheartedly I concur with the perceptive exposition of your contributor in "Open letter to Finniston" (Letters, July). l, too, am saddened by our industry's attitude to young graduates. It is in the nature of the "small-minded people" who manage our industry that jealousy ranks high in their emotions, and leads to the notion that these arrogant young persons need to be kept in their place. If they are engineers that place will be fairly lowly. No lunches with customers or company cars for them!

The next hurdle for the unwary graduate I would describe as the Barnes Wallace syndrome, experienced as total opposition to the novel concept. The grinding down begins! This, surely more than any biological phenomena, leads to the misconception that a person's inventiveness declines asymptotically to zero at the age of 30 . I satisfy myself that this is a myth by observing that my own "rate of acquisition of patents" index is no less now that I have passed 40. Not being afraid of the unconventional I have obstinately gone on designing things and being an engineer. But at what cost?
Taking the price of a house as reference level, my salary in a senior engineering post
is less now in real terms than it was over 20 years ago when I took my first job as a technical assistant.
To my mind, a crucial factor in this continuing decline in living standards is that engineers are not generally militant by nature. Even if they were, what hold have engineers got compared with, say, train drivers? If we all stopped designing and developing things tomorrow, who would notice? Who, anymore than would have "noticed" if radar, television or the digital computer had never been invented?

If it is accepted that the electronics industry has some merit in society, then for its survival very positive steps must be taken to provide a sufficiently attractive working climate for the young to develop their engineering skills, and without the necessity to go abroad, move into "sales" or "management" or even the drawing office, to feed their children.

## D. B. Brown

## Charlwood

Surrey.

Several points raised in the "Open Letter to Finniston" by "Chartered Engineer" (Letters, July) have a depressingly familiar ring to them. Phrases such as "short-term industrial fodder" - "managed by narrow and smallminded people" - "irrespective of experience" - are amply justified in the light of my own, albeit short, experience.

To a young electronics engineer working at least, until recently - in a large company, part of an even larger privately owned group, the management philosophy (or is it a lack of one?) has been made abundantly clear. Staff who have gained experience on a particular. system - and who are consequently much better at their job than those who have never seen that system before in their life, regardless of qualifications - are treated no better in any respect than new recruits. For instance, graduate entrants with no previous training are paid the same, in some cases greater, salary as some of my contemporaries who have not only had a year's system experience but also undergone "sandwich" course training with the company. And though they may start out as "bright eyed and bushy tailed", by the time they have grown wise to the way they themselves are being treated (even with the least perceptive engineer this process takes only a few months - in some cases less than a week), disillusionment and cynicism have set in for good - as witness the present writer. In such a climate, even if the job itself and its environment are pleasant and enjoyable - not always the case - dissatisfaction is inevitable.

If this malaise were confined to one large company, or even to one group, there would be little point in publishing this letter. But, from conversations with friends who have joined other organisations, this seems not to be the case. How many engineers in large companies do you know who are quite satisfied with their employers? The solution is therefore not to be found simply by moving among different leviathans (the grass is always greener ...). A general rule would appear to be that the smaller a firm is, the better it treats its employees; but, of course, small firms do not employ the bulk of the country's electronic engineers, and also they prefer those who have already gained some practical experience. Hence the reluctance of graduates to enter the industry, an instance of which is provided by the letter cited above.

In these circumstances; the most sensible course for a young engineer is initially to take a job with a large company solely in order to gain enough experience that he can leave after a year or two, to join or form a small firm where he will have a much greater chance of finding job satisfaction. If this were to happen on a large scale, the effect would be to leave the large groups with inexperienced youngsters and experienced nohopers (some would have it that this is already so); in which case, if their senior management were unaware or, through inertia, unable to rectify the situation, they would simply collapse, due to inability to compete - in the absence of other market factors. It may be that this process is already in motion, and that the elephants have had their day. While to many this would be a cause for rejoicing, it would be a shame if the vast resources inherent in them were to go to waste, and it is to be hoped that some at least of their creative potential could be salvaged in such event.

Young engineers should therefore be encouraged to be more aware of the state of the industry they are entering, and reassured that there is a future for them, albeit perhaps along slightly different lines to what is obvious at the present. It is, after all, far better to give birth to something new and vital than to despair of the dying.
Tim Williams
Ely
Cambs.

The "name and address supplied" open letter to Finniston in your July issue makes chilling reading. There, but for the grace of God, go I. The poor man is fifty-one, trapped in the horror of an electronic engineering career.

I graduated in Engineering in Cambridge in 1959 and went into electronics R\&D. I finally read the writing on the wall late very late - and gave up the idea of a professional career in Engineering in 197I. I began teaching Remedial English in a secondary school, for which I received the same pay, although unqualified and inexperienced, as I had been receiving as a design engineer with a State Scholarship in Mathematics, a Cambridge degree, many publications in the top journals, some impressive research and development achievements behind me, and twelve years of experience.

Recently, when I was asked to give Science and Technology career advice to the pupils at my son's school, Haberdashers', I told them that if they took up such a career they would look forward to being on half pay, and that Britain was getting out of high technology. I told them that if they were really keen they should make certain to study foreign languages and think in terms of a career abroad.

Contrary to some reports, I have never attacked Finniston or his Committee of Inquiry into the Engineering Profession. However, I would now like to say that in my opinion there is no chance that their final report will be helpful to those who work in electronics and computer design. This is because none of the seventeen members of his committee is drawn from electronics, although electronics is a very large part of the engineering profession. (Computers alone are the third biggest industry in the USA.) The Committee will repeat the lie that our best engineers must be coaxed into rolling up their sleeves (like the good plumbers they really are) and getting into production. Because of their limited background, their lack of experience in high technology industry, the committee
members will not know that Production is a facet which tends to disappear as the sophistication of the technology increases. It is difficult even to find the production department in a high technology company, for instance one involved in advanced radar. If you think that Production is the essence of advanced engineering, you ignore the kind of message that Marshall McLuhan was trying to put across thirteen years ago. Notice that all examples on tv lauding the supposedly noble, against-the-tide first class engineer working in production are taken from old, declining industries. Such stories are merely another attack on high technology and its massive potential.

I am pretty certain that I can guess, from the tone of his letter, which company the fifty-one year old is working for. I would advise him that although the harassment of professional engineers is severe throughout British industry, in the case of his company it is a little worse than usual. The managing director of his company is systematically rooting out the engineering competence in his company, and by now has to a large extent completed the task. In five years' time, this managing director will be pilloried for destroying his company. But that is all in the future, and cold comfort for the fifty one year old.
Ivor Catt
St. Albans
Herts.

## TALKING TO COMPUTERS

I was amused to read Mixer's remarks on computer programming (p.92, June).

The real reason we don't programme computers in the Queen's English is the same reason we don't write algebra textbooks in English prose - a formalized language is far clearer than a human language. There are things I can tell a computer succinctly in PL/l which I couldn't tell it (unless very awkwardly) in English.

Human languages are context-dependent; to understand a human language the hearer needs much more background knowledge and must make many more assumptions than are necessary in understanding a formal language. Though computer handling of natural language is not far away and will be quite useful, formal programming languages will hardly be superseded.
Michael A. Covington
Athens
Georgia, USA

## F.M. TRANSCEIVER SYNTHESISER

Like Mr Hankins (Letters, June) I have constructed the synthesiser part of T. D. Forrester's two-metre f.m. transceiver (November and December, 1977). I have also found modifications essential to make it work. These are as follows:

1. $\mathrm{L}_{1}$ was increased to 20 turns, with a separate l-turn coupling coil. The varicap diode $D_{17}$ was replaced by a device of greater capacitance - in my case a 1 N 4001 rectifier diode worked well - and $\mathrm{C}_{7}$ was increased to 330 pF .


The modified v.c.o.
2. I added a level shifter between the t.t.l. and c.m.o.s. to provide a proper voltage swing 'his was similar in design to Mr Hankins' circuit.
3. I found that the sensitivity of the 74LS74 was improved by increasing rather than decreasing $R_{39}$ : I used a value of $10 \mathrm{k} \Omega$. The reason for this is that this allows the t.t.l. output to bias itself up to the logic "l" level; the input resistor forward biases the input diode of the gate to a point where its noise margin is effectively nil; and the full "gain" of the circuit is used to amplify the input signal.

As the input impedance of the low-power Schottky device is quite high - about $18 \mathrm{k} \Omega$ I found it made no difference to dispense with the buffer stage, $\mathrm{Tr}_{4}$, altogether and to connect the t.t.l. input straight through a 2 n 2 capacitor to the collector of $\mathrm{Tr}_{2}$. This saves 12 mA or so of current.
4. In a further attempt to reduce the power consumption of the v.c.o. I removed the zener dioide, $\mathrm{D}_{18}$, and ran the oscillator stage (with changed resistor values) from the 5 -volt t.t.l. power supply (obtained from a 78LO5).

Replacing the 4049 hex c.m.o.s. buffer with a 4069 hex inverter might reduce the consumption of this chip, but unfortunately these devices are not pin-compatible.
5. Apart from using a different arrangement of switches and gates to obtain the Tx, Rx, $\mathrm{Tx}(\mathrm{Rpt})$ and $\mathrm{Rx}(\mathrm{Rev} . \mathrm{rpt})$ lines in the synthesiser logic. I also dispensed with many of the diodes and resistors: I used five diodes, and it can be done with four.
7. I used the spare divider section in $\mathrm{IC}_{4}$ to obtain a frequency close to 1750 Hz , for use as a toneburst generator.

The accompanying circuits illustrate these points.

As a further suggestion the whole v.c.o. could possibly be replaced with a single t.t.l. v.c.o. chip, such as the 74LS324, which has a typical operating limit of 30 MHz , a consumption of 30 mA maximum, and, of course, a t.t.l. output. The output would be rich in odd order harmonics, so could be followed by a frequency tripler, with a doubler up to 144 MHz : this is better practice as it avoids the generation of 48 MHz .

A friend has observed that on his synthesiser (and on mine) the frequency overshoots its target value. This could indicate that the filter components after the phase detector have not been chosen for critical damping of the control loop. Another problem that has been suggested is in the quadrature detector


Repeater access tone generator. By applying a suitable positive-going pulse to pin 10 of $I C_{4}$, the tone generator could be gated to provide a tone-burst.


The new Rx/Tx line generation and arrangement of diodes and resistors at inputs to b.c.d. adders.
of, the i.f.: the use of a crystal here is really only suitable for ultra-narrow bandwidths, and a friend who has tried this circuit reports that it doesn't work.
I would appreciate advice and suggestions concerning this design from other constructors; for example, the use of alternative microphone pre-amp/compressor circuits instead of the rather expensive Plessey chip specified.
I would like to conclude by congratulating Mr Forrester on producing an alternative to the Japanese "black boxes", although I daresay some constructors will be less enthusiastic about the D.I.Y. (Design-itYourself) aspects than those of us with access to 'scopes, frequency meters, etc.
J. D. Stumbles

Imperial College Computer Centre
Exhibition Road
London, SW7

## A'UDIO OSCILLATOR PRODUCTION DESIGNS

I have designed, and brought to a fully developed engineering state, two sinewave audio oscillators having unusual and attractive performance characteristics.
The more complex design, which is neither a function generator nor a b.f.o., covers the complete audio spectrum in a single sweep, either by rotation of a dial having an accurately logarithmic scale shape, or by means of an internally or externally generated ramp voltage. Frequency-marker pulses are generated as the frequency sweeps through $0.1,1$ and 10 kHz . A warble-tone facility is also incorporated, the oscillator being intended for acoustic measurement work.
The total harmonic distortion of the above instrument is less than $0.05 \%$ from 40 Hz to 10 kHz and less than $0.1 \%$ from 20 Hz to 40 kHz . The distortion is mainly second and third harmonic. The variation in output amplitude with frequency does not exceed $\pm 0.1 \mathrm{~dB}$ from 20 Hz to 40 kHz and there is no significant bounce.
The basic circuit is an. R-C oscillator in which the R's have been replaced by transistor junctions whose d.c. bias voltage is varied to control the frequency in an inherently logarithmic manner. Two outputs in phase quadrature are available and could be used in association with a simple type of tracking filter. Very great care indeed has been taken to achieve accurate temperature compensation of frequency under production conditions.

In addition to the complete batteryoperated instrument with the above facilities, ten of the main $11 \mathrm{~cm} \times 12 \mathrm{~cm}$ printed-circuit boards have by now been made, of which eight are in use by a large and well-known loudspeaker firm who have standardised on the design for their production test purposes. Experience with these eleven basic oscillators has shown that the design can readily be set up on a production
basis to meet the performance specification. A complete test schedule has been written and special. production-test items have been made.

The second design is a simple two-op-amp thermistor-controlled R-C oscillator giving less than $0.001 \%$ total harmonic distortion from 100 Hz to 10 kHz - about $0.0003 \%$ at 1 kHz - and less than $0.002 \%$ at 30 Hz and 20 kHz . The oscillator operates from a couple of PP9 batteries and takes about 14 mA . In its present form the instrument is switchtuned to give a number of spot frequencies throughout the audio band, but has a fine-tuning adjustment. A simple potentiometer output control is provided, this being adequate for most distortion-measuring applications.

The good performance of this latter oscillator results from (a) adopting an unconventional basic oscillator circuit giving far greater attenuation of thermistor distortion than does the usual. Wien-bridge circuit, (b) giving careful thought to the theory of thermistor control-loop behaviour in order to obtain an optimum compromise between distortion and amplitude bounce, and (c) using good wide-band i.c. op amps. The total component cost, at Radiospares prices, including the instrument case, is about $£ 14$.

I would be glad to hear from any firm interested in the possibility of commercial exploitation of these designs.

## Peter J. Baxandall

Malvern
Worcs.

## PROGRAMMING MICROPROCESSORS

Alas, K. G. Parr (August Letters) and I (June Letters) seem to be addressing different audiences, and so our viewpoints are difficult to reconcile. His admirable letter aligns with the opinions of at least one of my colleagues who is professionally engaged in systems development, both hardware and software, and who has both the opportunity (and perhaps the personality) to make effective use of microprocessors. I would be the first to concede that if one is committed to design these chips into, say, gambling machines (August issue, p.57) commercially, then one must try to be as impeccably disciplined as K. G. Parr suggests. Even so, as he tells us, "bugs still remain." And not only can they reveal themselves years later when an established programme faults, but so also one may discover errors in flow diagrams. This is not to disparage the flow diagram as a sometimes useful tool; but for a definitive final document I have usually found it more useful to edit a listing of the programme with some relevant textual comments, including reference to sources of algorithms, and with labelled arrows to indicate the purpose of loops, switches, jumps etc.

I hope that the readership of Wireless World includes already many "hobby programmers"; though surely all its readers are hobbyists at heart? I see that there will soon be a new generation of microprocessors designed to that they can be completely tested before use. So, hobbyists take heart if you are struggling with octal and hexadecimal. It can only be matter of time before decimalisation catches on.
Desmond Thackeray
Department of Chemical Physics University of Surrey

## DISCRIMINATIVE METAL DETECTOR

I read the article "Discriminative metal detector" by R. C. V. Macario (July issue) with interest. Although the author states that eddy currents mask diamagnetic effects in nonferromagnetic samples it is worth noting that these eddy currents do cause an increase in oscillator frequency. This is, of course, in the opposite direction to that caused by ferromagnetic samples, where the ferromagnetic effect usually masks the eddy currents.
The effect of eddy currents can easily be demonstrated by comparing the influence of a closed loop of thin wire with that of the same loop opened. [Surely a coupled shorted-turn. - Ed.]
The directional information could be restored with headphones by using the $Q$ outputs of $\mathrm{IC}_{2}$ to drive a simple resistor ladder and voltage controlled oscillator (eliminating $\mathrm{IC}_{3}$, etc.).
M. Walne

Halifax
More letters on the metal detector will be published next month.-Ed.

## PICKUP-ARM PROBLEMS

I would like to point out that adoption of the technique proposed by F. Holloway (Letters, July) of producing discs by means of a cutting head on a radius-arm does not overcome the other serious shortcomings of the radius-arm reproducer so well highlighted in Mr Randhawa's article, i.e. side-thrust compensation and lateral balance.
There is no case for "concentrating the difficulty and cost of mechanical design once and for all in the recording equipment" as your editorial note suggests when paralleltracking arm designs with electronic controls are now coming to the fore and which solve all three problems at a stroke.

Is it not about time that electronic engineers turned their attention from designing amplifiers with specifications of unnecessary excellence and concentrated their talents on programme sources such as tape and disc, which are still producing distortions that are audible?
R. Cooper

Sutton Coldfield

## Filter

A bucket-brigade filter, said to be the first charge transfer device filter i.c., is introduced by the American Reticon Corporation. The circuits consist of a 64 -stage, split-electrode brigade, the capacitors being the basic metaloxide semiconductors. Low-pass and band-pass filters are available, each being tuned by varying the clock frequency, and possess a linear phase characteristic. Extremely steep rolloffs of over 200dB/octave are exhibited. An example quoted is that of a bandpass filter, in which the centre frequency is at 0.25 the clock - frequency $(250 \mathrm{~Hz}-1 \mathrm{MHz})$, the bandwidth 0.055 the clock frequency and the ratio between stop band and pass band attenuation is 52 dB . The rate of change is 270dB/octave. Herbert Sigma Ltd, Spring Road, Letchworth, Herts.
WW301

## Storage oscilloscope

Signal storage is performed digitally in the Advance OS4100 oscilloscope. It is a two-channel instrument using a digitizer and 1 kbit r.a.m. to provide a bandwidth of 600 kHz . An X-Y display

is also provided; the sum or difference of the two inputs can be displayed and an unusual feature is a triggering window, in which triggering takes place outside two threshholds. Triggering can be obtained from 2 mm of trace deflection and a delay can be used to display events occurring up to a quarter of the time-base period before the trigger. The trace consists of a number of dots, which can be 'smoothed' if required, and a split-trace mode can be selected where alternate samples can be sorted and viewed against a new trace for comparison. Gould Instruments Division, Roebuck Road, Hainault, Essex IG6 3UE.
WW302


## Electronic <br> wattmeter

An electronic multiplying technique used in Feedback Instruments EW604 power meter allows wide frequency and power ranges to be handled by the one instrument. It also means that waveforms other than sinusoids can be measured. The range of the instrument is 0.25 W to 10 kW f.s.d., 5 V to 1 kV r.m.s., 80 mA to 10A r.m.s., all in the frequency band from zero to 20 kHz . To reduce effects on the circuit under test, the input resistance is $5 \mathrm{k} \Omega$ per volt, drawing $200 \mu \mathrm{~A}$ full scale, and the resistance offered to a current input is $60 \mathrm{~m} \Omega$. Feedback Instruments Ltd, Park Road, Crowborough, Sussex TN6 2QR.

## WW303

## Digital tester

Comprehensive facilities for measurement in the time and frequency domain, voltage, resistance and temperature are contained in one new Tektronix instrument, the 851 Digital Tester. A 5-digit display is fed by a 35 MHz counter, which performs all the usual frequency,

time, ratio and event counting from three input channels; a digital multimeter of $41 / 2$-digit resolution measuring voltage, resistance, temperature and the thresholds of the input channels, together with polarity. The in-
puts can be set to t.t.l. as a calibrated knob setting or to any thresholds compatible with other logic families within $\pm 30 \mathrm{~V}$. All necessary probes are housed within the case of the instrument. Tektronix UK Ltd, Beaverton House, P.O. Box 69, Harpenden, Herts.
WW304

## Audio system measurement

ATR-1 Audiotracer consists of a voltage-controlled oscillator, logarithmic in frequency from $20 \mathrm{~Hz}-20 \mathrm{kHz}$ or $200 \mathrm{~Hz}-200 \mathrm{kHz}$, an

output amplifier, a log. or linear input amplifier, a true r.m.s. rectifier and a pen recorder. The whole thing is contained in a 11 $\times 5^{1 / 2} \times 3$ in case and is intended to measure and record the frequency/amplitude performance of electronic or electroacoustic systems. A measuring microphone and an artificial ear coupler are available for acoustic measurements. The unit is mains-powered. ATR-1 is made by Neutrik A.G. of Switzerland and distributed here by Eardley Electronics Ltd, Eardley House, 182/4 Campden Hill Road, Kensington, London W8 7AS. WW305

## Frequency scalers

Frequency scalers are dividers, used to reduce high frequencies to within the capability of lowfrequency counter-type frequency meters. There is no display, and the division factor must be re-applied to the counter display. Two units produced by MTG fulfil this function, the PS 1200 enabling a 10 MHz counter to measure a frequency of 1 GHZ (PS520 up to 500 MHz ). Both units will accept 10 mV minimum input and are well protected against overload and out-

put short circuits. Versions offering division ratios of 10 or 100:1 are available. MTG (Instruments) Ltd, Beacon House, Christchurch Road, Lansdowne, Bournemouth, Dorset BH1 3LB. WW306

## U.h.f. amplifier

A two-stage hybrid amplifer, the SH120A offers 16 dB of gain, which is said to be constant from $40-900 \mathrm{MHz}$, and a noise figure of 5 dB . It is a low-level aerial amplifier, being terminated in 75 ohms in and out and its maximum output is 100 mV . Voltage supply needed is 12 V . SGS-Ates (UK) Ltd, Planar House, Walton Street, Aylesbury, Bucks HP21 7QN.
WW307


Pressure transducer Two transducers from Philips convert pneumatic pressure to standard $4-20 \mathrm{~mA}$ signals for transmission or processing. A metric type (PR9363/20) covers the range $0.2-1$ Bar, while an Imperial version (PR9363/30) works from 3-15 p.s.i., both units conforming to DIN 19231 and IEC 381. Unstabilized power can be used. The principle of the devices is the deflection of a strainsensitive diaphragm by the pressure, unbalance in the strain elements giving rise to an output voltage. Pye Ether Ltd, Caxton Way, Stevenage, Herts.

## WW308



## Instrument cases

The new 'Princess' range of cabinets from West Hyde is intended to house the various kinds of equipment to do with computers, e.d.p. and calculators. The cases are made from ABS, in two horizontal parts, and have two frontal surfaces, at differing slopes, for mounting controls and displays. The plastic can be drilled and cut and is finished in an imitation leather texture, in black

or black and tan. The internal dimensions are keyed to the Eurocard shape, the largest case taking double Eurocards. West Hyde Developments Ltd, Unit 9, Park Street Industrial Estate, Aylesbury, Bucks HP20 1ET. WW309

## Frequency meter

A u.h.f. counter, of the nondisplaying scaler type, of high stability and accuracy offered by the American firm of Davis is the 7208 Frequency Counter. The instrument will measure frequency up to 600 MHz at 10 mV
up to 60 MHz and 100 mV at 600 MHz -less with an optional preamplifier. Crystal frequency error is 1 part per million or 0.5 p.p.m. with the optional oven, while the drift is 1 p.p.m. per hour or 0.5 p.p.m. per month, again with the oven. An l.e.d. display of eight digits is used, with decimal point being adjusted by the switching. Davis Electronics, 636 Sheridan Drive, Tonawanda, New York. WW310

## Spray etcher

This unit, from P.B. Electronics, is a double-sided etcher, which is small enough to stand on a bench and fast enough to etch a 12 in square board in less than three minutes. It uses four gallons of etchant, which is kept at $120^{\circ} \mathrm{F}$, and a timer stops the pump after a given time. A lid interlock reduces the chances of etching the ceiling. P.B. say that the etcher will be followed by other units for sensitizing, exposing and developing. P.B. Electronics (Scotland) Ltd, 9 Radwinter Road, Saffron Walden, Essex CB11 3HU.
WW311


## Voltage-to-

frequency converter
The option of linear or logarithmic conversion and a dynamic. range of more than seven decades are the features of the Aragorn VFDI module. The output is a 10 V square wave, which responds to a $0-90 \%$ step within 1 cycle, the maximum output frequency being 2 MHz or 20 kHz to order. Accuracy of conversion is $0.1 \%$ and frequency stability is 200 parts per million at 1 kHz . Voltage supply needed is $15-0$ 15 V and the module measures 45 $\times 30 \times 16 \mathrm{~mm}$, with pins on a 0.1 in grid. Aragorn Dynamics Ltd, 8 South Side, Clapham Common, London SW4 7AA.
WW312

## Buzzers

These are miniature, electronic units for use in clocks, timers, telephones, etc. They are for use where $2.5 \mathrm{~V}, 6 \mathrm{~V}$ or 12 V supplies, of low stability, are present. At least


70 dB relative to 0.0002 dynes/ sq.cm. is produced 22 cm away from the unit, while taking around 20 mA from the supply. Type number is GA100. Highland Electronics Ltd, Highland House, 8 Old Steine, Brighton, East Sussex BN1 1EJ.
WW313

## V.m.o.s. memory

Two static 4 K memories from AMI use the v.m.o.s. technique to provide high-speed access and high density. The S 2114 is organized as a $1024 \times 4$ bit r.a.m. and is intended as a high-speed Intel 2114 replacement, for which it is claimed to be pin-compatible. Maximum response time can be down to 150 ns. The S4017 is a $4096 \times 1$ bit r.a.m. with a response time down to 55 ns . Both types are usable with t.t.I., operating from 5 V , and are contained in 18 -pin plastic or ceramic packages. AMI Microsystems Ltd, 108A Commercial Road, Swindon, Wiltshire.
WW314


## P.s.u. for logic and linear <br> Where 5 V digital circuitry and

 linear i.cs are to be used together the power needed can conveniently be provided by the Lascar 3-rail power supply. Outputs are 5 V at 1 A and tracking rails of 5 to 15 V , positive and negative, at 100 mA . The single and dual supplies are isolated from each other and both are well protected. A $160 \times 100 \mathrm{~mm}$ board carries the whole unit. Lascar Electronics Ltd, P.O. Box 12, Module House, Billericay, Essex CM12 9QA. WW315
## Signal conditioner

Platinum resistance elements are widely used in temperature measurement bridges, but their non-linearity restricts them to laboratory use, in the main. Ancom have now produced a signal conditioner, 15RP-3, to allow the use of a platinum sensor with a linear measuring system - a digital panel meter, for example. The $51 \times 29 \times 16 \mathrm{~mm}$, encapsulated, p.c.-mounted module provides 10 mV per degree Centigrade output, within the $-110^{\circ} \mathrm{C}$ to $100^{\circ} \mathrm{C}$ working range, with errors of better than $\pm 0.1^{\circ} \mathrm{C}$ and $\pm 0.1 \%$ of reading. Power needed is $15-0-15 \mathrm{~V}$ at 10 mA (each rail). Ancom Ltd, Devonshire Street, Cheltenham, Glos. GL50 3TL. WW316

## Keypad

This 12 -key pad from FR Electronics is of low height (less than 25 mm when the buttons are down - unspecified when they are not) and is said to offer a high degree of reliability as a consequence of its use of reed switches. The keys are mounted on a printed board, with

an edge connexion, from which the outputs are in binary form, compatible with t.t.l. levels. Voltage supply needed is 5 V and two-key rollover is a standard provision. F.R. Electronics Ltd, Wimborne, Dorset BH21 2BJ. WW317

## Microwave f.e.t.

A noise figure of 1.7 dB or better at 4 GHz and a useful range of application from $1-12 \mathrm{GHz}$ is claimed for the HFET-1102 GaAs f.e.t. from Hewlett-Packard. Minimum associated small-signal gain is quoted as 11 dB at 4 GHz . The encapsulation is HPAC100A. Hewlett-Packard Ltd, King Street Lane, Winnersh, Wokingham, Berks RG11 5AR.
WW318

## Coaxial relays

Between 2 and 30 coaxial inputs, switching to a single coax. output, all of either 50 or 75 -ohms, form a single-pole, multi-way, coaxial r.f. switch, with isolation of around 100 dB . Unselected inputs may be earthed; opencircuited or terminated in the relay block; contacts of the reed
relay can be expected to endure up to 20 million operations and will handle 250 mA at 100 V or less, depending on the switching mode. The switching signal can be anything from 5 to 50 V d.c. or a.c. Hercoax Ltd, Plumpton House, Plumpton Road, Hoddesdon, Herts.
WW319

## Chart recorder

Unicorders are desk-top, potentiometric chart recorders with three to six steel pens, which may overlap. A complete traverse of

the 250 mm chart takes 0.3 s , and the pens can be lifted individually, by solenoid as an option. The 20 m chart is stepper-motor driven at a speed selected from 199 dial-selected or remotelycontrolled possibilities. Plug-in amplifiers accommodate the voltage range 1 mV to 200 V , currents from $1 \mu \mathrm{~A}$ to 500 mA and thermocouples at $0^{\circ} \mathrm{C}-1600^{\circ} \mathrm{C}$. Common-mode rejection ratio is 170 dB at zero frequency and 160 dB with alternating signals. Rostol Ltd, 33 Byron Road, Earley, Reading, Berks IG 6 1EP. WW320

## P.c. switches

APEM 21000 N toggle switches are designed to be mounted on printed boards, projecting only 9 mm above the surface. Singlepole switches are $11 \times 7 \times$ 7.8 mm ; double-pole $21 \times 17 \times$ 7.8 mm , excluding pins. They possess either two or three positions, latched or momentary and have silver or gold-plated contacts. Voltage and current capability for the silver contacts are 250 V a.c. and 2 A a.c. and the gold contacts will control 10 mA at 50 mV . Iskra Ltd, Redlands, Coulsdon, Surrey CR3 2HT.
WW321


## Mechanical filters

Low-frequency filters using flexure-mode mechanical resonators are announced by Rockwell-Collins. Nickel-iron bars and piezoelectric ceramics are the active elements, pro-

ducing a response of 0.2 to $1.5 \%$ bandwidth/centre frequency which is equivalent to a Q of 500 to 70 . The filters work in the range $3.5-70 \mathrm{kHz}$, with an insertion loss of 2.5 dB , and terminating resistance of $33 \mathrm{k} \Omega$. Centre frequency varies less than 4 Hz over a $0^{\circ} \mathrm{C}$ to $60^{\circ} \mathrm{C}$ range of temperature. G. A. Stanley Palmer Ltd, Elmbridge Works, Island Farm Avenue, West Molesey Trading Estate, Surrey KT8 OUR. WW322

## Lead forming tool

Consistency and accuracy in the bending of component leads for printed-board mounting is the aim of the Litesold Opsec tool.


Components are dropped against a graduated step into the slots on the jaws, which are also graduated to take components of varying lengths. The tool is made of high-impact plastic and has non-slip feet, which can be screwed to a bench. Light Soldering Developments Ltd, 97/99 Gloucester Road, Croydon, Surrey CRO 2DN
WW323

## Low-resistance meter

A main-frame and plug-in adaptors, the 1700 series, from Electro Scientific Industries are capable of measurement and numerical display of resistance values in the range 1 micro-ohm to 200 milliohms, in which lie the resistances of contacts, inductor windings and printed-board
tracks. One of the plug-in units offers the option of d.c. measurement for reactive components or a pulsed test for those components whose characteristic would change if current were to be passed continuously. Tranchant Electronics Ltd, Tranchant House, 100A High Street, Hampton, Middlesex TWI2 2ST.
WW324

## R.f calorimeter

A numerical reading in kW , allied to the calorimetric determination of radio-frequency power, enables convenient power measurements to be made by the Bird 6080 up to 80 kW with a maxi-

mum error of $\pm 3 \%$. Temperature sensors and water flow monitors are remote, being connected via an 8 ft cable to the power meter. Aspen Electronics Ltd, 2 Kildare Close, Eastcote, Middlesex HA4 9UR.
WW325

## Dual-lamp switch

A dual-lamp switch, available from Pye Electro-Devices Ltd, has a $3 / 4$ in-square cap incorporating two TI3/4 lamps with a midget flange base and a matching indicator. The cap has horizontal or vertical split screens, with five choices of filter and eight lens colours for each of the two zones of the screen. Silver or gold contacts; with momentary or alternative actions, are available in s.p.d.t. and d.p.d.t. configurations. Pye Electro Devices Limited, Controls Division, Exning Road, Newmarket, Suffolk CB8 0AX.
WW326

## Darlingtons for tv deflectors

Two high power transistors, the BU806 and the BU807, are particularly suitable for use as horizontal deflectors in black and white televisions. The low driving power typical of the Darlington configuration allows elimination of the driving transformer and consequently reduces the cost of the stage. The high switching speed required is guaranteed by the presence of an integrated diode which extracts the charges accumulated during conduction. A clamper diode is also integrated in the devices. These devices, when used with the integrated horizontal deflection circuit, the TDA1180, no longer require a driving transistor and consequently the cost of the system is further reduced. SGS-Ates (United Kingdom) Limited, Planar House, Walton Street, Aylesbury, Bucks, HP21 7QN.
WW327

## Fibre light guides

General purpose fibre-optic light guides in a p.v.c. sheath and terminated in stainless steel end fittings are produced by Valtec. Fibres of 0.002 in diameter are used in single, bifurcated and trifurcated branch types in lengths up to 30 ft attenuation is $600-700 \mathrm{~dB} / \mathrm{km}$, numerical aperture is 0.56 with a 68 degree acceptance angle. Fiberoptics Division, Valtec Corporation, West Boylston, MA, USA 01583. WW328

## Supply failure simulator

Interference simulation by the Schaffner NSG200 series is extended to allow the simulation of d.c. power failure and interruptions. A new plug-in unit, the NSG204, is for use with the 200 C mainframe, and permits d.c. supplies between 5 V and 220 V at up to 10 A to be interrupted for adjustable duration between 1 ms and 2 s . The interruptions may be generated by an external trigger, a push-button or an internal timing generator, working between 5 Hz and $0-1 \mathrm{~Hz}$. Switching time is around $2 \mu s$ off and $1 \mu s$ on. Lyons Instrument Ltd, Hoddesdon, Herts.
WW329

## T-test

Embarrassing moments experienced by television and film "personalities" can't be the same kind as those I have - they always seem so delighted to tell anyone who asks them all about the grisly faux pas and face-burning ineptitudes they've committed. I can't bring myself to tell anyone about mine, but I'm always happy to hear of other people's. The only time I've ever been able to see one of my own episodes as even remotely funny was when I sidled up to my wife, who was trying on a hat in a shop and said "You can always use it as a flower-pot when you're tired of it" and yes, you've guessed it, it wasn't my wife. The woman wasn't trying on hats, either; it was her own.
"What's all this to do with electronics?" I hear all my readers ask, both at once. Well, it's just that we were talking, this morning, about Chairman's Visit time, from which many of us have suffered. You know what I mean; the one day a year that makes it all worthwhile. White lines are painted on the factory floor, guards are put on machines and all design engineers are told to wear their clean shirt and tie and have a shave if they can possibly manage it.

Our chairman used to come round the labs. and demonstrate that democracy was still a force to be reckoned with by speaking to us. Actually, he usually directed his questions to us by way of the chief engineer, and didn't bother listening to the answers, but the intention was there. We always had to show him the newest bits of gear we had prised out the buying officer during the year so that the management could show him how forward-looking they were and, this year, we had bought an oven for environmental testing of components. It was a big metal box, with lots of terminals and leads sprouting from them and any amount of meters and generators clustered round it. The chap who was using it explained what it was for but seemed a bit reluctant to
open the door. It would upset the test run and probably spoil a lot of hard work and other protestations, but the Man wanted a look inside, so with a dramatic flourish, the door was opened. There were three shelves inside and nothing else at all, except that right in the middle of the centre shelf was a very small pork pie, gently steaming.

Not a word was spoken. The group moved on and the only sign of anything amiss was that our chief engineer was abstractedly chewing his tie. I don't remember what happened to the chap with the pie - he's probably a permanent lab. assistant now.

## Timeo Danaos

It seems we're about to be saved. You can all come back in off the window ledge, because Whitehall has decided that the electronics industry could do with a hand and is intent on injecting $£ 50 \mathrm{~m}$ or so to revitalize, rejuvenate and rekindle the sparkle in our eyes.

Well, that's great, but you will, I hope, forgive me if I don't instantly leap to my feet and turn cartwheels all the way along Stamford Street. I'm too old and .frail, for one thing, and the other is that I have this sudden presentiment of
doom. I put it down to the SADIM syndrome, so named because of a Greek character - a king, as it happens who had a regrettable tendency to turn everything he touched into clay; he could have made a fortune in the china industry but he had no marketing sense. There he sat all day, strumming on his bouzouki and surrounded by little piles of pure gold; every now and then he would snatch a pile of gold and, calling on his training as an alchemist, transmogrify it into base clay. (His old prof. used to say that the lad had never seemed to get the point of the subject).
The striking thing about all this is, though, that not only did we learn our democracy from chaps like that, but some of his financial expertise seems to have rubbed off too. The cream of our society at Westminster have, it seems, only to take a passing interest in an industry for it to become a disaster area. There is no need to plod wearily through the list of victims, but if you kick off with the Brabazon and doff a mental hat at the TSR2 and the Hovertrain, finishing up with British Leyland (or whatever they call it these days) and Strathearn you'll see what 1 mean. It isn't a view of life to make one happy at the sight of Ministers bearing gifts.
Still, the china industry could have a rosy future.

## Byter bit

Among the faults of David Bligh, excessive faith in l.s.i.
was very probably the worst, though in this field he was the first. He gathered chips from every source, attended every single course and ultimately he was known as Dai the Sums, and stood alone. As hardware goes, it came and went (around a thousand pounds he spent) but gradually, he amassed sufficient gear, and stopped his fast. Computers large, computers small, Dai knew the workings of them all. His own could indicate, at speed with all the confidence you need the contents of its ROM, in clear on a v.d.u., and bend your ear with cacaphonic sounds of bytes in battle for their storage rights. When Dai was asked if this was what his monster did, he waxed quite hot and instantly applied his mind to write a programme of a kind to show quite positively that computing's really where it's at. His friends all gathered round to stare being sure he'd finished with hot air. He ran the tape - the display flashed, the printer rattled - keys were bashed. Then, on the screen in letters twee was printed "Pawn to King's Knight Three".
The move was made, but all in vain, because a rook in wait had lain; it sidled gently up and said
"Checkmate, old son, afraid you're dead!"

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The unit is designed to decode not only UHJ but virtually all other quadrophonic systems (Not CD4), including the new BBC HJ 10 input The unit is designed to decode not only UHJ but virtually all other quadrophonic sys selections
The decoder is linear throughout and does not rely on listener fatiguing logic enhancement techniques. Both 2 or 3 input signals and 4 or 6 output signals are provided in this most versatile unit. Complete with mains power supply, wooden cabinet, panel, knobs, etc.

Complete kit, including licence fee $£ \mathbf{4 5 . 0 0}+$ VAT or ready built and tested £61.50 + VAT

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## With Home Office Type approval

As in "Wireless World", designed by Mike Hosking. 240V ac mains operated and disguised as a hardbacked book. Detection range up to 30 feet.

Complete exclusive designer approved kit $£ \mathbf{4 6 . 0 0}+$ VAT
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## Wireless World Dolby ${ }^{\text {® }}$ noise reducer

Trademark of Dolby Laboratories Inc.


Featuring

- switching for both encoding (low-level h.f. compression) and decoding
- a switchable f.m. stereo multiplex and bias filter.
- provision for decoding Dolby f.m. radio transmissions (as in USA).
- no equipment needed for alignment.
- suitability for both open-reel and cassette tape machines.
- check tape switch for encoded monitoring in three-head machines.


## Typical performance <br> Noise reduction better than 9 dB weighted. <br> Clipping level 16.5 dB above Dolby level (measured at $1 \%$ third harmonic content) <br> Harmonic distortion $0.1 \%$ at Dolby level typically $0.05 \%$ over most of band, rising to a maximum of $012 \%$ <br> Signal-to-noise ratıo $75 \mathrm{~dB}(2 \mathrm{OHz}$ to 20 kHz , sıgnal at Dolby level) at Monitor output <br> Dynamic Range $>90 \mathrm{db}$ <br> 30 mV sensitivity

Complete Kit
PRICE: $£ 39.90+$ VAT
Price $£ 54.00+V A T$
Price £2.20+VAT
Calibration tapes are available for open-reel use and for cassette (specify which)
Single channel plug-in Dolby (MR) PROCESSOR BOARDS ( $92 \times 87 \mathrm{~mm})$ with gold plated contacts are availabie with all components
Single channel board with selected fet
Gold Plated edge connector
Selected FETs $\mathbf{6 0 p}$ each + VAT, 100p + VAT for two, $\mathbf{£ 1 . 9 0 + \text { VAT for four }}$
Please addVAT@ $12 \frac{1}{2} \%$ unless marked thus., when $8 \%$ applies (or current rates)
We guarantee full after-sales tecnnical and servicing facilties on all our kits. have you checked that these services are avalable from otner suppiers?

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# S-2020TA STEREO TUNER / AMPLIFIER KIT 

## SOLID MAHOGANY CABINET

## A high-quality push-button

 FM Varicap Stereo Tuner combined with a 24 W r.m.s. per channel Stereo Amplifier.

Brief Spec. Amplifier Low field Toroidal transformer, Mag, input, Tape in/Out facility (for noise reduction unit etc.), THD less than $0.1 \%$ at 20W into 8 ohms. Power on / off FET transient protection. All sockets, fuses, etc., are PC mounted for ease of assembly. Tuner section uses 3302 FET module requiring no RF alignment, ceramic if INTERSTATION MUTE, and phase-locked IC stereo decoder. LED tuning and stereo indicators. Tuning range $88-104 \mathrm{MHz}$. 30 dB mono $\mathrm{S} / \mathrm{N} @ 1.2 \mu \mathrm{~V}$. THD $0.3 \%$. Pre-decoder 'birdy' filter. PRICE: £58.95+VAT

## NELSON-JONES MK. I STEREO FM TUNER KIT

A very high performance tuner with dual gate MOSFET RF and Mixer front end, triple gang varicap tuning, and dual ceramic filter/dual IC IF amp.

quieting @ 0.75 uV . Image rejection -70 dB . IF rejection - 85dB. THD typically $0.4 \%$.

IC stabilized PSU and LED tuning indicators. Push-button tuning and AFC unit. Choice of either mono or stereo with a choice of stereo decoders. With ICPL Decoder £36.67 + VAT With Portus-Haywood Decoder
Compare this spec. with tuners costing twice the price.
$£ 39.20$ + VAT


Tuning range $88-104 \mathrm{MHz}$
LED sig. strength and stereo indicator
PRICE: Stereo £31.95 + VAT


S-2020A AMPLIFIER KIT
Developed in our laboratories from the highly successful
"TEXAN" design. PC mounting potentiometers, switches, sockets and fuses are used for ease of assembly and to minimize wiring Power 'on /off' FET transient protection.

Typ Spec. $24+24 \mathrm{~W}$ r.m.s. into 8 -ohm load at less than $0.1 \%$ THD. Mag. PU input $\mathrm{S} / \mathrm{N} 60 \mathrm{~dB}$. Radio input $\mathrm{S} / \mathrm{N}$ 72 dB . Headphone output. Tape In/Out facility (for noise reduction unit, etc.). Toroidal mains transformer.

PRICE: $£ 3 \mathbf{3 . 9 5}+$ VAT
ALL THE ABOVE KITS ARE SUPPLIED COMPLETE WITH ALL METALWORK, SOCKETS, FUSES, NUTS AND BOLTS, KNOBS, FRONT PANELS, SOLID MAHOGANY CABINETS AND COMPREHENSIVE INSTRUCTIONS

[^6]

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Designed in response to demand for a tuner to complement the world．wide acclamed Linsley－Hood 75W Amplitier，this kit provides the perfect match．The Wireless World （Skingley and Thompson）published original circuit has been developed further for inclusion into this outstanding slimine unit and features a pre－aligned front end module． excellent a m relection and lemperature compensated varicap luning．which may be by a frequency meth and slidin Push－bution pre－selection Frequencies are indicated pre－set The PLL stereo decoder incorporates active fllers for each channel selecior power is supplied via a toroidal transtormer and integrated regutator for lone arm slability metal oxide resistors are used throughout gion ter
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LINSLEY－HOOD CASSETTE DECK


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Pubished in Wireless World（May．June．August 1976 by Mr Linsley－Hood．this design．although straightforward and relatively low cost nevertheless provides a very high standard of performance To permit circuit optimization separate record and replay amplifiers are used the latter using a discrete component front－end designed such that the noise level is below that microphone use The mechanism used is the Goldring－Lenco CRV．a unit distinguished in its robustness and ease of operation Speed control and using an additional pre－amplifier for implemented by electronic circuitry This unit which is powered by a unit distinguished in its robustness and ease of operation Speed control and automatic cassette ejection are both and the Linsley－Hood 75 Watt Amplifier Circuit changes as published in February．1978，follow－up article are included in the kit AT NO EXTRA COSTI A higher performance head （Matsushita WY 436 AZ head as recommended in the follow－up article）is offered as an optional extra but this will be automaticaliy supplied FREE OF CHARGE with all orders for complete

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| 103 | 1.0 | 4.57 |
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| 118 | 8.0 | 17.05 |
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| ---: |
| .78 |
| 96 |
| 114 |
| 1.32 |
| 1.50 |
| 1.64 |
| $2.0 B$ |
| $O A$ |


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| :---: | :---: | :---: | :---: | :---: |
|  | 12v | 24v |  |  |
| 111 | 0.5 | 0.25 | 2.20 | 45 |
| 213 | 1.0 | 0.5 | 2.64 | 78 |
| 71 | 2 |  | 3.51 | 78 |
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| 13 | 100 | 9-0-9 | 2.14 | 38 |
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| 207 | 500, 500 | 0-8-9.9.8-9 | 2.59 |  |
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50 MHz Pulse Generator PM5715. Specification on request $\mathbf{L 6}$. 600.00 $10 \mathrm{~Hz}-100 \mathrm{kHz}$ sine \& Square wave Sine a Square Wave Oscillator PM5125. $10 \mathrm{~Hz}_{2} 1 \mathrm{MHz} \quad £ 145.00$ PM5324 AM/FM Signal Generator. $100 \mathrm{KHz}-110 \mathrm{MHz}<5 \mu \mathrm{~V}-50 \mathrm{mV}$ o/p into
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3.30 Hz As new condition $\mathbf{E 6 9 5 0 0}$


PM2513A O.M.M. AC-DC volts and new condition $\quad £ 95.00$
PM3010 Miniature oscilloscope. DC$5 \mathrm{MHz} 30 \mathrm{mV}-1 \mathrm{~V} / \mathrm{div} 1 \mathrm{~S}$ S-O. $1 \mathrm{~S} / \mathrm{div}$ with
$\times 10 \mathrm{mag}$ Battery operation Supplied with $\times 10 \mathrm{mag}$ Battery operation Supplied with
$\times 10$ probe and battery charger As new

## RACAL

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interval. Freq range $10 \mathrm{~Hz}-100 \mathrm{MHz}$ H F Communications £285.00 RA117E. $1-30 \mathrm{MHz}$ full specification on request | Modulation Meter $210 A$ | $\mathbf{2} 5350.00$ |
| :--- | ---: |
| 300 MHz |  | M range $0-100 \%$. F.M range $0: 0$ $\pm 100 \mathrm{kHz} \quad \mathbf{E 2 4 5 . 0 0} \mathbf{- 1 2 8 5 . 0 0}$ A.M./F.M. Modalation Meter 409. Freq

## DUMONT

Porabie 'Scope 1100 P . oc 100 MH a Delayed ume base Full $8 \times 10 \mathrm{~cm}$ display

## MARCONI

## INSTRUMENTS

F.M./A.M. Signal Generator TF995B/2 Frequency Range $0, \mathrm{mV}$ to 200 mv m 675.00 U.H.F. Signal Generator TF1060. Frequency range $450-1250 \mathrm{MHz}$ ( 1 band). Output $015 \mu V$ to 445 mV Output impedmod

A.M. Signal Generator TF801D / Frequency Range
five bonds Output Attenuator $0.1, \mathrm{~V}$ to 1 V
Output Impedance $50 \Omega £ 400.00-£ 750.00$

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04.12 MHz


Two-Tone Signal Source TF2005R Frequency Range $20 \mathrm{~Hz}-20 \mathrm{kHz} \quad £ 415.00$ Blanking and Sync Mixer TF2908. Fo Reshapes and mixes blanking and sync $\mathbf{£ 9 0 . 0 0}$ L.F. Extension Unit TM6448, For use with $100 \mathrm{~Hz} 103 \mathrm{MHz} £ 200.00$ Wide Range R.C. Oscillator TF1370A. 10 Hz to 10 MHz sine wave 10 Hz to 100 kHz DC multiplier TM5033A. HV prote 30 kV . Impedance $3000 \mathrm{M} \Omega$ for use with TF104 1 series or TF2604 5 wate De.00 5 watt Dummy Load TM5582 for use with
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Output Impedance $50 \Omega$ Modulation In ternal $A M \quad 1 \mathrm{kHz}$ \& $5 \mathrm{kHz} 0-40 \%$ External
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$\mathbf{\& 9 0 . 0 0}$
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For further information, please contact Geoff Aldridge, ref GAB

## Computer Engineers

c. $£ 6,000$

PROJECT ENGINEERS

- with previous project experience associated with electronic equipment, preferably on computer terminal equipment or data com munications equipment

MANUFACTURING TEST ENGINEERS
-experienced in electronic equidment pro duction preferably concerning computer terminal equipment

## SERVICE ENGINEERS

- with several years experrence in the servicing and repair of minicomputer (PDPII useful) microprocessor, video display and communications systems

Forfurtherinformation, please contact Mike Geinat. ref: ME8

## Quality Inspection

## Rickmansworth

This is a progressive opportunity within a growing company for an inspector with experience of electronic and electro mechanical components and sub-assemblies.

Responsibifity will be for the quality contro of all inputs to manufacture. This will include the inspection of a range of items from PCBs and components through to power supplies. minicomputers and terminals

Our key requirements for this post are experience and difigence: age is relatively unimportant. The post carries a first class remuneration package, which includes free life and sickness insurance, a good pension scheme and an achievement-related bonus

For further information, please contact Mike Gernat. ref MD8

## Post - Design Services International

## Digital Projects

ro £5.360
North-West London
To $£ 5.600$
An unusual and exciting appointment for either a Digital Designer seeking customer contact and overseas travel, or for a Mini computer Service Engineer who wishes to retain mobility but is interested in problen solving and design.

The job entails updatıng and modifying minicomputer-based systems to suit customer requirements and investigating and over coming operating problems in close laison with the user This could involve, on average perhaps one trip per month overseas - within Europe or further afield
Out client is a progressive, medium-sized company whose products have earned an international reputation. As a member of a small friendly team in which hard work is rewarded, you may confidently expect an excellent salary/benefits package

For further information, please contact Mike Gernat, ref: MC8

An excellent opportunity to join a multi disciplinary tean and use your creative skills to design and commission sophisticated processor based Production Control and Information Systems In addition you will be encouraged to master new skills and evaluate possible applications using the latest tech. nology
This small but essential department serves a large company with several UK sites giving you project variety and a chance for occasional travel

With one vear's design experience, coupled with an HNC or Degree in an Electronic or related discipline there are exciting prospects for career advancement

The femuneration package is excellent, with regular salary reviews and attractive fringe benefits including removal expenses
For further information, please contact Geoff Aldridge, ref GC8

## Probe Advanced Engineering

Rewarding opportunities at the forefiont o technology for Test Technicians at all levels TEST SUPERVISOR c. $£ 5.500$

If you have 4 years' Analogue and Digital test experience, and proven supervisory ability, this post offers you the opportunitv to develop a useful new expertse while equip. ping yourself for future advancement promotion. on merit, is a real possibility.
You will be responsible for the smooth running of a team of 40 Technicians their training and work scheduling
TEST TECHNICIANS
To $£ 5,000$
With a minimum of 4 years Analogue and Digital test experience, you could be working on varled and advanced systems gaining the right experience 15 improve your future prospects

These appointments are largely new ones due to the success and expansion of the company It is based in North West London and offers an attractive benefits package including housing assistance.
For further information, please contact Geoff Aldradge, ref GD8

## An Open Invitation

Technomark invites you to discuss you career with our qualifted consultants who understand your needs and ambitions. We offer you FREE and constructive advice on advancement opportunities and can bring a new dimension to your career
This advertising feature outlines only a few of the many and varied opportunities available to our candidates: many more are neve, advertised
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# Land a goodjob 

# Your Radio Officer's qualifications can mean a lot here on shore 

If you're thinking of a shore-based job. here's where you'll find interesting work, job security, good money, and the opportunity to enjoy all the comforts of home where you appreciate them most - at home!

The Post Office Maritime Service has vacancies at Portishead Radio and some of its other coast stations for qualified Radio Officers to undertake a wide variety of duties. from Morse and teleprinter operating to traffic circulation and radio telephone operating.
To apply you must have a United Kingdom Maritime Radio Communication Operator's General Certificate or First Class Certificate of Proficiency in Radio-telegraphy or an equivalent certificate issued by a

Commonwealth Administration or the Irish Republic. And. ideally you should have some sea-going experience. The starting pay at 25 or over works out at around £4093; after three years' service this figure rises to around £5093. (I $\ddagger$ you are between 19 and 24 your pay on entry will vary between approximately $£ 3222$ and $£ 3732$ ). Overtime is additional. and there is a good pension scherne, sıck-pay benefits, at least 4 weeks' hol iday a year: and excellent prospects of promotion to senior management.

For further information, please telephone Andree Trionfi on 01-432 4869 or write to her at the following address: ETE Maritime Radio Services Division (L690), ET 17.1.2, Room 643 Union House, St. Martins-le-Grand, London EC1A 1 AR.

## Post Office Telecommunications

## ELECTRONICS ENGINEER

## THE COMPANY

We are a young company experiencing vigorous growth, full of good ideas and successful in putting these ideas into practical uses
We are now the dominant force in our original market area and have expanded into others.

## THE PRODUCTS:

Our products are state of the art, well conceived and built with care. To back this up we pride ourselves on the service our customers receive. Our products include Traffic Monitoring Equipment, Data Loggers, through to OEM Single Board Microcomputers.

## THE JOB:

This involves the design, development and debugging of microprocessor based products. The ability to work in an inventive and practical manner is essential. Knowledge of programming would also be an advantage
THE APPLICANT:
This person will be qualified and hold a relevant degree, H.N.C. or H.N.D., although the emphasis is on ability rather than qualifications. A good salary is offered and the chance to grow with the company

IT'S YOU? Then for an interview write or phone Roy Tuthill (Technical Director) -

[^7](8454)

## R \& D Engineers at senior and intermediate levels

required to work on digital and cable television systems for the domestic and surveillance market

Engineers should hold a degree, HNC or equivalent qualification and have some knowledge of either HF video or digital circuit design

Salaries will be commensurate with qualifications, age and experience

Fringe benefits include a contributory pension, life assurance scheme, subsidised canteen, etc

If you are seeking an enjoyable position in R \& D. write giving full details of your career to date, or telephone Dr. G. O. Towler, B.Sc. Ph.D. (Manager), Research and Development Establish ment, British Relay Ltd. Cleeve Road, Leatherhead, Surrey. Tel 76056

8469

# SULTANATE OF OMAN 

## COLOUR TELEVISION SERVICE

We are recruiting on initially one year contracts and have vacancies for the following and other positions.

## PROGRAMME STAFF

- Production Director
- Programme Director
- Film Editor
- Transmission Controller
- Administration


## OPERATIONS STAFF

- Sound Supervisor
- Sound Dubbing \& Mixing
- Film Processing
- Film Cameramen
- T.V. Lighting/Cameramen


## ENGINEERS

- Studio - V.T.R. - Telecine
- Transmitters
- Microwave
- O/B Van
- Technical Administration


## ADMINISTRATION

- Training Officer


## PLUS

- Aerial Rigger/Mechanic
- Electricians
- Diesel Mechanic
- Air Condition Technician

Let us discuss with you your abilities for these interesting and important positions. Would previous applicants re-confirm their interest.

Write or phone: Tony Owers, 01-5738333
PERSONNEL \& ELECTRONICS LTD.
TRIUMPH HOUSE
1096 UXBRIDGE ROAD
HAYES, MIDDLESEX UB4 80H

## Listening-in at 75 fathoms needs your kind of engineering experience




#### Abstract

Modern anti-submarine warfare relies heavily on detection devices suich as the sono-buoys cations Lid that atter drop from an arrcraft flying at up to 10.000 ft cieploy ons llying at up to 10.000 ft , Geploy themselves automaticaled depth and rising a rado aenal so as to listen for amplify them and transmat the intormation amplify them and transmst the information back to subnuarine hunting arcraft. Group. also manufactures and denal Dowty Group also manufactures and develops communication control systems and interconn units for civit and military aircratt, arrborne emergency radios, and beacons for homung and rescue applications. Our latest project is in British Rall with a communication system between signal a communication system between signal boxes and trains. Many of these systems need a high degree of ingenuity and the kind of engineerng experience that maybe you can offer. In particular we


Electronic Development Engineers
We are looking for mien or women to join a small team of Engineers and Technicians working on the design of analogue systems and circuits. Visits to trials may be necessary
and opportunities might arise for visits to clients and suppliers.
clients and suppliers.
Applicants should be qualified to HND or Applicants should be qualified to HND or
preferably degree level with several years preferably degree
design expenence

Senior Development
Technicians
We requre men or women to fon project teams working on the design and development of analogue systems and circuits for prototype equipment will be responsible for building. testing and evaluating experimenta equpment and for assisting with the development of analogue circuitry.
Applicants, aged between 25 and 45 , should hold City \& Guilds Electronics, Radio \& TV. or Tetecommunications Certificates up to 5 years deralo 5 years development experience, preferably involving government contracts.
Test Technicians
Our production department require additional male or female Testers with experience of radio or analogue circuits and test equipment Candidates should have several years practical experience in this area with or without qualifications.

## We are offering attractive salaries, negotiable

 according to qualifications and experience plus a wide range of attractive large company benefits There are good promotion prospects and generous relocation package is avalable where necessary covering all legal and estate agency fees. Buiding Society survey fees. viewing expenses, and a disturbance allowanceFor further information and an application form phone or write to
Mr Gavin Rendall. Personnel Manager. Ultra Electronic Communications
Limited, 419 Bridport Road.
Greenford,
Greenford,
MiddlesexUB68AU.
Tel:01-5780081.

## Listening-in at 75 fathoms needs your kind of engineering experience

## Electronics Technician

required to work with small team of Engineers on custom built equipment

Duties include assembly, wiring and test of complete equipment as well as testing small batches of PCB's

Previous experience of wiring essential, preferably to mulitary standards, previous production testing experience would be an advantage

Suitable candidate must be able to work unsupervised
Telemotive looks only for above average personnel, and this is reflected in conditions of employment offered

Please apply in writing. giving details of previous experience, to -
Telemotive U.K. Limited

## Ministry of Defence

 Radio TechniciansThe Ministry of Defence has vacancies at RAF Henlow for Radio Technicians to work on the maintenance, fault diagnosis, repair, recalibration and modification of radio communication, radar, and electrical and electronic test equipment. Applicants must be experienced technicians in the radio/electronics field.
Starting pay according to age, up to $£ 3,700$ a year (at age 25 ) rising to $£ 4,252$ a year.
5 day week - 4 weeks paid holiday in addition to Public Holidays - prospects of promotion - pension scheme.

Applicants must be United Kingdom residents.
Write for further details to

## Officer Commanding Radio Engineering Unit Royal Air Force <br> Henlow <br> Bedfordshire SG 16 6DW

# Aword to the wise Aboutour advanced support engineering atBasildon 

In the field of electro-optics, we're leading lights in more ways than one. Our work covers the development and manufacture of a wide range of advanced equipment for ground based, airborne, shipborne and underwater surveillance, guidance and tracking systems. We're work ing on acoustics and optical projects and the technology employs sensors in the visual to IR band and data transmission links toget her with all the associated signal processing.

It's a see all, hear all, and tell all enviromment and in our Electro-Optical Systems Group here in Basildon we cansu provide engineers with exceptional scope for creative involvement in all manner of high interest projects.

The continuing growth of our work has created unusually attractive career development opportunities for both men and women and at the moment we have a particular requirement for:-

## Field Trials Engineers

To provide support to the development programmes during engineering and customer evaluation trials; commissioning. calihration and maintenance of prototype systems; acquisition of trials data and assisting with post trials analysis.

## Trials Planning and <br> AnalysisEngineers

For detailed planning of proving trials including definition of trials requirement; co-ordinating trials and analysing results, utilising such data acquisition techniques as audio and video recording and data analysis using computing facilities.

## Component Engineers

To liaise closely with project development teams to ensure correct choice of components; prepare purchasing documents to ensure quality and reliability requirements and liaise with suppliers to secure acceptance of specification.

## ProductSupport Engineers

Entails close liaison with company engineering production and test departments and with customers' technical staff in support of established equipments during manufacture and customer evaluation.

## Technical Writers

'To prepare documentation in support of commercial and military projects including design and test specifications, handbooks and the preparation and editing of proposals and technical reports.

These appointments call for at least ()NC and preferably an HNC or equivalent qualification with relevant experience in servicing or design of major electronic systems.

If you have the sort of qualifications and experience we're looking for vou'd be wise to get in touch with us without delay: Write with details of your career to date to J. S. Nealon at Marconi Avionics Limited, Christopher Martin Road, Basildon, Essex 'Telephone: Basildon (0268) 22822 ext. 86 . Where necessary we can assist you with relocation to this attractive part of the country.


MARCONI

A GEC-Marconi Electronics Company

## TECHNICAL INSTRUCTOR

## c. 55,500

The Company wishes to appoint a Technical Instructor with HNC or equivalent experience in electronics. Preference will be given to candidates with proven lecturing experience covering analogue and digital techniques, including microprocessors.
Successful applicants, either male or female, will be responsible for the preparation and evaluation of course material using modern training aids which include OHP, slides, CCTV and video tape. This job involves training in-house staff, including sales and service engineers, customers' engineers and operators, and assisting where necessary in the preparation of technical and operators manuals. Occasional overseas travel may be necessary for 'on-site' training.
Based in London, this position offers the challenge, interest, satisfaction and rewards to attract the best of today's technical instructors.
Please telephone or write, quoting reference G/2002, to:-
Mrs L Geers, Personnel Officer, Crosfield Electronics Limited, 766 Holloway Road, London N19 3JG. Telephone 01-272 7766.

BRENT AND HARROW AREA
HEALTH AUTHORITY (Harrow District) NORTHWICK PARK HOSPITAL AND CLINICAL RESEARCH CENTRE
Watford Road, Harrow Middlesex HA1 3UJ Tel: 01-8645311

## ELECTRONICS TECHNICIAN

(MPT GRADE III)
A technician is required to service and calibrate a wide range of equipment used for medical, surgical and engineering purpose The successtul applicant will work closely with medical and other protessional staff ONC. HNC. HND or Science Degree (or three years previous experience as a Technician Grade IV) is a necessity. Salary £3744-£4788 plus £354 London Weighting Allowance.
For further detals and application form please contact Personnel Department. Ext. 2001
(8429)

## TELECINE/VTR ENGINEERS

you have VTR and Telecine experienc and want to move into Broadcasting in the West Country then Westward Television would like to hear from you
acancies exist within two teams of six engineers to undertake operational and maintenance duties

Salary according to age and ex a maximum of $£ 5.000$ basic

Apply, in writing giving full detais to the Personnel Manager. Wesiward TV Lid Derrys Cross. Plymouth PL1 2SP, or telephone 075269311 ext. 215 for further details and application form
$\qquad$

## RADIO INSTALLERS

# Radio and Radar Engineer 

## BRITISH AEROSPACE Dunsfold Aerodrome

This is a worthwhile position for a man or a woman wishing to strengthen a small team responsible for servicing and maintaining air traffic radio/radar installations.

Applicants should have a minimum of 5 years current air traffic radio / radar equipment maintenance experience.

We will pay you an attractive salary and fachlities include a subsidised Canteen and an active Sports and Social Club
Please write or telephone quoting WW/92 to
The Personnel Officer
BRITISH AEROSPACE
Aircraft Group
Kingston-Brough Division
Dunsfold Aerodrome
Nr. Godalming
Surrey
Telephone: Cranleigh 2121

## H.M.G.C.C.

## ELECTRONIC ENGINEERS

Designers and Development Engineers are required for work in the HF and UHF fields and in general analogue and digital circuitry.

The Establishment is sited in rural surroundings in North Bucks. within easy reach of Northampton, Bedford and Milton Keynes. A frequent rail service and the M1 motorway provide easy access to London. House prices in the area are still at provincial levels.

Minimum academic qualification is HNC and, for Higher Scientific Officer, five years' post-qualification experience (for graduates with First or Second Class Honours this is reduced to two years' post-graduate experience).

Salaries are
Scientific Officer £2839-£4415
Higher Scientific Officer £4101-£5448

# DRAWING OFFICE STAFF 

Drawing Office Staff are required in a supporting role to the above engineers
Salaries are in the ranges $£ 3148-£ 4326$
£4326-£4869
Salaries for Drawing Office Assistants are £2119-£3189, depending upon age, qualifications and experience

For application form please apply to
The Administrative Officer (Dept. WW) HM Government Communications Centre

Hanslope Park
Hanslope
Milton Keynes
Bucks.
MK197BH

## 干

TYNE TEES TELEVISION LIMITED

A Member of the Trident Television Group
have a vacancy for an

## ENGINEER

In the Central Technical Facilities Department for operational duties in video tape recording, film transmission and network circuit tests. H.N.C./ H.N.D. in an appropriate subject is a desirable qualification together with an interest in current television broadcasting techniques.
Starting salary for an experienced applicant, in accordance with A.C.T.T. scale, will not be less than $£ 3580$ per annum. Shift working required. Company benefits include pension scheme, 4 weeks' holiday and staff restaurant.

Please write, in confidence, to
Mrs. J. M. Jacobson, Personnel Manager TYNE TEES TELEVISION LIMITED The Television Centre, City Road Newcastle-upon-Tyne NE1 2AL

## Service Engineer

Dixons Technical forms part of the Dixons group of Companies. We wholesale export and provide sophisticated close circuit and video equipment to major T.V. companies, commerce and industry
We are currently looking for a Service Engineer with a minimum of two years' experience in the video field to work in our new headquarters in Croydon. Service experience on VTR is essential and training will be given in servicing cameras and monitors.
We can offer you a competitive salary, excellent fringe benefits which include 4 weeks' holiday and massive discounts on the very best photographic and audio equipment.
Contact
Ron Irving, Personnel Manager
Dixons Photographic UK Ltd., Prinz House
54-58 High Street, Edgware, Middx.
Tel. 01-952 2345, ext. 341
(8452)

Dixans

## AUDIO + VIDEO LTD. SENIOR VIDEO ENGINEERS AND HIGH GRADE TELEVISION ENGINEERS

Because Audio + Video are the largest video duplicators in Europe, we naturally have a lot of high-class equipment tp produce our top quality video tapes. We have in house, the Marconi D.IC.E., the Rank Cintel Flying Spot Telecine, the RCA TK28 Telecine, TR60, TR70c and Ampex 20002 Quad machines, Sony D100 duplicator, 2850. 2600, 2030, 2630. Betamax. Philips VCR 1500 and 1700 , VHS, Keyline editor, etc.
We now require Senior Video Engineers with experience of maintaining and servicing any or all of the above equipment and high grade Television Engineers who can be trained to help maintain most of it. We will pay salaries in excess of $£ 5,500$ for the right people who enjoy working in television.
Please contact Cliff Carroll on 01-580 7161.


We require staff, male or female, to prepare and maintain the latest in communications equipment used by the Police and Fire Brigades in England and Wales.

You will need to be qualified at least to City and Guids Intermediate Telecommunications standard and be able to demonstrate practical skills in locating and dagnosing faukts in a wide range of equipment from computer based data transmission to FM and AM radio systems You would live near to and work from our service centres located throughout England and Wales or our Headquarters in the London area Specialised courses of training are run to assist staff to keep up to date with developments and new equipment and there are opportunities for day release to gain higher qualifications Applicatıons from registered disabled persons will be considered
Promotion prospects are good and the work represents a secure future with generous leave allowances and a non-contributory pension scheme
Possession of a driving licence is essential since some travelling will normally be involved
The salary is $£ 2627$ (at 17). $£ 3176$ (at 21) and $£ 3700$ (at 25). rising to £4252.
If you are interested in working with us. then write for further details and an application form to:

MrCBConstable<br>Directorate of Telecommunications<br>Horseferry House<br>Dean Ryle Street<br>LONDON SW1P 2AW<br>Telephone:01-2116420

## The City University

## Studio Technician

required to work in a team developing new facilities in the University, There will be two main areas of responsibility
In the Electronic Music Studio with responsibility for developing and maintaining equipment for students work as well as planning a computer link and a digital research programme
In the Language Laboratory carrying out regular servicing and immediate fault correction on language equipment and participating in forward planning
Applicants should have experience of both design and practical work some knowledge and interest in electronic music is useful.
Salary will be on the scale $£ 3441-£ 3890$ or $£ 3674-£ 4209$ per annum inclusive. Application forms are obtainable from: Mrs K. Fowler, Personnel Office, The City University, St. John Street, London EC IV 4PB (01-253 4399, ext. 334).

# How to sind a better job without leaving your armchair. 

Don't for a single moment question your motives. Striving for a higher income is a philosophy practised by people in all walks of life.

Perhaps though, you cannot get a fair picture of the opportunities avalable from the standard, limited sources of job information.

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We match your ambitions and skills with our clients' needs. When the two are compatrble, the clients hear about you right away and you should get an invitation to talk.

Take this chance to find out how many companies are interested in having you on their side. They use us because our method is simple, quick, efficient. Lansdowne Appointments Register, Design House, The Mall, London W5 5LS. Tel: 01-579 2282 (24 hour answering service).


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(24 hour answering service) 8485

## FOREIGN AND COMMONWEALTH OFFICE COMMUNICATIONS DIVISION

## has vacancies for

## RADIO TECHNICIANS

to carry out shift duties concerned with MW and HF broadcasting systems involving frequency changing, fault finding and routine maintenance, keeping logs, and recordings.

Applicants should have minimum qualifications of City and Guilds Intermediate Certificate in Telecommunications or its equivalent.

The successful candidates will serve initially at Crowborough, but may be required to serve elsewhere in the UK or overseas should the necessity arise

Salary is according to age, e.g. $£ 3,176$ per annum at age $21, £ 3,435$ at age $23, £ 3,700$ at age 25 or over on entry rising by annual increments to a maximum of £4,252 per annum.

The appointments attracts 4 weeks ' paid holiday and prospects of pensionable employment.

## Recruitment Section

Foreign and Commonwealth Office
Hanslope Park, Hanslope, Milton Keynes MK19
7BH
(8320)

## Sound out your new career in $\mathrm{Hi}-\mathrm{Fi}$

Hardman's are now the No. 1 name in hi-fi retailing in Birmingham. Chester. Liverpool. Manchester and Preston with superb spacious showrooms offering the best hi-fi selection in town. But we're not just a single fast expanding company with five stores and more to follow; we're part of a large and successful Group with interests throughout the leisure industry. To you that means the big benefits that only a large Group can offer you: security plus the almost unlimited scope that an enthusiast like you will relish

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## Senior Audio and Hi-Fi Sales Staff

You must have a proven sales ability and/or sound audio or hi-fiexperience. We'll give you specific sales training and keep you up to date with regular seminars. Good salary plus commission. PLUS excellent opportunities for promotion to management within the group Hi-Fi Engineers
You're a qualified hi-fi/audio/video engineer, or you're mid way through an apprenticeship scheme with no immediate
opportunities. You'd like to service, test and repair hi-fi
equipment in our well-equipped premises. We'll tell you all about the equipment and keep you updated on new developments. Excellent salary. Please write for an application form to:
The Managing Director, Hardman Radio Limited, 26 Exchange Street East, Liverpool, L2 3PH.
(8401)

Talk to the helpful Hi-Fi people
HARDMANS

## Development Engineers

## for Pye TVT.

the Broadcast Company of Philips.
We are fast expanding into the areas of digital signal processing, microprocessors and computer based systems in studio engineering.

We are therefore looking for Development Engineers with at least 4 years experience in the design of high-speed digital signal and/or data processing equipment.

They will be involved in and carry responsibility for all aspects of the design of digital equipment for broadcast TV applications.

Applicants should possess a degree or equivalent. Software experience is an advantage, together with a background in broadcast TV equipment.

The positions offer competitive salaries, plus relocation expenses and the normal benefits offered by a progressive company at the forefront of broadcast technology.

For further details, contact Alison Millar, Personnel Department, Pye TVT Limited. Coldhams Lane, Cambridge CB1 3JU.
Telephone Cambridge (0223) 45115

## Senior Design Development Engineer

Circa $£ 9,000$ p.a. + car + benefits
HIGH FREQUEMCY - HIGH POWER generation for industrial processing

A unique opportunity exists for a first-class engineer to contribute originality and professionalism to a number of development projects newly created within a vigorous, progressive company situated in the South of England.

The position calls for proven ability in innovative development and experience in R.F. Generation, High Voltage techniques and a working knowledge of light current electronics including digital and analogue circuitry. Emphasis is placed upon the candidates willingness to adopt more than one engineering discipline.

This attractive post carries benefits consistent with the responsibility and status of the position Advancement within the company will relate to the contribution of the candidate - progress to a Board position is expected. Additional benefits include BUPA, first-class superannuation and, where necessary, relocation expenses.

In the first instance telephone your nearest branch or write, enclosing c.v. to

ATA SELECTION \& MANAGEMENT SERVICES
23 Cumberland Place, Southampton
Southampton (0703) 37555
London 01-6370781
Crawley (0293) 514071
Bristol 0272211035
Birmingham 021-643 1994 Manchester 061-832 5856 Edinburgh 031-226 5381


## BRENTFORD ELECTRIC LIMITED

A thriving Company of over 400 people with a number of "firsts" to its credit in the field of Industrial Power Converters seeks:

## ELECTRONICS DESIGN ENGINEER <br> for General Circuit Design

[^8]Please write with details of your career or for further information to

## VIDEO ENGINEER

## TECHNICOLOR VIDTRO.

 NICS LTD. have a vacancy for a Broadcast video engineer. Cinema film technology experience an advantage. Salary in a range of $£ 3,900$ to $£ 5,300$ commensurate with experience plus pending increase.Telephone 01-759 5432 and ask for Mr. Edgerton or Mr. Blight.
Technicolor Vidtronics Ltd. Bath Road, Harmondsworth, Middx.
(8474)

## ASSISTANT MANAGER

Lynx offers an opportunity for an ambitious. enthusiastic young hobbyist to help build Lynx into a major maıl order retail organisatıon. This will involve expanding the range of products such as IC's, discreet semiconductors and passives Knowledge of radio, TV. and other electronic projects would be most useful.
Applications in writing to W. J. Bulman, Lynx Electronics Ltd., 92 Broad Street, Chesham, Bucks.
(8421)

NEWCASTLE AREA HEALTH AUTHORITY (TEACHING) ELECTRONICS \& MEDICAL ENGINEERING SECTION NEWCASTLE GENERAL HOSPITAL

## CHIEF ELECTRONICS TECHNICIAN (GRADE 2)

Applications are invited for the above position. The Chief Electronics Technician will assist the Senior Area Electronics Engineer in the maintenance of electronic and medical engineering equipment
The position offers a unique opportunity to lead a specialist team of Technicians covering all applications of electronics in medicine. including brain scanning equipment and communications
Salary Scale $£ 4.470$ rising to $£ 5.610$ by 8 annual increments
Candidates must have a broad experience of electronics, experience of medical electronics an advantage. Minimum academic qualifications - H.N.C. Electronic Engineering or equivalent
Job description and application forms available from Area Engineer's Office, Newcastle Area Health Authority (T), Area Headquarters. Scottish Life House. $2 \cdot 10$ Archbold Terrace. Newcastle upon Tyne NE2 1 EF. Closing date for completed application forms. Friday. 25th August. 1978.
(8431)

## ELECTRONICS ENGINEERS

ITA's expansion programme has created more en gineering vacancies. Secure future for Engineers with proven electronic ability. Varied and interesting work providing an attractive salary

Contact the Chief Engineer

## INDUSTRIAL TAPE APPLICATIONS 1-7 HAREWOOD AVENUE, MARYLEBONE, LONDON, NW1 <br> TELEPHONE: 01-724 2497/8

## Electronic Design Engineer

This new appointment is to join a small but growing engineering team involved in the design and development of electronic wheel balancers and electronic wheel alignment equipment. It involves completing product design from an initial outline specification prototype production, and assistance to manufacturing during the initial production stages

The Electronic Design Engineer will be responsible to the En gineering Manager for electronic aspects of the company's designs, and will work with development engineers, design draughtsmen and technicians. Some travel within the United Kingdom and occasionally abroad is required

The successful candidate, male or female, will be educated to degree or HNC level in Electronic or Electrical Engineering, and have at least three years experience with analogue and digital circuit techniques utilising transistors and integrated circuits

We offer an attractive starting salary, together with the employment benefits one would expect from a major industrial group

Please write for an application form, or telephone. Mrs. S. R Ballantyne, V. L. Churchill Limited, PO Box 3, London Road Daventry, Northants, NN 11 4NF. Telephone Daventry (032-72) 4461

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